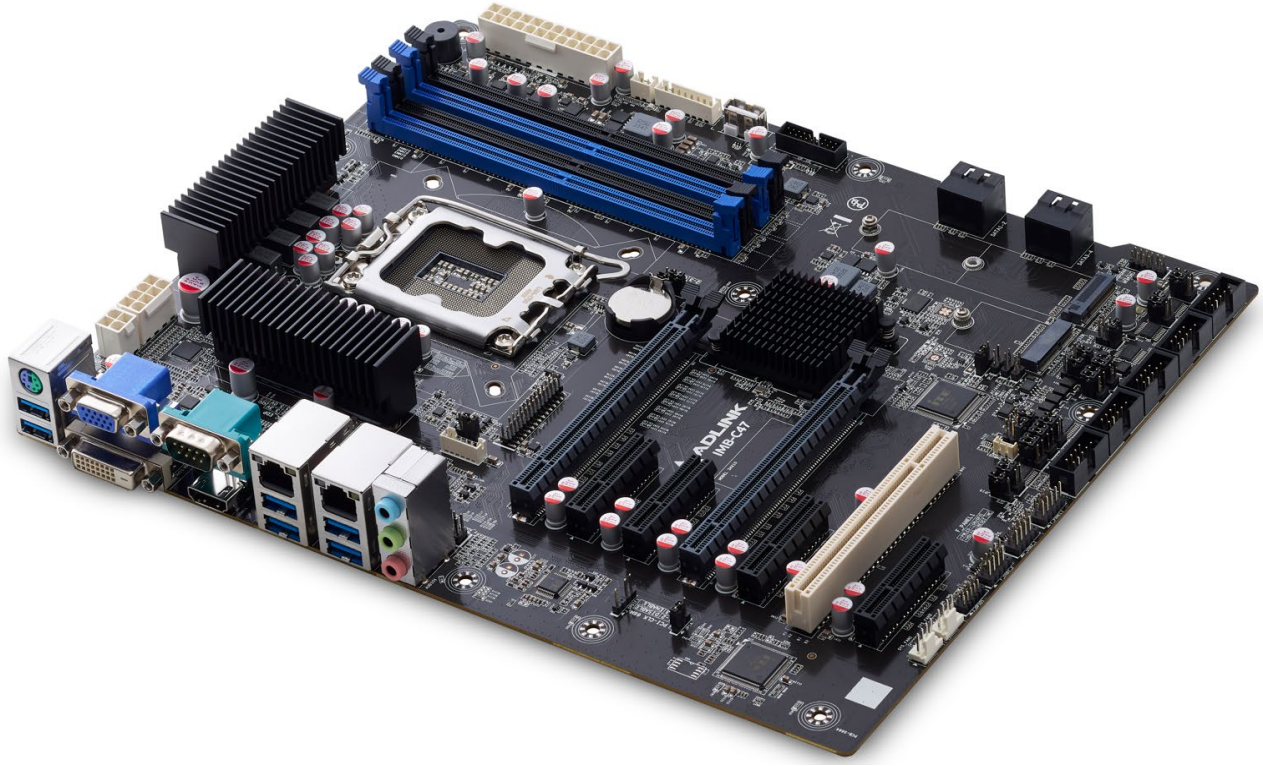
IMB-C47

User's Manual

ATX Motherboard with 12th/13th/14th Gen Intel® Core™ i9/i7/i5/i3 Processors and Intel® Q670 Chipset



Manual Rev.: 0.3 Preliminary
Revision Date: October 11, 2024
Part Number: 50M-13019-1000

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Leading **EDGE COMPUTING**

Preface

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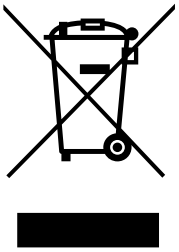
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Revision History

Revision	Description	Date	By
0.1	Preliminary release	2024-09-23	AL
0.2	Specifications and connector pinouts updated	2024-10-04	AL
0.3	Connector pinouts updated	2024-10-11	AL

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1 Introduction

ADLINK IMB-C47 ATX is an industrial motherboard supporting 12th/13th/14th Generation Intel® Core™ i9/i7/i5/i3 desktop processors, an Intel® Q670 Chipset, and 7 PCIe expansion slots to provide a cost-competitive embedded computing solution. It includes high-speed data transfer interfaces such as PCIe 4.0, USB 3.0, and SATA 6 Gb/s (SATA III), and dual-channel DDR4 3200 MHz RAM for industrial automation applications. With industrial-grade I/O port design, the IMB-C47 offers a significant competitive advantage for embedded computing applications in terms of device compatibility, durable connectivity, and extreme environment readiness.

1.1 Packing List

- IMB-C47 ATX motherboard
- Rear I/O shield

1.2 Optional Accessories

- CPU cooler for 65W CPU (Part Number: 32-20976-0000-A0)
- CPU cooler for 125W CPU (Part Number: 32-21041-0000-A0)
- 2-port USB2.0 cable with bracket (Part Number: 30-25010-3010)
- 2-port USB3.0 cable with bracket (Part Number: 30-25046-0100)
- 4-port USB 2.0 cable with bracket (Part Number 30-25009-3000)
- SATA cable with bracket (Part Number: 30-10057-7000)
- 2-port COM cable with bracket (Part Number: 30-25003-3000)

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2 Specifications

2.1 Core System

- **CPU:**

Family	Number	CPU Core	Frequency (GHz)	Cache(MB)	Power
Raptor Lake-S Refresh	i9-14900T	24	1.1GHz	36MB	35
Raptor Lake-S Refresh	i9-14900K	24	3.2GHz	36MB	125
Raptor Lake-S Refresh	i9-14900	24	2.0GHz	36MB	65
Raptor Lake-S Refresh	i7-14700T	20	1.3GHz	33MB	35
Raptor Lake-S Refresh	i7-14700K	20	3.4GHz	33MB	125
Raptor Lake-S Refresh	i7-14700	20	2.1GHz	33MB	65
Raptor Lake-S Refresh	i5-14600K	14	3.5GHz	24MB	125
Raptor Lake-S Refresh	i5-14500T	14	1.7GHz	24MB	35
Raptor Lake-S Refresh	i5-14500	14	2.6GHz	24MB	65
Raptor Lake-S Refresh	i3-14100T	4	2.7GHz	12MB	35
Raptor Lake-S Refresh	Ri3-14100	4	3.5GHz	12MB	60
Raptor Lake-S	i9-13900K	24	3.0GHz	36MB	125
Raptor Lake-S	i9-13900E	24	1.8GHz	36MB	65
Raptor Lake-S	i7-13700TE	16	1.1GHz	30MB	35
Raptor Lake-S	i7-13700T	16	1.4GHz	30MB	35
Raptor Lake-S	i7-13700K	16	2.5GHz	30MB	125
Raptor Lake-S	i5-13600K	14	2.6GHz	24MB	125
Raptor Lake-S	i5-13500T	14	1.6GHz	24MB	35
Raptor Lake-S	i3-13100T	4	2.5GHz	12MB	35
Raptor Lake-S	i3-13100E	4	3.3GHz	12MB	60
Alder Lake-S	i9-12900K	16	2.4GHz	30MB	125
Alder Lake-S	i9-12900E	16	2.3GHz	30MB	65
Alder Lake-S	i7-12700TE	12	1.4GHz	25MB	35
Alder Lake-S	i7-12700K	12	2.7GHz	25MB	125
Alder Lake-S	i5-12600K	10	2.8GHz	20MB	125
Alder Lake-S	i5-12500E	6	2.9GHz	18MB	65
Alder Lake-S	i3-12100TE	4	2.1GHz	12MB	35
Alder Lake-S	G7400TE	2	3.0GHz	6MB	35

- **Chipset:** Intel® Q670 Express Chipset
- **Memory:** 4x 288-pin dual-channel DDR4 3200 MHz, up to 128 GB
- **BIOS:** AMI® UEFI BIOS, 256 Mb SPI Flash Memory

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- **Hardware Monitor:** CPU voltage
  - +3.3V voltage
  - +5V voltage
  - +12V voltage
  - CPU temperature
  - System temperature
  - CPU fan speed
  - System fan speed

2.2 I/O Interface

- **Expansion slots:**
  - 2x PCIe x16 (supports PCIe Gen4 x8 signals)
  - 4x PCIe x4 (supports 3x PCIe Gen4 x4 and 1x PCIe Gen3 x4 signals)
  - 1x PCI
- **SATA:** 4x SATA 6.0 Gb/s connectors, Intel® software RAID 0/1/5/10 support
- **USB:**
  - 6x USB 3.0 connector (rear)
  - 2x USB 3.0 (via pin headers)
  - 4x USB 2.0 (via pin headers)
  - 1x USB 2.0 (vertical type A, internal)
- **COM:**
  - 4x RS-232 (COM1 by DB9/M, others via headers)
  - 1x RS-232/422/485 (via headers)
  - 1x RS-232/485 (via headers)
- **PS/2 Combo Port:** 1x PS/2 keyboard/mouse pin header
- **DIO:** 8-bit GPIO
- **TPM:** Support Intel PTT
- **M.2 connectors**:
  - 1x M.2 E-key 2230 (PCIe Gen3 x1, USB2.0, CNVi) for WIFI/Bluetooth Module
  - 1x M.2 M-key 2242/2280 (PCIe Gen4 x4 NVMe/SATA Auto Detect) for SSD

2.3 Video

Supports four independent displays:

- 1x DVI-D (DVI-I), max resolution up to 1920x1200@60Hz
- 1x HDMI 1.4b, max resolution up to 4096x2160@30Hz
- 1x VGA, max resolution up to 1920x1200@60Hz
- 1x eDP (via header), max resolution up to 4096x2304@60Hz

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2.4 Audio

- **Audio Codec:** Realtek® ALC897
- **Interfaces**: 1x Mic-in, 1x Line-in, and 1x Line-out connectors (rear)

2.5 LAN

- **LAN1:** Intel® I219-V via RJ45 connector, with 10/100/1000 Mbps
- **LAN2:** Intel® I226-V via RJ45 connector, with 10/100/1000/2500 Mbps

2.6 Temperatures

- **Operating Temperature:** 0°C to 60°C
- **Storage Temperature:** -20°C to 75°C

2.7 Humidity

- 60° C at 95% RH, non-condensing

2.8 Certificate (EMC)

- CE/FCC Class A

2.9 Form Factor

- **ATX**: 305 mm x 244 mm (W x L)

2.10 Operating Systems

- Microsoft® Windows® 10, 64-bit
- Microsoft® Windows® 11, 64-bit

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2.11 Functional Block Diagram

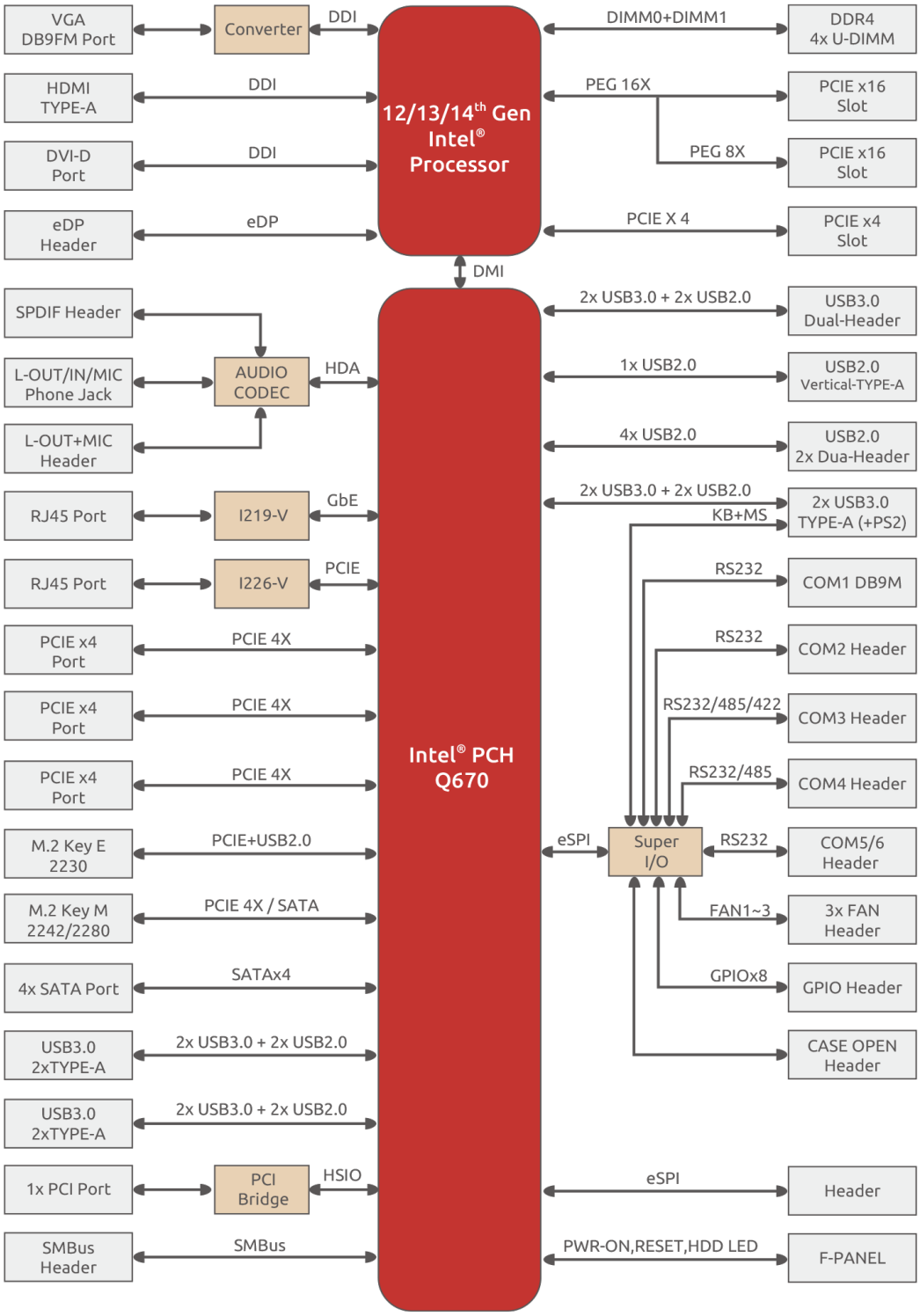


Figure 1: Functional Block Diagram

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3 Mechanical Layout

3.1 IO Panel Connector Locations

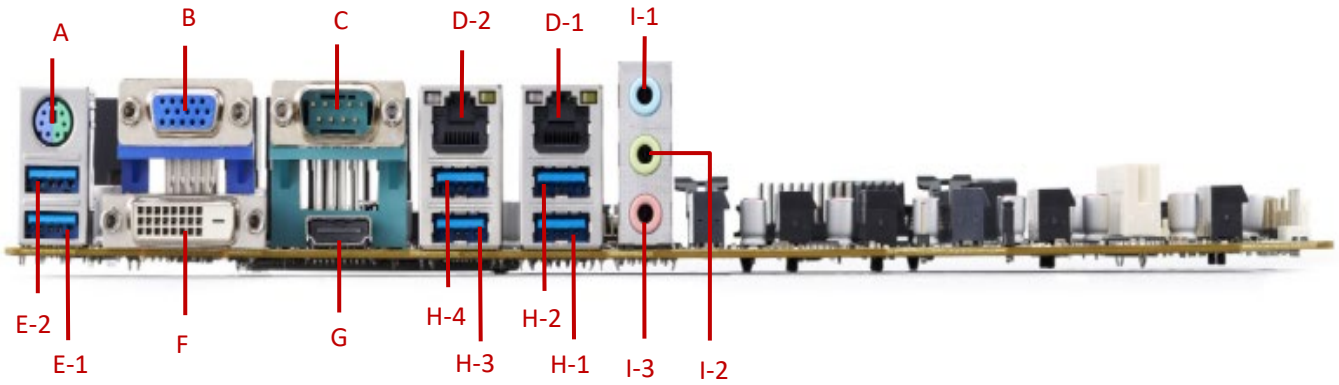


Figure 2: IO Panel Connector Locations

Table 1: IO Panel Connector Definitions

Item	Description	Description
A	PS/2_USB1 (PS/2)	PS/2 Connector (Keyboard & Mouse)
B	DVI_VGA1 (VGA)	VGA DB15/F Connector
C	COM1	COM1 DB9/M Connector
D-1	RJ45_USB1 (LAN1)	GBE LAN RJ45 Connector1
D-2	RJ45_USB2 (LAN2)	2.5GBE LAN RJ45 Connector2
E-1	PS/2_USB1 (USB1)	USB3.0 TYPE-A Connector
E-2	PS/2_USB1 (USB2)	USB3.0 TYPE-A Connector
F	DVI_VGA1 (DVI-D)	DVI-I 24+4P/F Connector (Support DVI-D)
G	HDMI1	HDMI TYPE-A Connector
H-1	RJ45_USB1 (USB1)	USB3.0 TYPE-A Connector1
H-2	RJ45_USB1 (USB2)	USB3.0 TYPE-A Connector2
H-3	RJ45_USB2 (USB3)	USB3.0 TYPE-A Connector3
H-4	RJ45_USB2 (USB4)	USB3.0 TYPE-A Connector4
I-1	AUDIO1 (BLUE)	Line-In 3.5mm Jack
I-2	AUDIO1 (GREEN)	Line-Out 3.5mm Jack
I-3	AUDIO1 (PINK)	MIC-In 3.5mm Jack

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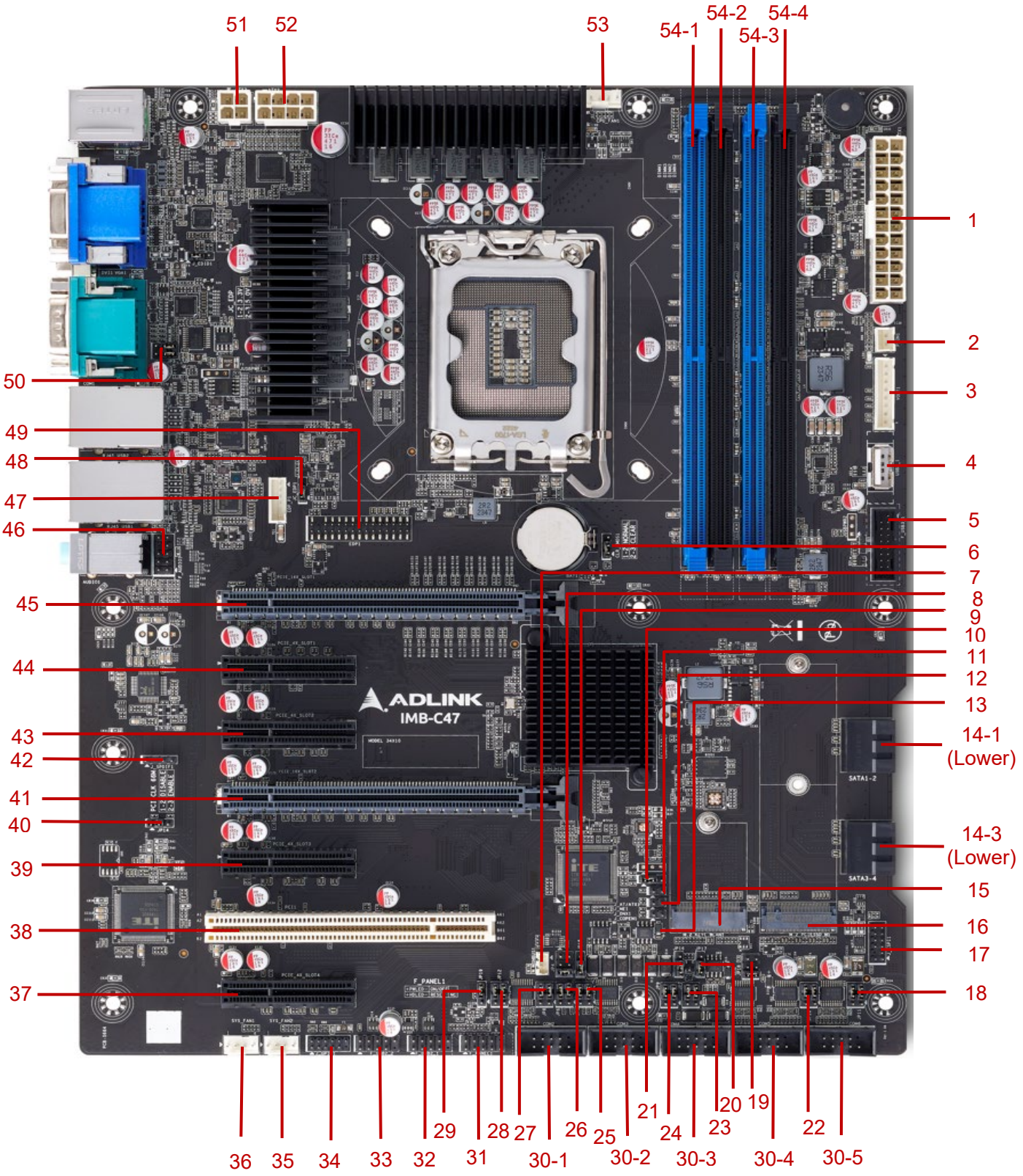


Figure 3: Onboard Connector – Header and Jumper Locations

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Table 2: Onboard Connector – Header and Jumper Definitions

Item	Description	Description
1	ATX1	ATX 24P CPU Power Input Connector
2	PSON1	Power On Wafer
3	VOLT1	Power Monitor Wafer
4	USB2_1	USB2.0 Internal Vertical TYPE-A Connector
5	F_USB3_1	Front USB3.0 Box Header
6	CLR_CMOS1	CMOS Clear Select Jumper
7	SMBUS1	SMBUS Wafer
8	JP4	COM3 RS232/422/485 Select Jumper3
9	JP15	COM3 RS422 RX Signal 120Ω Resistive termination Select Jumper
10	J_AT/ATX1	AT or ATX Select Jumper
11	J_ME1	ME Flash Jumper
12	J_DNX1	DNX Force Reload Disable/Enable Select Jumper
13	J_COPEN1	Case Open Header
14-1	SATA1	SATA3.0 7P Upright Connector
14-2	SATA2	SATA3.0 7P Upright Connector
14-3	SATA3	SATA3.0 7P Upright Connector
14-4	SATA4	SATA3.0 7P Upright Connector
15	M.2_KEYE_WLAN1	M.2 Key-E Slot (PCIE+USB2.0+ CNVi, support WIFI+BT/ CNVi Module, 2230)
16	M.2_PCIESSD_M1	M.2 Key-M Slot (PCIe x4 GEN4 NVMe/SATA SSD Auto Detect, 2242/2280)
17	J_ESPI1	ESPI Header (Debug Only)
18	JP11	COM6 VCC5/DCD+VCC12/RI Select Jumper
19	JP18	COM4 RS232/RS485 Select Jumper2
20	JP17	COM4 RS485 Signal 120Ω Resistive termination Select Jumper
21	JP16	COM3 RS422 TX/RS485 Signal 120Ω Resistive termination Select Jumper
22	JP10	COM5 VCC5/DCD+VCC12/RI Select Jumper
23	JP7	COM4 RS232/RS485 Select Jumper1
24	JP9	COM4 VCC5/DCD+VCC12/RI Select Jumper
25	JP5	COM3 RS232/RS422 Select Jumper1
26	JP6	COM3 RS232/RS422 Select Jumper2
27	JP8	COM3 VCC5/DCD+VCC12/RI Select Jumper
28	JP12	COM2 VCC5/DCD+VCC12/RI Select Jumper
29	JP19	Watch Dog Reset Enable/Disable Select Jumper
30-1	COM2	COM2 Header
30-2	COM3	COM3 Header

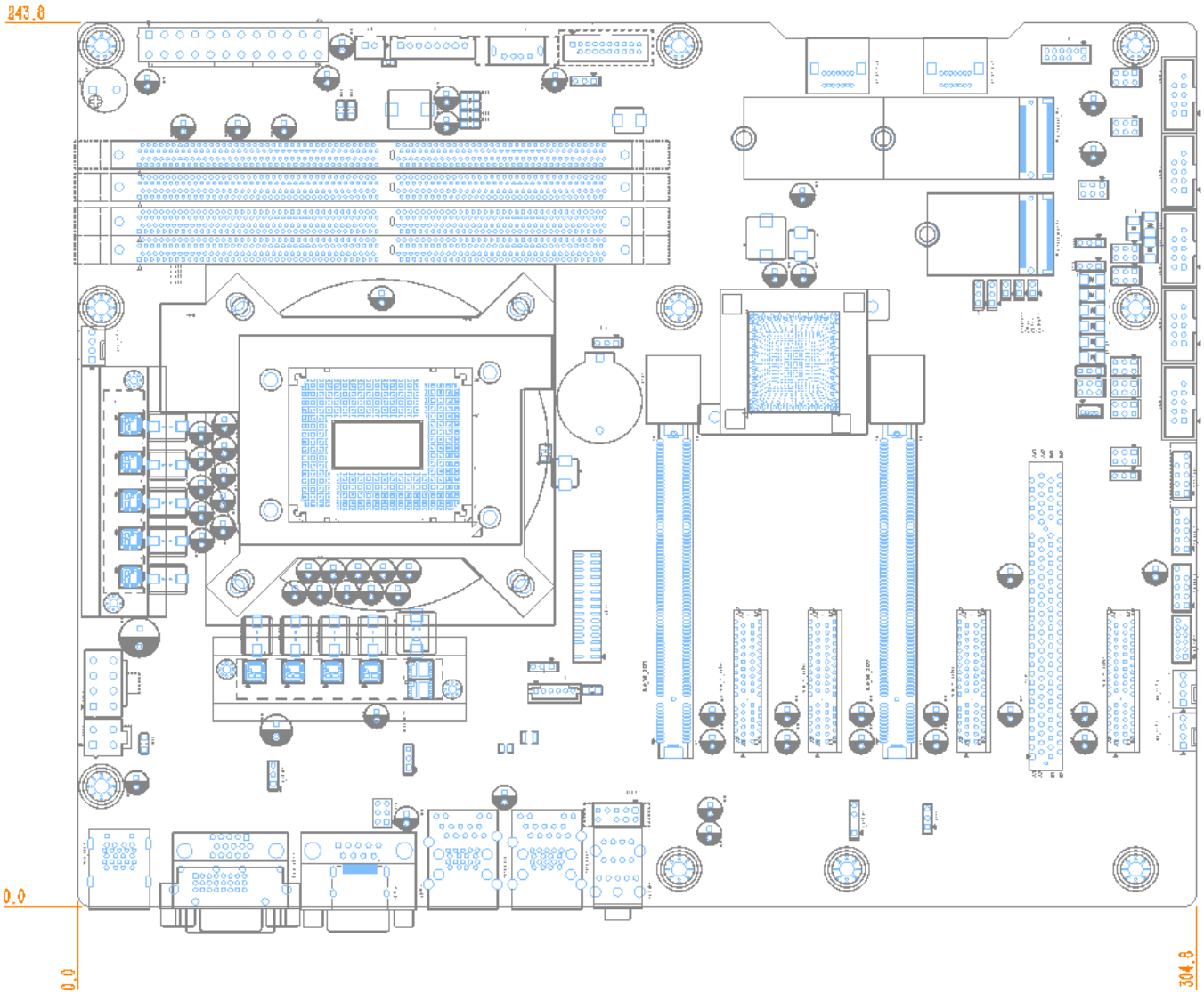
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30-3	COM4	COM4 Header
30-4	COM5	COM5 Header
30-5	COM6	COM6 Header
31	F_PANEL1	Front Panel Header
32	F_USB2_1	Front USB2.0 Header1
33	F_USB2_2	Front USB2.0 Header2
34	J_GPIO1	GPIO Header
35	SYS_FAN2	System FAN Wafer2
36	SYS_FAN1	System FAN Wafer1
37	PCIE_4X_SLOT4	PCI-E 4x Slot4 (PCIe 4X)
38	PCI1	PCI Slot
39	PCIE_4X_SLOT3	PCI-E 4x Slot3 (PCIe 4X)
40	JP14	PCI CLK 66MHz Enable/Disable Select Jumper
41	PCIE_16X_SLOT2	PCI-E 16x Slot2 (PCIe 8X)
42	J_SPDIF1	SPDIF Out Header
43	PCIE_4X_SLOT2	PCI-E 4x Slot2 (PCIe 4X)
44	PCIE_4X_SLOT1	PCI-E 4x Slot1 (PCIe 4X)
45	PCIE_16X_SLOT1	PCI-E 16x Slot1 (PCIe 8X)
46	F_AUDIO1	Front Audio Header (Line-Out + MIC)
47	EDP_P1	eDP Backlight Control Wafer
48	JC_EDP1	eDP VDD Select Jumper
49	EDP1	eDP Signal Header
50	JP3	COM1 VCC5/DCD+VCC12/RI Select Jumper
51	ATX3	ATX 4P CPU Power Input Connector
52	ATX2	ATX 8P CPU Power Input Connector
53	CPU_FAN1	CPU FAN Wafer
54-1	DIMM1	DDR4 CHA DIMM Slot1
54-2	DIMM3	DDR4 CHA DIMM Slot2
54-3	DIMM2	DDR4 CHB DIMM Slot3
54-4	DIMM4	DDR4 CHB DIMM Slot4

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3.2 Mechanical Dimensions

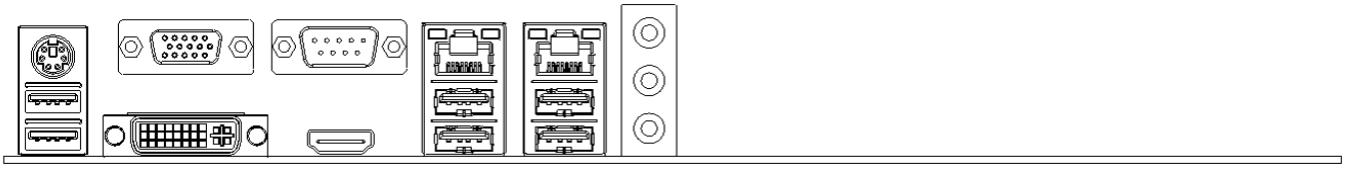
Top View



Dimensions: mm

Figure 4: Mechanical Dimensions

Side View



Dimensions: mm

Figure 5: Mechanical Dimensions - IO Panel

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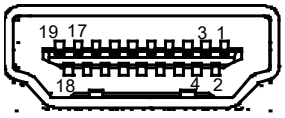
4 Connector Pinouts

See 3.1 Connector Locations on page 7 for connector locations.

4.1 Rear IO Connectors

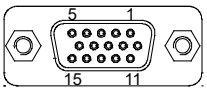
4.1.1 HDMI

Pin	Signal	Pin	Signal
1	HDMI1_CON_DP2	2	GND
3	HDMI1_CON_DN2	4	HDMI1_CON_DP1
5	GND	6	HDMI1_CON_DN1
7	HDMI1_CON_DP0	8	GND
9	HDMI1_CON_DN0	10	HDMI1_CON_CKP
11	GND	12	HDMI1_CON_CKN
13	NC	14	NC
15	HDMI1_DDC_CLK	16	HDMI1_DDC_DATA
17	GND	18	+5V_HDMI
19	HDMI1_CON_HPD		



4.1.2 VGA Connector

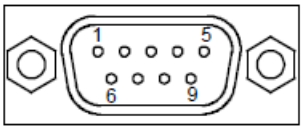
Pin	Signal	Pin	Signal
1	VGA_CON_RED	2	VGA_CON_GREEN
3	VGA_CON_BLUE	4	NC
5	GND	6	GND
7	GND	8	GND
9	+5V_HDMI	10	GND
11	NC	12	VGA_DDCDAT
13	VGA_CON_HS	14	VGA_CON_VS
15	VGA_DDCCLK		



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4.1.3 COM 1 (COM1 DB9/M Connector)

Pin	Signal
	RS-232
1	PIN 1 [1]
2	RxD
3	TxD
4	DTR#
5	GND
6	DSR#
7	RTS#
8	CTS#
9	PIN 9 [1]

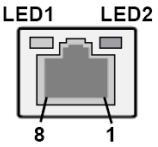


Note: PIN1 of COM1 can be DCD (default) /5V and Pin9 of COM1 can be RI(Default) / 12V, selectable by “COM1 DCD/RI Select Jumper”. (JP3)

4.1.4 Ethernet Connectors (LAN1, LAN2)

RJ45_USB1 (LAN1) (GBE LAN RJ45 Connector1)

Pin	Signal		Pin	Signal	
1	MDI1_0+		5	MDI1_2+	
2	MDI1_0-		6	MDI1_2-	
3	MDI1_1+		7	MDI1_3+	
4	MDI1_1-		8	MDI1_3-	
LED1	Active LED	ACT: Blinking Green	LED2	Speed LED	1000M: Turn Orange
		LINK only: Lights On			100M: Turn Green
		Stop: Lights Off			10M: Lights Off



Note: RJ45_USB1 (LAN1) I219-V does not support Intel AMT 12.0 and Intel vPro components by default.

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RJ45_ USB2 (LAN2) (GBE LAN RJ45 Connector2)

Pin	Signal		Pin	Signal	
1	MDI2_0+		5	MDI2_2+	
2	MDI2_0-		6	MDI2_2-	
3	MDI2_1+		7	MDI2_3+	
4	MDI2_1-		8	MDI2_3-	
LED1	Active LED	ACT: Blinking Green	LED2	Speed LED	1000M/2.5G: Turn Orange
		LINK only: Lights On			100M: Turn Green
		Stop: Lights Off			10M: Lights Off

4.2 Onboard Headers / Connectors

4.2.1 ATX12V Power Connector (ATX2)

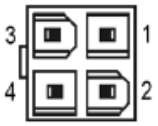
This motherboard provides an 8-pin and 4-pin ATX 12V power connector.

Pin	Signal	Pin	Signal	Pin	Signal	Pin	Signal
8	ATX12V	7	ATX12V	6	ATX12V	5	ATX12V
4	GND	3	GND	2	GND	1	GND

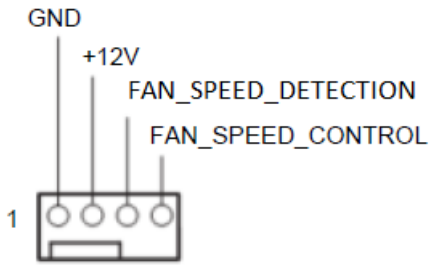


4.2.2 ATX12V Power Connector (ATX3)

Pin	Signal	Pin	Signal
3	ATX12V	1	GND
4	ATX12V	2	GND



4.2.3 Fan Connectors (SYS_FAN1/2, CPU_FAN1)



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4.2.4 USB Connectors (USB2_1)

There are two USB 2.0 Type-A vertical connectors on this motherboard.

Pin	Signal
4	GND
3	USB_D+
2	USB_D-
1	USB_PWR



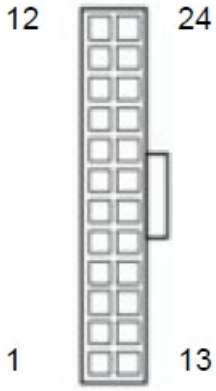
4.2.5 SMBUS1

Pin	Signal
1	GND
2	SMB_DATA
3	SMB_CLK
4	VCC5



4.2.6 24-pin ATX Power Input Connector (ATX1)

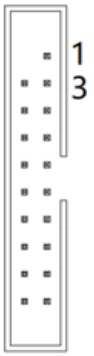
Pin	Signal	Pin	Signal
12	+3V	24	GND
11	+12V	23	+5V
10	+12V	22	+5V
9	ATX+5VSB	21	+5V
8	PWROK_PS	20	NA
7	GND	19	GND
6	+5V	18	GND
5	GND	17	GND
4	+5V	16	PSON#
3	GND	15	GND
2	+3V	14	-12V
1	+3V	13	+3V



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4.2.7 USB 3.0 Header (F_USB3_1)

Pin	Signal	Pin	Signal
		1	VCC5
4	VCC5	3	USB3.0_RX1-
6	USB3.0_RX2-	5	USB3.0_RX1+
8	USB3.0_RX2+	7	GND
10	GND	9	USB3.0_TX1-
12	USB3.0_TX2-	11	USB3.0_TX1+
14	USB3.0_TX2+	13	GND
16	GND	15	USB2.0_1-
18	USB2.0_2-	17	USB2.0_1+
20	USB2.0_2+	19	N/C



Front USB 3.0 Box Header 10*2 Pin 2.00 mm

4.2.8 SATA Connector (SATA 1/2/3/4)

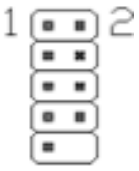
Pin	Signal	Pin	Signal
1	GND	5	SATA-B-
2	SATA-A+	6	SATA-B+
3	SATA-A-	7	GND
4	GND		



4.2.9 Front Panel Audio Header (F_PANEL1)

This header accommodates several system front panel functions.

Pin	Signal	Pin	Signal
1	HDD 3.3V LED+	2	POWER 3.3V LED+
3	HDD 3.3V LED-	4	POWER 3.3V LED-
5	RESET-	6	POWER+
7	RESET+	8	POWER-
9	N/C		

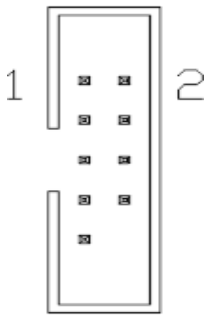


Front Panel Header 5*2 Pin 2.54mm

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4.2.10 COM Port Header (COM 2-6)

	Pin	Signal	Pin	Signal
COM2	1	COM2_PIN1 [1]	2	COM2_DSR
	3	COM2_RXD	4	COM2_RTS
	5	COM2_TXD	6	COM2_CTS
	7	COM2_DTR	8	COM2_PIN8 [1]
	9	GND		
	Pin	**Signal**	**Pin**	**Signal**
COM3	1	COM3_PIN1 [2] [3]	2	COM3_DSR
	3	COM3_PIN3 [3]	4	COM3_RTS
	5	COM3_PIN5 [3]	6	COM3_CTS
	7	COM3_PIN7 [3]	8	COM3_PIN8 [2] [3]
	9	GND		
	Pin	**Signal**	**Pin**	**Signal**
COM4	1	COM4_PIN1 [4] [5]	2	COM4_DSR
	3	COM4_PIN3 [4]	4	COM4_RTS
	5	COM4_TXD	6	COM4_CTS
	7	COM4_DTR	8	COM4_PIN8 [5]
	9	GND		
	Pin	**Signal**	**Pin**	**Signal**
COM5	1	COM5_PIN1 [6]	2	COM5_DSR
	3	COM5_RXD	4	COM5_RTS
	5	COM5_TXD	6	COM5_CTS
	7	COM5_DTR	8	COM5_PIN8 [6]
	9	GND		
	Pin	**Signal**	**Pin**	**Signal**
COM6	1	COM6_PIN1 [7]	2	COM6_DSR
	3	COM6_RXD	4	COM6_RTS
	5	COM6_TXD	6	COM6_CTS
	7	COM6_DTR	8	COM6_PIN8 [7]
	9	GND		



Box Header 5*2 Pin 2.54mm

Note: [1]: PIN1 of COM2 can be DCD (default) or 5V, and Pin8 of COM2 can be RI (Default) or 12V, selectable by the “COM2 VCC5/DCD+VCC12/RI Select Jumper”. (JP12)

[2]: PIN1 of COM3 can be DCD (default) or 5V, and Pin8 of COM3 can be RI(Default)or 12V, selectable by the “COM3 VCC5/DCD+VCC12/RI Select Jumper”. (JP8)

[3]: COM3 can be RS232 (default), RS422, or RS485 selecting by JP4, JP5, JP6. COM3 can also be to RS485 or RS422 by using the jumper and setting the BIOS to RTS mode.

[4]: COM4 can be RS232 (default) or RS485 selecting by JP7and JP18. COM4 can also be set RS485 by using the jumper and setting the BIOS to RTS mode.

[5]: PIN1 of COM4 can be DCD (default) or 5V, and Pin8 of COM4 can be RI(Default) or 12V, selectable by the “COM4 VCC5/DCD+VCC12/RI Select Jumper”. (JP9)

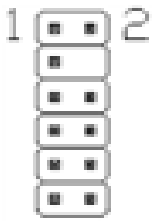
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[6]: PIN1 of COM5 can be DCD (default) or 5V, and Pin8 of COM5 can be RI(Default) or 12V, selectable by the “COM5 VCC5/DCD+VCC12/RI Select Jumper”. (JP10)

[7]: PIN1 of COM6 can be DCD (default) or 5V, and Pin8 of COM6 can be RI(Default) or 12V, selectable by the “COM5 VCC5/DCD+VCC12/RI Select Jumper”. (JP11)

4.2.11 ESPI Header (J_ESPI1)

Pin	Signal	Pin	Signal
1	ESPI_IO0	2	VCC3.3_Dual
3	ESPI_IO1		
5	ESPI_IO2	6	ESPI_CLK
7	ESPI_IO3	8	GND
9	ESPI_CS0	10	VCC3.3S
11	ESPI_ALERT0_N	12	PLT_RST_N [1]

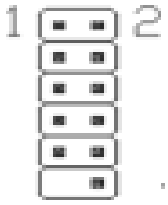


ESPI Header (Debug Only) 6*2 Pin 2.00mm

Note: “H” or “L” indicates whether the default voltage is at high or low level. 95V GPIO)

4.2.12 GPIO Header (J_GPIO1)

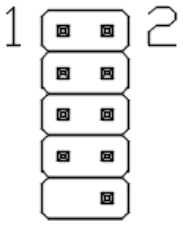
Pin	Signal	Pin	Signal
1	SIO_GPI70 (0xA06 Bit0, H [1])	2	SIO_GPI71 (0xA06 Bit1, H)
3	SIO_GPI72 (0xA06 Bit2, H)	4	SIO_GPI73 (0xA06 Bit3, H)
5	GND	6	SIO_GPO74 (0xA06 Bit4, H)
7	SIO_GPO75 (0xA06 Bit5, H)	8	SIO_GPO76 (0xA06 Bit6, H)
9	SIO_GPO77 (0xA06 Bit7, H)	10	VCC5
		12	N/C



GPIO Header 6*2 Pin 2.00mm

4.2.13 USB 2.0 Header (F_USB2_1, F_USB2_2)

Pin	Signal	Pin	Signal
1	VCC5	2	VCC5
3	USB2.0_1-	4	USB2.0_2-
5	USB2.0_1+	6	USB2.0_2+
7	GND	8	GND
		10	N/C



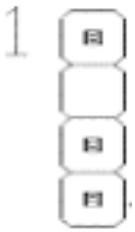
Front USB2.0 Header2 5*2 Pin 2.54mm

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4.2.14 SPDIF Header (J_SPIDIF1)

Pin	Signal	Pin	Signal
1	VCC5		
3	SPDIF_OUT	4	GND

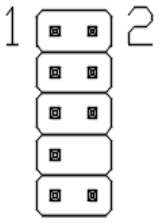
SPDIF Out Header 4*1 Pin 2.54mm



4.2.15 Front Panel Audio Header (F_Audio1)

Pin	Signal	Pin	Signal
1	MIC_IN_L	2	GND
3	MIC_IN_R	4	VCC3.3
5	LINE_OUT_R	6	MIC_RET
7	GND		
9	LINE_OUT_L	10	LINE_OUT_RET

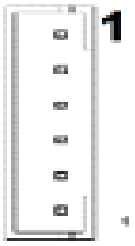
Front Audio Header (Line-Out + MIC) 5*2 Pin 2.54mm



4.2.16 eDP Backlight Control Wafer (EDP_P1)

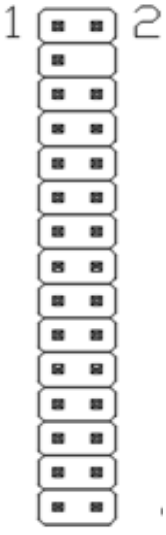
Pin	Signal	Pin	Signal
1	GND	4	EDP_BKLT_EN
2	GND	5	VCC12
3	EDP_BKLT_CTL	6	VCC12

eDP Backlight Control Wafer 6*1 Pin 2mm



4.2.17 eDP Signal Header (EDP1)

Pin	Signal	Pin	Signal
1	VDD_PANEL [1]	2	VDD_PANEL [1]
3	VDD_PANEL [1]		
5	GND	6	EDP_HPD
7	N/C	8	N/C
9	N/C	10	N/C
11	N/C	12	N/C
13	GND	14	GND
15	N/C	16	N/C
17	N/C	18	N/C
19	EDP_TX0-	20	EDP_TX0+
21	EDP_TX1-	22	EDP_TX1+
23	EDP_TX2-	24	EDP_TX2+



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Pin	Signal	Pin	Signal
25	GND	26	GND
27	EDP_TX3-	28	EDP_TX3+
29	EDP_AUX-	30	EDP_AUX+

eDP Signal Header 15*2 Pin 2mm

Note: Panel Power VDD can be set to 3.3V (default) /5V selectable by the eDP VDD Select Jumper" (JC_EDP1).

4.3 Jumper and Switch Settings

4.3.1 COM1 VCC5/DCD+VCC12/RI Select Jumper (JP3)

Setting	Function
1-3, 2-4	COM1_PIN1: VCC5 COM1_PIN8: VCC12
3-5 , 4-6(Default)	COM1_PIN1: DCD COM1_PIN8: RI



Jumper 3*2 Pin 2.54mm

Note: Jumper 1-3/3-5 select the COM1_PIN1 signal. Jumper 2-4/4-6 select the COM1_PIN8 signal.

4.3.2 COM2 VCC5/DCD+VCC12/RI Select Jumper (JP12)

Setting	Function
1-3, 2-4	COM2_PIN1: VCC5 COM2_PIN8: VCC12
3-5, 4-6(Default)	COM2_PIN1: DCD COM2_PIN8: RI



Jumper 3*2 Pin 2.54mm

Note: Jumper 1-3/3-5 select the COM2_PIN1 signal. Jumper 2-4/4-6 select the COM2_PIN8 signal.

4.3.3 COM3 VCC5/DCD+VCC12/RI Select Jumper (JP8)

Setting	Function
1-3, 2-4	COM3_PIN1: VCC5 COM3_PIN8: VCC12
3-5, 4-6(Default)	COM3_PIN1: DCD/ RS422_TX-/ RS485- COM3_PIN8: RI



Jumper 3*2 Pin 2.54mm

Note: Jumper 1-3/3-5 select the COM3_PIN1 signal. Jumper 2-4/4-6 select the COM3_PIN8 signal.

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4.3.4 COM4 VCC5/DCD+VCC12/RI Select Jumper (JP9)

Setting	Function
1-3, 2-4	COM4_PIN1: VCC5 COM4_PIN8: VCC12
3-5, 4-6(Default)	COM4_PIN1: DCD/TX COM4_PIN8: RI



Jumper 3*2 Pin 2.54mm

Note: Jumper 1-3/3-5 select the COM4_PIN1 signal. Jumper 2-4/4-6 select the COM4_PIN8 signal.

4.3.5 COM5 VCC5/DCD+VCC12/RI Select Jumper (JP10)

Setting	Function
1-3, 2-4	COM5_PIN1: VCC5 COM5_PIN8: VCC12
3-5, 4-6(Default)	COM5_PIN1: DCD COM5_PIN8: RI



Jumper 3*2 Pin 2.54mm

Note: Jumper 1-3/3-5 select the COM5_PIN1 signal. Jumper 2-4/4-6 select the COM5_PIN8 signal.

4.3.6 COM6 VCC5/DCD+VCC12/RI Select Jumper (JP11)

Setting	Function
1-3, 2-4	COM6_PIN1: VCC5 COM6_PIN8: VCC12
3-5, 4-6(Default)	COM6_PIN1: DCD COM6_PIN8: RI



Jumper 3*2 Pin 2.54mm

Note: Jumper 1-3/3-5 select the COM6_PIN1 signal. Jumper 2-4/4-6 select the COM6_PIN8 signal.

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4.3.7 COM3 RS232/422/485 Select Jumpers (JP4/JP5/JP6)

Setting	Function
JP4: 1-2 JP5: 3-5、4-6 JP6: 3-5、4-6 (Default)	COM3: RS232 (COM3_PIN1: DCD COM3_PIN3: RXD COM3_PIN5: TXD COM3_PIN7: DTR)
JP4: 3-4 JP5: 1-3、2-4 JP6: 1-3、2-4	COM3: RS422 (COM3_PIN1: RS422_TX- COM3_PIN3: RS422_TX+ COM3_PIN5: RS422_RX+ COM3_PIN7: RS422_RX-)
JP4: 5-6 JP5: 1-3、2-4 JP6 (No Effect)	COM3: RS485 (COM3_PIN1: RS485- COM3_PIN3: RS485+)

Jumper 3*2 Pin 2.54mm



4.3.8 COM4 RS232/485 Select Jumpers (JP7/JP18)

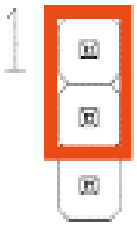
Setting	Function
JP18: 1-2 JP7: 3-5、4-6 (Default)	COM4: RS232 (COM4_PIN1: DCD COM4_PIN3: RXD)
JP18: 5-6 JP7: 1-3、2-4	COM4: RS485 (COM4_PIN1: RS485- COM4_PIN3: RS485+)



4.3.9 COM3 RS422 RX Signal 120Ω Resistive Termination Select Jumper (JP15)

Setting	Function
1-2 (Default)	Disable
2-3	Enable

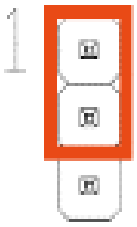
Jumper 3*1 Pin 2.54mm



4.3.10 COM3 RS422 TX/RS485 Signal 120Ω Resistive Termination Select Jumper (JP16)

Setting	Function
1-2 (Default)	Disable
2-3	Enable

Jumper 3*1 Pin 2.54mm

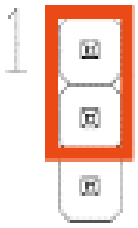


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4.3.11 COM4 RS485 RX Signal 120Ω Resistive Termination Select Jumper (JP17)

Setting	Function
1-2 (Default)	Disable
2-3	Enable

Jumper 3*1 Pin 2.54mm



4.3.12 Case Open Headers (J_COPEN1)

Setting	Function
1-2: Connected	Active Case Open
1-2: Open	Normal

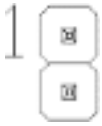
Header 2*1 Pin 2.54mm



4.3.13 DNX Force Reload Disable/Enable Select Jumper (J_DNX1)

Setting	Function
1-2: Connected	Enable
1-2: Open	Disable

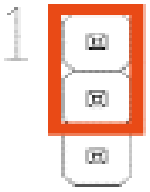
Header 2*1 Pin 2.54mm



4.3.14 ATX/AT Mode Jumper (J_AT/ATX1)

Setting	Function
1-2 (Default)	ATX Mode
2-3	AT Mode

Jumper 3*1 Pin 2.54mm



4.3.15 ME Flash Jumper (J_ME1)

Setting	Function
1-2: Open (Default)	ME Protect Enable
1-2: Connected	ME Protect Disable

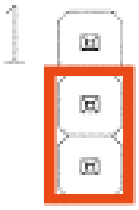
Jumper 2*1 Pin 2.54mm



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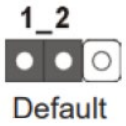
4.3.16 Watchdog Reset Enable/Disable Select Jumper (JP19)

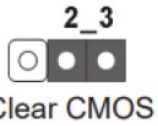
Setting	Function
1-2	Normal
2-3(Default)	Reset



Jumper 3*1 Pin 2.54mm

4.3.17 Clear CMOS Jumper (CLR_CMOS1)





1-2 : Normal
2-3 : Clear CMOS

CLR_CMOS1 allows you to clear the data in CMOS. To clear and reset the system parameters to the default setup, please turn off the computer and unplug the power cord from the power supply. After waiting for 15 seconds, use a jumper cap to short pin 2 and pin 3 on CLR_CMOS1 for 5 seconds. However, please do not clear the CMOS immediately after updating the BIOS. If you need to clear the CMOS right after updating the BIOS, you must first boot up the system, and then shut it down before performing the clear-CMOS action. Please do note that the date, time, and user default profile will only be cleared if the CMOS battery is removed.

4.3.18 Power Monitor Wafer (VOLT1)

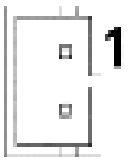
Pin	Signal	Pin	Signal
1	VCC5A	5	VCC5S
2	GND	6	VCC3.3S
3	GND	7	VCC12_N
4	VCC5PS	8	VCC12S



Jumper 8*1 Pin 2.54mm

4.3.19 Power on Wafer (PSON1)

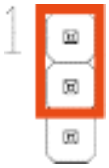
Pin	Signal	Pin	Signal
1	PSON_N	2	GND



Jumper 2*1 Pin 2.54mm

4.3.20 eDP VDD Select Jumper (JC_EDP1)

Setting	Function
1-2(Default)	VCC3.3
2-3	VCC5



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4.4 Expansion Slots

There are 6 PCI Express slots, 1 PCI slots, 2 M.2 sockets on this motherboard.

4.4.1 PCIe and PCI slots:

	Slot1 (PCIe1)	Slot2 (PCIe2)	Slot3 (PCIe3)	Slot4 (PCIe4)	Slot5 (PCIe5)	Slot6 (PCI)	Slot7 (PCIe6)
Source	CPU	CPU	PCH	CPU	PCH	PCI bridge	PCH
Bandwidth	PCIe 4.0	PCIe 4.0	PCIe 3.0	PCIe 4.0	PCIe 4.0	PCI	PCIe 4.0
Lane Config	X8	X4	X4	X8	X4	PCI	X4

4.4.2 M.2 sockets:

- M.2 Key-M (M_2_PCIESSD_M1)

Pin	Signal	Signal	Pin
1	GND	+3.3V	2
3	GND	+3.3V	4
5	PERn3	NA	6
7	PERp3	NA	8
9	GND	LED	10
11	PETn3	+3.3V	12
13	PETp3	+3.3V	14
15	GND	+3.3V	16
17	PERn2	+3.3V	18
19	PERp2	NA	20
21	GND	NA	22
23	PETn2	NA	24
25	PETp2	NA	26
27	GND	NA	28
29	PERn1	NA	30
31	PERp1	NA	32
33	GND	NA	34
35	PETn1	NA	36
37	PETp1	DEVSLP	38
39	GND	SMB_CLK	40
41	PERn0	SMB_DATA	42
43	PERp0	NA	44
45	GND	NA	46
47	PETn0	NA	48

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Pin	Signal	Signal	Pin
49	PETp0	PERST#	50
51	GND	CLKREQ#	52
53	PEFCLKn	WAKE#	54
55	PEFCLKp	NA	56
57	GND	NA	58
67	NA	NA	68
69	PEDET	+3.3V	70
71	GND	+3.3V	72
73	GND	+3.3V	74
75	GND		

- M.2 Key-E (M.2_KEYE_WLAN1)

Pin	Signal	Signal	Pin
1	GND	+3VSB	2
3	USB_D+	+3VSB	4
5	USB_D-	NA	6
7	GND	NA	8
9	CNV_WGR_D1-	CNV_RF_RESET	10
11	CNV_WGR_D1+	NA	12
13	GND	MODEM_CLKREQ	14
15	CNV_WGR_D0-	NA	16
17	CNV_WGR_D0+	GND	18
19	GND	NA	20
21	CNV_WGR_CLK-	CNV_BRI_RSP	22
23	CNV_WGR_CLK+		
33	GND	CNV_BGI_DT	32
35	PETp	CNV_RGI_RSP	34
37	PETn	CNV_BRI_DT	36
39	GND	NA	38
41	PERp	NA	40
43	PERn	NA	42
45	GND	NA	44
47	PEFCLKp	NA	46
49	PEFCLKn	NA	48
51	GND	SUSCLK	50
53	CLKREQ#	PERST0#	52

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Pin	Signal	Signal	Pin
55	WAKE#	W_DISABLE1#	54
57	GND	W_DISABLE2#	56
59	CNV_WT_D1-	SMB_DATA	58
61	CNV_WT_D1+	SMB_CLK	60
63	GND	NA	62
65	CNV_WT_D0-	CLKIN_XTAL_LCP	64
67	CNV_WT_D0+	NA	66
69	GND	NA	68
71	CNV_WT_CLK-	NA	70
73	CNV_WT_CLK+	+3VSB	72
75	GND	+3VSB	74

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5 Driver Installation

Download the requisite drivers for your system from the IMB-C47 product page:
https://www.adlinktech.com/Products/Industrial_Motherboards_SBCs/ATXMotherboards/IMB-C47

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6 BIOS Setup

6.1 Menu Structure

This section presents the primary menus of the BIOS (UEFI) Setup Utility. Use the following table as a quick reference forthe contents of the BIOS Setup Utility. The subsections describe the submenus and options for each menu item.

Options to enter BIOS Setup Utility:

- Press [Del] during POST (Power-On-Self-Test)

Main	**Advanced**	**Chipset**
Sets up the system time/date and displays the system information: - BIOS Information - Memory Information - System Date - System Time	Contains advanced system configurations, including: ▶ CPU Configuration ▶ PCH-FW Configuration ▶ Trusted Computing ▶ ACPI Settings ▶ Super IO Configuration ▶ Hardware Monitor ▶ Power Control ▶ S5 RTC Wake Settings ▶ USB Configuration ▶ Network Stack Configuration ▶ NVMe Configuration	Displays Hardware Status info, including: ▶ System Agent (SA) Configuration ▶ PCH-IO Configuration
Security	**Boot**	**Save & Exit**
Changes or clears the supervisor/user password for the system: - Administrator Password - User Password ▶ Secure Boot	Configures the boot settings and boot priority for available devices: - Setup Prompt Timeout - Bootup Num Lock State - Full Screen Logo FIXED BOOT ORDER Priorities - Boot Option #X	Exits the BIOS Setup Utility while saving or discard the changes made: Save Options - Save Changes and Exit - Discard Changes and Exit - Save Changes and Reset - Discard Changes and Reset - Save Changes - Discard Changes Default Options - Restore Defaults - Save as User Defaults - Restore User Defaults Boot Override - BIOS: Built-in EFI shell Launch EFI Shell from filesystem device

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6.2 Main Menu

Upon entering the BIOS Setup Utility, the Main Menu is displayed, providing read-only information about your system and also allows you to set the System Date and Time. Refer to the screenshots and tables below for details of the submenus and settings.

Feature	Options
BIOS Information	
BIOS Vendor	Info only
Core Version	Info only
Compliancy	Info only
Project Version	IMB-C47_x.xx.xx
Build Date and Time	Info only
Access Level	Info only
Memory Information	
Total Memory	Info only
Memory Frequency	Info only
System Date	xxx mm/dd/yyyy
System Time	hh:mm:ss

6.3 Advanced

This menu contains the following information:

Feature	
▶ CPU Configuration	▶ Power Control
▶ PCH-FW Configuration	▶ S5 RTC Wake Settings
▶ Trusted Computing	▶ USB Configuration
▶ ACPI Settings	▶ Network Stack Configuration
▶ Super IO Configuration	▶ NVME Configuration
▶ Hardware Monitor	▶

6.3.1 CPU Configuration

Feature	Description
Efficient-core Information	Displays the E-core Information.
Performance-core Information	Displays the P-core Information.
ID	Displays the Processor ID.
Brand String	Displays the Brand String.
VMX	VMX (Virtual-Machine Extensions) supported or not.
SMX/TXT	SMX (Safer Mode Extensions) /TXT (Trusted Execution Technology) Supported or Not.
Intel (R) SpeedStep(tm)	Allows more than two frequency ranges to be supported.
Intel (R) Speed Shift Technology	Enables or disables Intel (R) Speed Shift Technology Support. Enabling will expose the CPPC v2 interface to allow for hardware-controlled P-states.
Turbo Mode	Enables or disables processor Turbo Mode (requires EMTTM enabled too). Auto means enabled.
C states	Enables or disables CPU Power Management. Allows CPU to go to C states when it's not 100% utilized.
Enhanced C-states	Enables or disables C1E. When enabled, CPU will switch to minimum speed when all

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	cores enter C-State.
Package C State Limit	Maximum Package C State Limit Setting. CPU Default: Leaves to factory default value. Auto: Initializes to deepest available Package C State Limit.
Intel (VMX) Virtualization Technology	When enabled, a VMM can utilize the additional hardware capabilities provided by Vanderpool Technology.
Hyper-Threading	Enables or disables Hyper-Threading Technology.

6.3.2 PCH-FW Configuration

Configure Management Engine Technology Parameters.

Feature	Description
ME State	Displays the ME Firmware information.

6.3.3 Trusted Computing

Trusted Computing Settings.

Feature	Description
TPM 2.0 Device information	Displays the information of the TPM 2.0 device.
Security Device Support	Enables or Disables BIOS support for security devices. The O.S. will not show security devices. The TCG EFI protocol and INT1A interfaces will not be available.

6.3.4 ACPI Settings

Set ACPI (Advanced Configuration and Power Management Interface) Parameters.

Feature	Description
Enable Hibernation	Enables or disables System ability to Hibernate (OS/S4 Sleep State). This option may not be effective with some operating systems.
ACPI Sleep State	Selects the highest ACPI sleep state the system will enter when the SUSPEND button is pressed.

6.3.5 Super IO Configuration

System Super IO Chip Parameters.

Feature	Description
COM1-6	Sets Parameters of Serial Port 1-6. The options after entering the interface settings above are as follows. • Serial Port: Enable or Disable Serial Port (COM). • Device Settings: Displays the Current Device Settings. • Change Settings: If the board is supported, this menu shows. Select an optimal setting for Super IO devices.
CHASSIS OPEN	Enables or disables the CHASSIS OPEN function.

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To Set Up the Chassis Open Detection:

1) Select “Advanced – Super IO Configuration - CHASSIS OPEN” (This menu shows that only this function is supported);

2) Set the item to “Enabled”.

Sound the alarm if the chassis is opened; stop the alarm when closed.

When the open-box detection cap is shorted, the chassis is considered to have been compromised. The POST interface will prompt for chassis intrusion alarms, and the status record needs to be cleared to close the alarm.

Clear Chassis Open Status:

1) In the BIOS Setup Utility, press the F3 key and then the Enter key to load the optimization default values. Save & exit to clear the out-of-box status record.

6.3.6 Hardware Monitor

Monitor hardware status.

Feature	Description
PC Health status	Displays the current of PC Health Status.
► **Fan function**	Fan function settings. **To Set Up the Fan Control:** 1) Select “Advanced - Hard Monitor - Fan Function” (This menu shows that only this function is supported); 2) Select the fan to set up, such as “CPU_FAN1 Mode”. Select the operating mode for this fan: Full on Mode, Automatic Mode, or Manual Mode. 3) After selecting manual mode, input the PWM value (range 0-255) to specify the fan speed. 4) After selecting Automatic mode, you will find options for Fan off Temperature Limit, Fan start Temperature Timit, Fan start PWM, and PWM SLOPE SETTING. Due to differences in fan performance, the actual performance of smart fans may differ from the set values, it is generally recommended to set the PWM SLOPE SETTING to 8 PWM to ensure that the fan speed correlates with the temperature rise.
CHASSIS OPEN	Sound the alarm if chassis is opened; Stop the alarm when closed. **To Set Up the Chassis Open Detection:** 1) Select “Advanced – Hardware Monitor - CHASSIS OPEN” (This menu shows that only this function is supported). 2) Set the item to “Enabled”. When the open-box detection cap is shorted, the chassis is considered to have been compromised. The POST interface will prompt for chassis intrusion alarms, and the status record needs to be cleared to close the alarm. **To Clear the Chassis Open Status:** In the BIOS Setup Utility, press the F3 key and enter key to load the optimization default value. Then save & exit to clear the out-of-box status record.

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6.3.7 Power Control

Power Button Control Settings.

Feature	Description
PowerOn After PowerFail	Specify which state to enter when power is re-applied after a power failure (G3 state).
Soft-Off by PWR-BTTN	Soft-Off by PWR-BTTN. Instant-Off/Delay 4 Sec.
PCIE OOR FIXED Enabled	Enables or disables PCIE OOR FIXED.

Set Up Power-on Self:

1) Select “Advanced - Power Control - PowerOn After PowerFail”.
2) Change “PowerOn After PowerFail” value: Power Off, Power On, or Last State.

6.3.8 S5 RTC Wake Settings

Enable system to wake from S5 using RTC alarm.

Feature	Description
Wake system from S5	Enables or disables System wake on alarm event. Select Fixed Time for the system to wake at the specified hr::min::sec. Select Dynamic Time for the system to wake at the the current time + the specified increase in minute(s).

To Set Up the Timed Power On:

1) Select “Advanced - S5 RTC Wake Settings - Wake system from S5”;
2) Change it from Disabled to Fixed Time;
3) Set the desired time by customizing the “Wake up day, Wake up hour, Wake up minute, wake up second” that appear. For example, day: 2, hour: 13, minute: 0, second: 0 means the system will boot on the 2nd of each month at 13:00. When day is set to 0, it means the system will power on every day at the specified time.

6.3.9 USB Configuration

USB Configuration parameters.

Feature	Description
USB Module Version	Displays the USB Module version.
USB Controllers	Displays the USB controllers.
USB Devices	Displays the USB devices.
Legacy USB Support	Enables Legacy USB support. The AUTO option disables legacy support if no USB devices are connected. The DISABLE option keeps USB devices available only for EFI applications.
XHCI Hand-off	This is a workaround for OSes without XHCI hand-off support. The XHCI ownership change should be claimed by the XHCI driver.
USB Mass Storage Driver Support	Enables or disables USB Mass Storage Driver support.
USB hardware delays and time-outs	USB transfer time-out: The time-out value for control, bulk, and interrupt transfers. Device reset time-out: The USB mass storage device Start Unit command time-out. Device power-up delay: The maximum time the device will take before it properly reports itself to the host controller. ‘Auto’ uses default value: for a Root port, it is 100ms, for a Hub port, the delay is taken from the hub descriptor.

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6.3.10 Network Stack Configuration

Network Stack Settings.

Feature	Description
Network Stack	Enables or disables BIOS Network Stack

Open BIOS PXE:

1) Select “Advanced - Network Stack Configuration”.
2) Set Network Stack or BIOS PXE to Enabled.

6.3.11 NVME Configuration

NVME Device options settings.

Feature	Description
NVMe Configuration	Displays the NVME device information

6.4 Chipset

This menu contains the following information:

Feature	Description
► **System Agent (SA) Configuration**	System Agent (SA) Parameters.
► **PCH-IO Configuration**	PCH Parameters.

6.4.1 System Agent (SA) Configuration

Feature	Description
► **Memory Configuration**	Memory Configuration Parameters.
► **Graphics Configuration**	Graphics Configuration.
► **VMD setup menu**	VMD Configuration Settings.
► **PCI Express Configuration**	PCI Express Configuration Settings.

6.4.1.1 Memory Configuration

Feature	Description
Memory Information	Displays the Memory RC version, speed, Timings, etc

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6.4.1.2 Graphics Configuration

Feature	Description
Primary Display	Select which of IGFX/PEG/PCI graphics devices should be the primary display, or select HG for hybrid graphics.
Internal Graphics	Keep IGFX enabled based on the setup options.
Above 4GB MMIO BIOS assignment	Enables or disables above 4GB memory-mapped IO BIOS assignment. This is enabled automatically when the aperture size is set to 2048 MB.
PCIE Resizable BAR Support	Enables or disables PCIE resizable BAR support.

6.4.1.3 VMD setup menu

Feature	Description
Enable VMD controller	Enables or disables to VMD controller.

6.4.1.4 PCI Express Configuration

Feature	Description
PCIE_4X_SLOT1	PCI Express Root Port Settings.
PCIE_16X_SLOT1	PCI Express Root Port Settings.
PCIE_16X_SLOT2	PCI Express Root Port Settings.

6.4.2 PCH-IO Configuration

Feature	Description
► **PCI Express Configuration**	PCI Express Configuration settings.
► **SATA Configuration**	SATA Device Configuration settings.
LAN1 Controller	Enables or disables onboard NIC.
Wake on LAN Enable	Enables or disables integrated LAN to wake the system.

6.4.2.1 PCI Express Configuration

Feature	Description
PCI Express Configuration	PCI Express Root Port Settings. ► LAN2 ► M.2_KEYE_WLAN1 ► PCI1 ► PCIE_4X_SLOT2 ► M.2_PCIESSD_M1 ► PCIE_4X_SLOT3 ► PCIE_4X_SLOT4

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6.4.2.2 SATA Configuration

Feature	Description
SATA Controller(s)	Enables or disables SATA devices.
SATA Mode Selection	Determines how SATA controller(s) operate.
M.2_KEYM_SSD1	Displays SSD Information.
Serial ATA Port X	Enables or Disables SATA ports.

6.5 Security

This menu contains the following information:

Feature	Description
Administrator Password	Sets Administrator Password.
User Password	Sets User Password.
► **Secure Boot**	Secures Boot configuration.

When you set up an administrator password, it is recommended that you enter the administrator password when you access the BIOS setup program to avoid encountering unavailable BIOS information or settings due to insufficient user rights. (no password is entered as a user).

When you set a user password, you must use the user password to access the BIOS setup program, which means that you need to enter the user password when booting the operating system (using the BIOS boot feature).

To Set/Change the Administrator Password:

1) Select “Security - Administrator Password”.

2) Enter the password you want to set in the “Create New Password” window and press Enter. You will need to enter your current password to confirm when changing your administrator password before accessing the “Create New Password” window.

3) Enter the password you want to set again to make sure it is entered correctly.

When you want to delete an administrator password, you should change the password by pressing carriage return directly in the “Create New Password” window to clear the password.

To Set/Change the User Password:

1) Select “Security - User Password”.

2) Enter the password you want to set in the “Create New Password” window that appears and press Enter. You will need to enter the current password to confirm when changing the user's password before accessing the “Create New Password” window.

3) Enter the password you want to set again to make sure it is entered correctly.

When you want to delete a user password, you should change the password by pressing carriage return directly in the “Create New Password” window to clear the password.

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6.5.1 Secure Boot

Feature	Description
Secure Boot	Secure Boot feature is active if Secure Boot is enabled, Platform Key (PK) is enrolled, and the system is in user mode. The mode change requires platform reset.
Secure Boot Mode	Secure Boot mode options: Standard or Custom. In Custom mode, Secure Boot Policy variables can be configured by a physically present user without full authentication.

6.6 Boot

This menu contains the following information:

Feature	Description
Setup Prompt Timeout	Number of seconds to wait for setup activation key. 65535 (0xFFFF) means indefinite waiting.
Bootup Num Lock State	Select the keyboard Num Lock state.
Full Screen Logo	Enables or disables Full Screen Logo option.
FIXED BOOT ORDER Priorities	Displays Boot order and allow to set Hard Drive/USB/XXX boot order in this group.
► **BIOS USB Key Drive BBS Priorities**	Sets the system boot order.

Set Boot Sequence:

1) Select the “Boot” menu page.

2) Change “Boot Option #1” to the desired device.

3) If there are more than one similar device, such as multiple hard drives, enter “Hard Disk Drive BBS Priorities” and change the boot order priority among the drives.

When the Boot page has the Boot mode select option, it is recommended that the value of “dual” so that both Legacy and BIOS boot projects can be booted. BIOS boot items will be filtered when selected for Legacy, and legacy boot items will be filtered when selected as BIOS.

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6.7 Save & Exit

This menu contains the following information:

Feature	Description
Save Options	
Save Changes and Exit	Exits system setup after saving the changes.
Discard Changes and Exit	Exits system setup without saving any changes.
Save Changes and Reset	Resets the system after saving the changes.
Discard Changes and Reset	Resets the system setup without saving any changes.
Save Changes	Saves changes done so far to any of the setup options.
Discard Changes	Discards changes done so far to any of the setup options.
Default Options	
Restore Defaults	Restores/Loads default values for all the setup options.
Save as User Defaults	Saves the changes done so far as User Defaults.
Restore User Defaults	Restore sthe User Defaults to all the setup options.
Boot Override	
BIOS: Built-in EFI shell	Launches BIOS Built-in BIOS Shell.
Launch EFI Shell from filesystem device	Attempts to launch EFI Shell application (Shell.efi) from one of the available filesystem devices.

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Safety Instructions

Read and follow all instructions marked on the product and in the documentation before you operate your system. Retain all safety and operating instructions for future use.

- Please read these safety instructions carefully.
- Please keep this User‘s Manual for later reference.
- Read the specifications section of this manual for detailed information on the operating environment of this equipment.
- When installing/mounting or uninstalling/removing equipment, turn off the power and unplug any power cords/cables.
- To avoid electrical shock and/or damage to equipment:
  - Keep equipment away from water or liquid sources.
  - Keep equipment away from high heat or high humidity.
  - Keep equipment properly ventilated (do not block or cover ventilation openings).
  - Make sure to use recommended voltage and power source settings.
  - Always install and operate equipment near an easily accessible electrical socket-outlet.
  - Secure the power cord (do not place any object on/over the power cord).
  - Only install/attach and operate equipment on stable surfaces and/or recommended mountings.
  - If the equipment will not be used for long periods of time, turn off and unplug the equipment from its power source.
- Never attempt to fix the equipment. Equipment should only be serviced by qualified personnel.

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