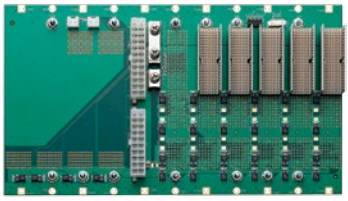
cBP-3210PR

3U 32-bit 10-slot CompactPCI® PlusIO Hybrid Backplane with Rear I/O



Front View



Rear View

Specifications

Model Name	cBP-3210PR
PICMG®	2.0 R3.0, 2.30, S.0
Dimensions	224.06 x 128.7 (mm, W x H)
Segment	One
cPCI Bus	32-bit/33 MHz
cPCI PlusIO System Slot	One, Right-hand side
cPCI Peripheral Slot	Five
cPCI Peripheral Slot Rear I/O	P2 rear I/O with AB-type shroud
CompactPCI® Serial Slot	Four slots, support PCIe x1, SATA/SAS, USB2.0 and GbE
V (I/O)	3.3V or 5V selectable, factory default is 5V
Power connectors	ATX connector x2, DC screw terminals
Other connectors	INH#, Reset, PWR_FAL#, Voltage LEDs

Ordering Information

Model Number	Description/Configuration
cBP-3210PR	3U 32-bit 10-slot CompactPCI PlusIO hybrid backplane with rear I/O

Functional Diagram

