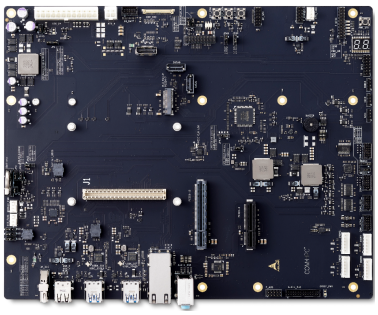
COM-HPC Mini Base

COM-HPC® Mini Type Reference Carrier Board in ATX Form Factor



Specifications

Core Module Interface	PICMG COM-HPC Revision 1.2 Support COM-HPC Mini Type modules
Dimensions	305 mm x 244 mm (ATX)
Expansion Buses	1x PCI Express x8 slot (PCIe lane 8-15)** * Note: An add-on card is required for 2 PCIe x4 signal lanes. 1x PCI Express x4 slot (PCIe lane 0-3) 1x M.2 M-key connector (PCIe lane 4-7)
POST LEDs	Onboard IO expander for BIOS POST code data on I^2C bus
Secondary BIOS	Onboard socket for secondary SPI flash
COM-HPC Connectors	Single 400-pin (10 mm height)
DP (Rear)	1x DP connector (DDI0)
eDP (Internal)	40-pin connector
Audio (Rear)	3x jacks (Mic-in/Line-in/Line-out)
SATA3.0 (Optional)	2x SATA3.0 (SATA0/SATA1)
USB 4 (Rear)	2x ports (Type C)
USB 3.2 gen2 (Rear)	2x ports (Type A)
USB 2.0 (Rear) USB 2.0 (Internal)	2x ports (Type A) 2x ports (w/ 1 pin header)
Ethernet (Rear)	2x ports RJ45 10GBase-T up to 10 GbE
GPIO	1x 12-bit header GPIO
Serial Port	2x ports 10-pin header (from module) 2x ports 10-pin header (from Super I/O)

Note: "Build option" indicates an alternative BOM configuration to support additional or alternative functions that are not available on the standard product. Be aware that these "build option" part numbers will need to be newly created and this will result in production lead times.

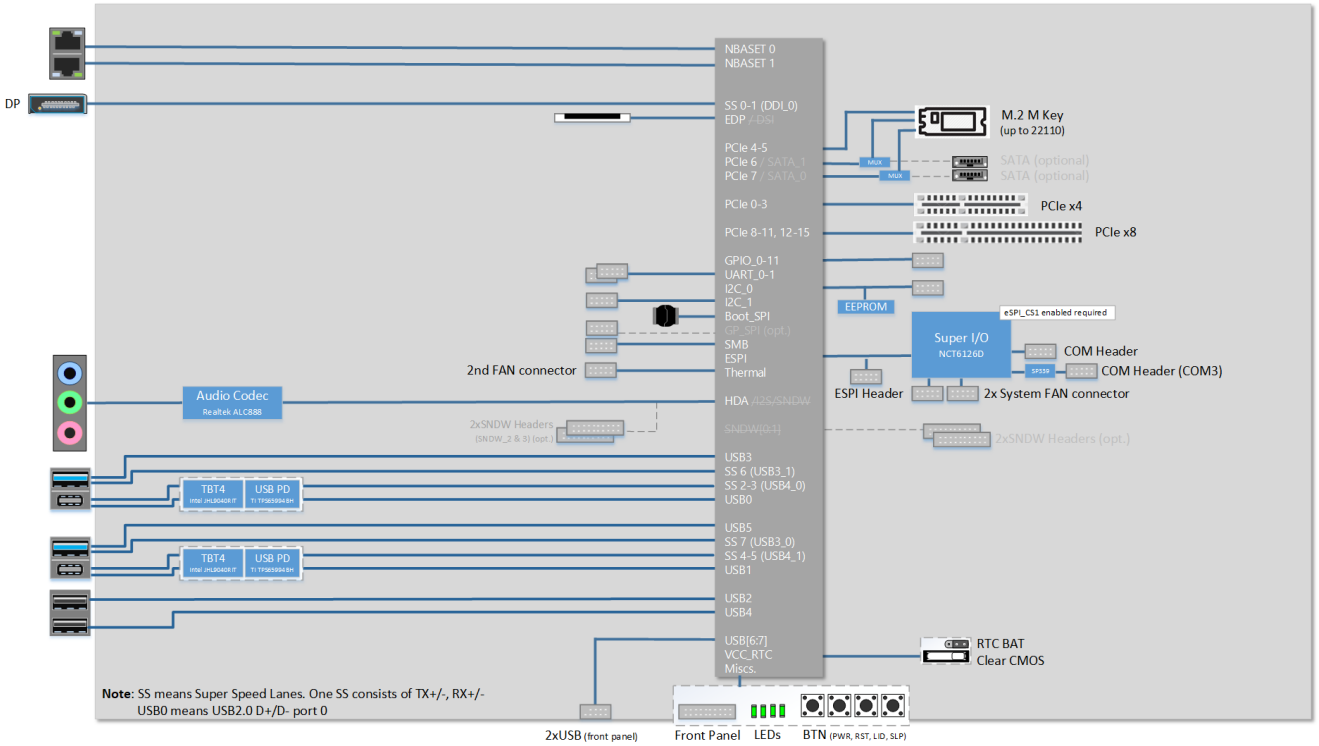
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Specifications

Fan	1x 4-pin fan connector, from module 2x 4-pin fan connector, from Super I/O
MIPI-CSI	2x CSI connector (CSI0/CSI1)*
GP_SPI	1x 12-pin header*
Feature Connectors	Headers for SMBus, I2C0/1, eSPI Header for front panel Jumpers for module signal controls
Miscellaneous	Power button, Reset button, Sleep button, Status LEDs (Lid button and Buzzer by project support)

* Note: Functions available are dependent on the COM-HPC module used.

Block diagram



Note: "Build option" indicates an alternative BOM configuration to support additional or alternative functions that are not available on the standard product. Be aware that these "build option" part numbers will need to be newly created and this will result in production lead times.

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