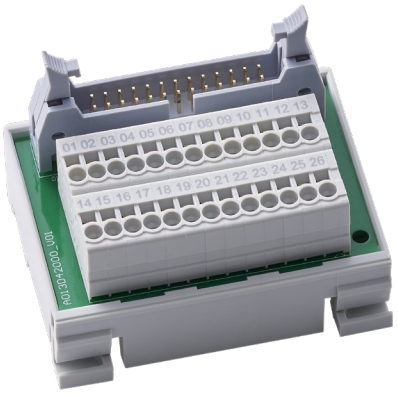
DINKLE 0241-C126

26-pin Terminal Board for ADLINK and Euresys Frame Grabbers

Features

- Field I/O wiring connection for ADLINK and Euresys series products
- Spring terminal for easy field wiring
- DIN-rail mounting



Introduction

The DINKLE 0241-C126 is a 26-channel terminal base designed specifically for Euresys Grablink,Coaxlink series, and ADLINK PCIe-CPL64V series products. The terminal board provides end users with easy I/O configuration.

Specifications

Model	DINKLE 0241-C126
I/O Wire Gauge	16 AWG. (max.); 26 AWG. (min.)
Rated Voltage / Current	24V DC / 1A
Dimensions	60.5 x 47.9 x 43.0 mm (W x D x H)
Weight	52 g

Ordering Information

- **DINKLE 0241-C126 (92-97314-0010)**
  IDC 26P to terminal block 26P terminal board
- **3m IDC 2x13P to D-sub 26P cable (30-22113-0000-A0)**
  Wire, 2x13P (2.54) to DB26P (M), L=3M
- **5m IDC 2x13P to D-sub 26P cable (30-22113-1000-A0)**
  Wire, 2x13P (2.54) to DB26P (M), L=5M



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Supported Product List

- 1622 Grablink Full
- 1626 Grablink Full XR
- 1623 Grablink DualBase
- 1628 Grablink Duo
- 1633 Coaxlink Quad G3
- 1633-LH Coaxlink Quad G3 LH
- 1637 Coaxlink Quad 3D-LLE
- PCIe-CPL64V Full
- 3603 Coaxlink Quad CXP-12
- 3620 Coaxlink Quad CXP-12 JPEG
- 3625 Coaxlink QSFP+
- 3623 Coaxlink Quad CXP-12 Value
- 1635 Coaxlink Quad G3 DF with 1636 or 3304
- 3624 Coaxlink Quad CXP-12 DF with 1636 or 3304
- 1636 InterPC C2C-Link Adapter
- 3304 HD26F I/O Adapter Cable

Pinout Information

- Grablink Full and Full XR

Pin	Signal	Usage
1	+12 V	+12 V Power output
2	DIN1+	High-speed differential input #1 – Positive pole
3	DIN2+	High-speed differential input #2 – Positive pole
4	IOUT4-	Isolated contact output #4 – Negative pole
5	IIN1+	Isolated input #1 – Positive pole
6	IIN2+	Isolated input #2 – Positive pole
7	IIN3+	Isolated input #3 – Positive pole
8	IIN4+	Isolated input #4 – Positive pole
9	+5V	+5V Power output
10	IOUT1+	Isolated contact output #1 – Positive pole
11	IOUT2+	Isolated contact output #2 – Positive pole
12	IOUT3+	Isolated contact output #3 – Positive pole
13	IOUT4+	Isolated contact output #4 – Positive pole

Pin	Signal	Usage
14	GND	Ground
15	DIN1-	High-speed differential input #1 – Negative pole
16	DIN2-	High-speed differential input #2 – Negative pole
17	GND	Ground
18	IIN1-	Isolated input #1 – Negative pole
19	IIN2-	Isolated input #2 – Negative pole
20	IIN3-	Isolated input #3 – Negative pole
21	IIN4-	Isolated input #4 – Negative pole
22	GND	Ground
23	IOUT1-	Isolated contact output #1 – Negative pole
24	IOUT2-	Isolated contact output #2 – Negative pole
25	IOUT3-	Isolated contact output #3 – Negative pole
26	GND	Ground

- Grablink DualBase

Pin	Signal	Usage
1	+12 V	+12 V Power output
2	DIN1A+	Channel A - High-speed differential input #1 – Positive pole
3	DIN2A+	Channel A - High-speed differential input #2 – Positive pole
4	IOUT1B-	Channel B -Isolated contact output #1 – Negative pole
5	IIN1A+	Channel A - Isolated input #1 – Positive pole
6	IIN2A+	Channel A - Isolated input #2 – Positive pole
7	DIN1B+	Channel B - High-speed differential input #1 – Positive pole
8	DIN2B+	Channel B - High-speed differential input #2 – Positive pole
9	+5V	+5V Power output
10	IOUT1A+	Channel A - Isolated contact output #1 – Positive pole
11	IIN1B+	Channel B - Isolated input #1 – Positive pole
12	IIN2B+	Channel B - Isolated input #2 – Positive pole
13	IOUT1B+	Channel B - Isolated contact output #1 – Positive pole

Pin	Signal	Usage
14	GND	Ground
15	DIN1A-	Channel A - High-speed differential input #1 – Negative pole
16	DIN2A-	Channel A - High-speed differential input #2 – Negative pole
17	GND	Ground
18	IIN1A-	Channel A - Isolated input #1 – Negative pole
19	IIN2A-	Channel A - Isolated input #2 – Negative pole
20	DIN1B-	Channel B - High-speed differential input #1 – Negative pole
21	DIN2B-	Channel B - High-speed differential input #2 – Negative pole
22	GND	Ground
23	IOUT1A-	Channel A - Isolated contact output #1 – Negative pole
24	IIN1B-	Channel B - Isolated input #1 – Negative pole
25	IIN2B-	Channel B - Isolated input #2 – Negative pole
26	GND	Ground

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Pinout Information

- **CXP Series & Grablink Duo**

Pin	Signal	Usage
1	+12V	+12 V Power output
2	DIN11+	High-speed differential input #11 – Positive pole
3	DIN12+	High-speed differential input #12 – Positive pole
4	GND	Ground
5	IIN11+	Isolated input #11 – Positive pole
6	IIN12+	Isolated input #12 – Positive pole
7	IIN13+	Isolated input #13 – Positive pole
8	IIN14+	Isolated input #14 – Positive pole
9	NC	Not connected
10	IOUT11+	Isolated contact output #11 – Positive pole
11	IOUT12+	Isolated contact output #12 – Positive pole
12	TTLIO11	TTL input/output #11
13	TTLIO12	TTL input/output #12

Pin	Signal	Usage
14	GND	Ground
15	DIN11-	High-speed differential input #11 – Negative pole
16	DIN12-	High-speed differential input #12 – Negative pole
17	GND	Ground
18	IIN11-	Isolated input #11 – Negative pole
19	IIN12-	Isolated input #12 – Negative pole
20	IIN13-	Isolated input #13 – Negative pole
21	IIN14-	Isolated input #14– Negative pole
22	GND	Ground
23	IOUT11-	Isolated contact output #11 – Negative pole
24	IOUT12-	Isolated contact output #12 – Negative pole
25	GND	Ground
26	GND	Ground

- **PCIe-CPL64V series**

Pin	Signal	Usage
1	NC	Floating
2	EA+	Encoder Input Pair A+
3	EB+	Encoder Input Pair B+
4	NC	Floating
5	DI0+	Digital Input Pair0+
6	DI1+	Digital Input Pair1+
7	DI2+	Digital Input Pair2+
8	DI3+	Digital Input Pair3+
9	NC	Floating
10	DO0+	Digital Output0
11	DO1+	Digital Output1
12	DO2+	Digital Output2
13	DO3+	Digital Output3

Pin	Signal	Usage
14	NC	Floating
15	EA-	Encoder Input Pair A-
16	EB-	Encoder Input Pair B-
17	NC	Floating
18	DI0-	Digital Input Pair0-
29	DI1-	Digital Input Pair1-
20	DI2-	Digital Input Pair2-
23	DI3-	Digital Input Pair3-
22	NC	Floating
23	DO0-	DO GND
24	DO1-	DO GND
25	DO2-	DO GND
26	DO3-	DO GND

Connection Diagram



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