DLAP-5200 Series

User's Manual

Fanless High Performance AI Computer



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Revision Date: July 16, 2025

Part Number: 50M-00125-2000

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Revision History

Revision	Release Date	Description of Change(s)
1.0	2025-07-16	Initial release

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Preface

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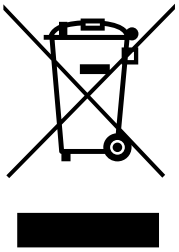
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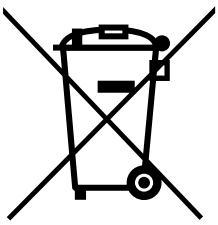
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Battery Labels (for products with battery)









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WARNING: This product can expose you to chemicals including acrylamide, arsenic, benzene, cadmium, Tris(1,3-dichloro-2-propyl)phosphate (TDCPP), 1,4-Dioxane, formaldehyde, lead, DEHP, styrene, DINP, BBP, PVC, and vinyl materials, which are known to the State of California to cause cancer, and acrylamide, benzene, cadmium, lead, mercury, phthalates, toluene, DEHP, DIDP, DnHP, DBP, BBP, PVC, and vinyl materials, which are known to the State of California to cause birth defects or other reproductive harm. For more information go to www.P65Warnings.ca.gov.

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Conventions

Take note of the following conventions used throughout this manual to make sure that users perform certain tasks and instructions properly.



NOTE:

Additional information, aids, and tips that help users perform tasks.



CAUTION:

Information to prevent ***minor*** physical injury, component damage, data loss, and/or program corruption when trying to complete a task.



WARNING:

Information to prevent ***serious*** physical injury, component damage, data loss, and/or program corruption when trying to complete a specific task.

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Table of Contents

Preface ..... **iii**

List of Figures ..... **vii**

List of Tables ..... **vii**

1 Introduction **9**

1.1 Overview 9

1.2 Features 9

1.3 Packing List 9

1.4 Optional Accessories 9

2 Specifications **11**

2.1 Product Specifications 11

2.2 Functional Block Diagram 12

2.3 Display Options 12

3 Mechanical Layout **13**

3.1 Mechanical Dimensions 13

3.2 Front/Side Panel I/O Locations 14

3.3 Internal I/O Connectors 15

4 Connectors and Jumpers **19**

4.1 Front/Side I/O Connectors and Jumpers 19

4.2 Internal I/O Connectors and Jumpers 22

5 Getting Started **25**

5.1 Package Contents 25

5.2 Attach DC Power Connector 25

5.3 Mounting the DLAP-5200 Series 26

5.4 Driver Installation 29

6 Appendix A Power **31**

6.1 Power Consumption Reference 31

7 Appendix B BIOS Setup **33**

7.1 Main 33

7.2 Advanced 34

7.3 Chipset 44

7.4 Security 51

7.5 Boot 53

7.6 Save & Exit 54

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7.7 MEBx 55
8 Appendix C Watchdog Timer (WDT) Function Library 57
8.1 WDT with API/Windows.....57
8.2 WDT with DOS/Linux 59
9 Appendix D Digital Input/Output Function Library 61
Safety Instructions.....62
Getting Services.....64

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List of Figures

Figure 1: Functional block diagram....12
Figure 2: DLAP-5200 Series top view....13
Figure 3: DLAP-5200 Series front view....14
Figure 4: Front panel I/O....14
Figure 5: Mainboard top connectors....15
Figure 6: Mainboard bottom connectors....16

List of Tables

Table 1: Mainboard top connector legend....15
Table 2: Mainboard bottom connector legend....17

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1 Introduction

1.1 Overview

The DLAP-5200 industrial AI computers are powered by 13th/12th Gen Intel® Core™ i9/i7/i5/i3 processors. Featuring the Intel R680E chipset and supporting up to 65W, these computers can also incorporate GPU cards in a rugged package suitable for AI inferencing at the edge. They can be used for, but are not limited to, smart manufacturing, semiconductor equipment, and warehouse applications.

1.2 Features

- 13th/12th Generation Intel® Core™ i9/i7/i5/i3 FCLGA1700 processor and R680E chipset
- 4x DDR5 SODIMM socket, supporting up to 128GB DDR5 3600 SODIMM module
- 3x DisplayPort, 2x HDMI, supporting 4 independent displays
- 6x External USB ports (4x USB 3.2 Gen 2, 2x USB 3.2 Gen 1)
- 6x Intel 2.5Gbe i225 LAN ports with TSN support
- 2 software-programmable RS-232/422/485
- Onboard SATA 6Gb/s port for 2.5” SSD installation
- Supports MXM 3.1 Type A/B form factor GPU module

1.3 Packing List

Before unpacking, check the shipping carton for any damage. If the shipping carton and/or contents are damaged, inform your dealer immediately. Retain the shipping carton and packing mate- rials for inspection. Obtain authorization from your dealer before returning any product to ADLINK. Ensure that the following items are included in the package.

- DLAP-5200 Series Fanless Embedded Computer
- Accessory Box
    - DC power jack
    - Wall-mount brackets
    - Screw pack

1.4 Optional Accessories

- AC/DC Adapter
    - 280W (P/N: 31-62162-1010-A0)

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2 Specifications

2.1 Product Specifications

Model		DLAP-5200-3DP	DLAP-5200-6DP
System	CPU	12th/13th Gen Intel® Core™ processors	12th/13th Gen Intel® Core™ processors
	Chipset	R680E	R680E
	Memory	Max. 128GB, DDR5 4x SO-DIMMs, up to 4800MHz	Max. 128GB, DDR5 4x SO-DIMMs, up to 4800MHz
	Storage	2x SATA IIIx (6Gb/s) for 2.5-inch SSD	2x SATA IIIx (6Gb/s) for 2.5-inch SSD
	MXM module	Intel Arc A370M NVIDIA A1000, A2000, A4500, RTX 3000	Intel Arc A370M NVIDIA A1000, A2000, A4500, RTX 3000
	Operating System	Windows 10/11 IoT Enterprise 64-bit Ubuntu 22.04	Windows 10/11 IoT Enterprise 64-bit Ubuntu 22.04
	TPM	TPM 2.0 on board	TPM 2.0 on board
	LED indicator	1 storage, 1 WDT, 1 diagnostic and 3 user-defined	1 storage, 1 WDT, 1 diagnostic and 3 user-defined
I/O interfaces	Display Ports	2x HDMI 1.4b 3x DisplayPort 1.4 (1x from MXM module)	2x HDMI 1.4b 6x DisplayPort 1.4 (4x from MXM module)
	Ethernet Ports	6x 2.5GbE with TSN supported (i225)	3x 2.5GbE with TSN supported (i225)
	USB Ports	4x USB 3.2 Gen2 (Type A) 2x USB 3.1 Gen2 (Type A)	4x USB 3.2 Gen2 (Type A) 2x USB 3.1 Gen2 (Type A)
	Serial Ports	2x RS-232/422/485	2x RS-232/422/485 1x RS-232
	Expansion Slots	1x M.2 2280 M Key 1x M.2 2230 E Key	1x M.2 2280 M Key 1x M.2 2230 E Key
Mechanical	Dimension (W X D X H)	125mm x 240mm x 210mm	125mm x 240mm x 210mm
	Weight	6.8 kg	6.8 kg
	Mounting	Wall mount	Wall mount
	Interface	2-pin phoenix	2-pin phoenix
Power	Power Supply	12/24 VDC, AC-DC 280W adapter (optional)	12/24 VDC, AC-DC 280W adapter (optional)
Environment	Operating Temperature	-20 to 50 °C (35W CPU) -20 to 40 °C (65W CPU)	-20 to 50 °C (35W CPU) -20 to 40 °C (65W CPU)
	Storage Temperature	-40°C to 85°C (-40°F to 185°F)	-40°C to 85°C (-40°F to 185°F)
	Humidity	~95% @ 40°C (Non-condensing)	~95% @ 40°C (Non-condensing)
	Vibration Operating	3Grms, random, 5-500Hz, 3 axes (w/SSD)	3Grms, random, 5-500Hz, 3 axes (w/SSD)
	Shock Operating	30G, half sine 11ms duration (w/SSD)	30G, half sine 11ms duration (w/SSD)
	ESD	Contact +/-4KV, Air +/-8KV	Contact +/-4KV, Air +/-8KV
	EMC	CE & FCC Class A	CE & FCC Class A
	Safety	UL/CB	UL/CB

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2.2 Functional Block Diagram

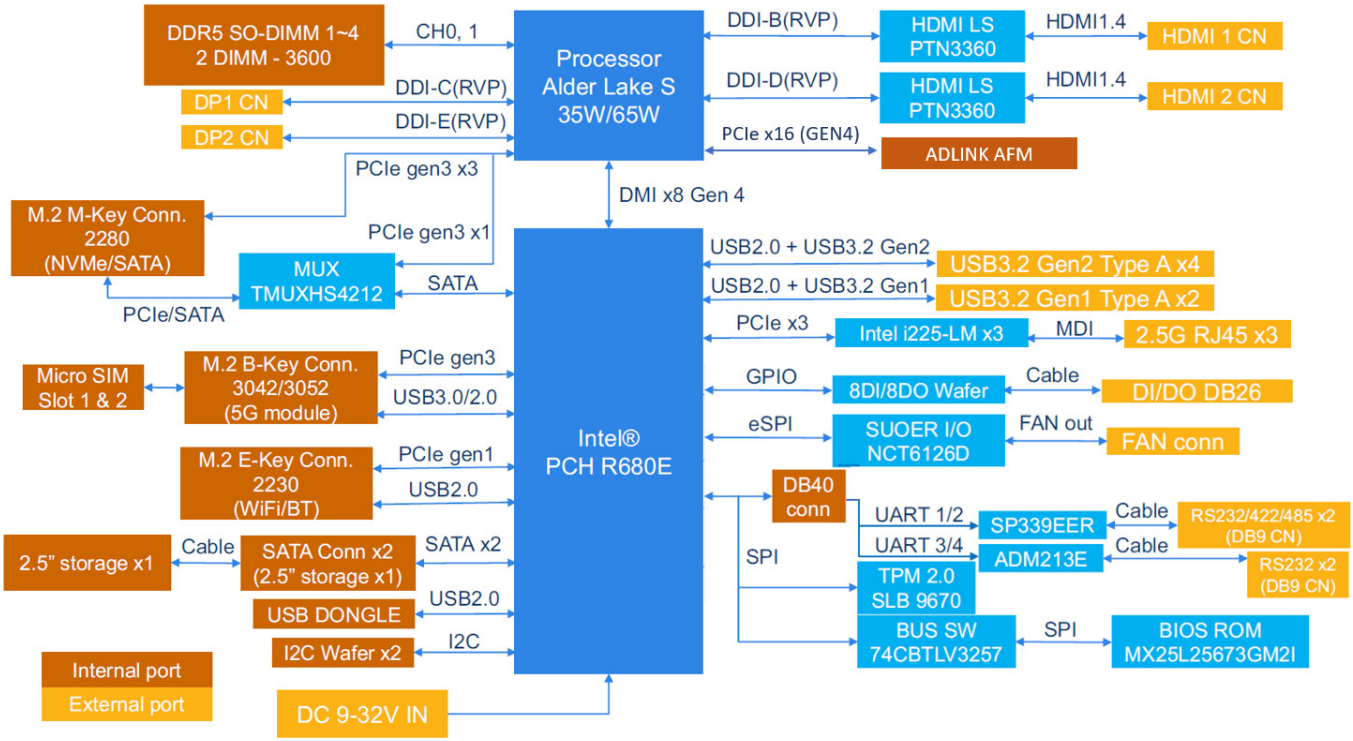


Figure 1: Functional block diagram

2.3 Display Options

With computing and graphics performance enhancements from its 13th/12th Generation Intel processors, the DLAP-5200 Series controller can support four independent displays from the mainboard, with configurations as follows.

	Port	Resolution
Display Option 1	DisplayPort1	4096x2304@60Hz
Display Option 2	DisplayPort2	4096x2304@60Hz
Display Option 3	HDMI1	3840x2160 @ 60Hz
Display Option 4	HDMI2	3840x2160 @ 60Hz

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3 Mechanical Layout

3.1 Mechanical Dimensions

NOTE:

All dimensions shown are in millimeters.

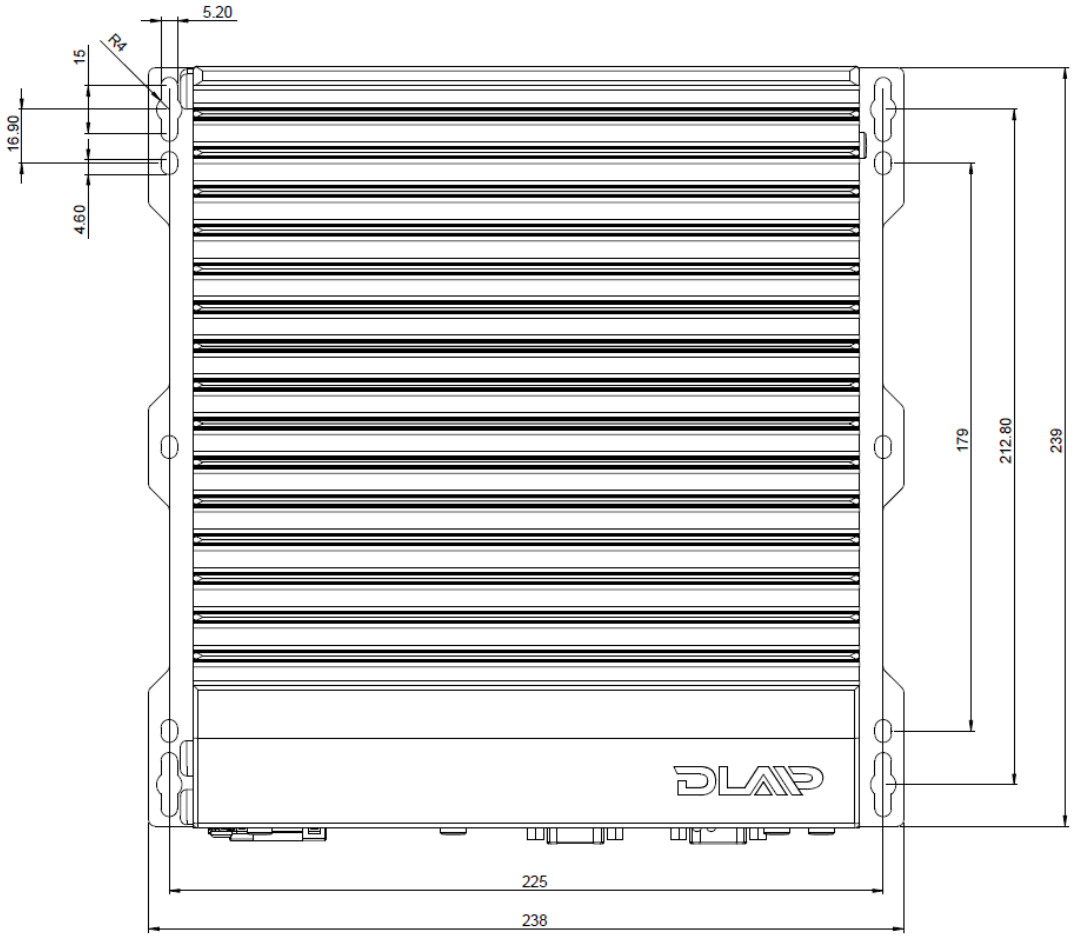


Figure 2: DLAP-5200 Series top view

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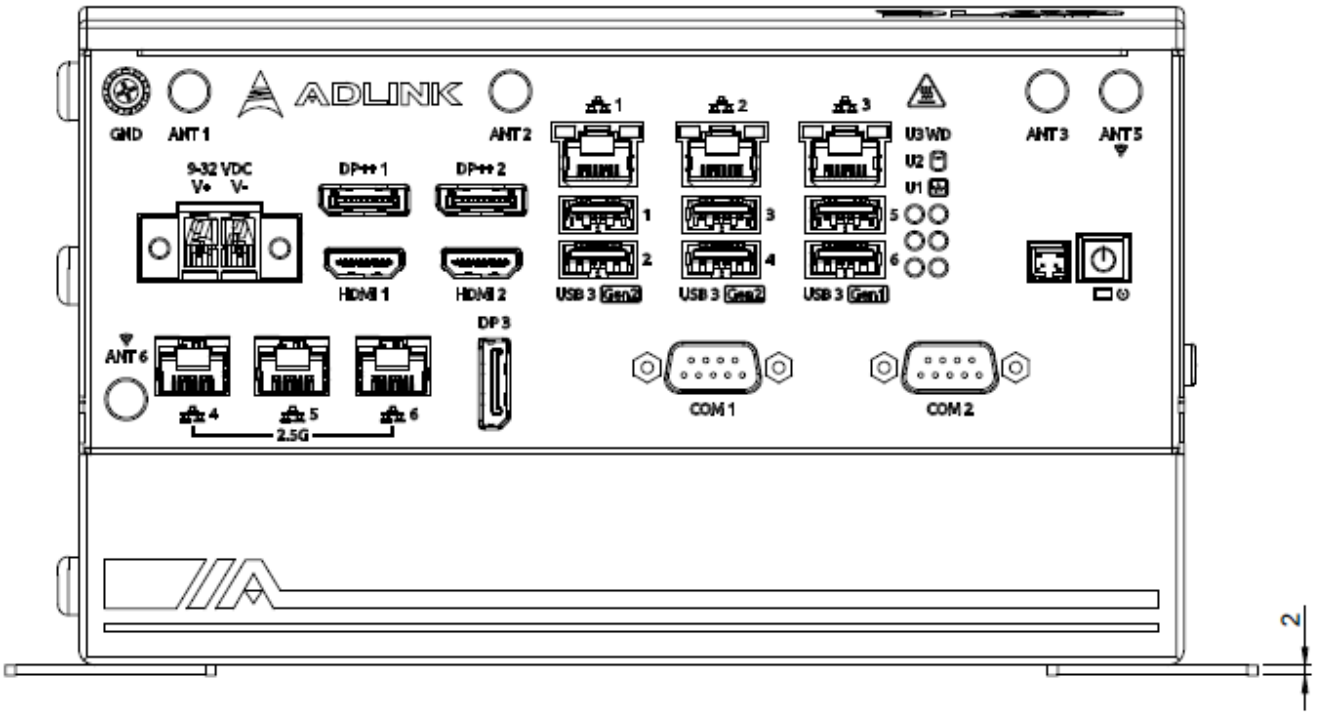


Figure 3: DLAP-5200 Series front view

3.2 Front/Side Panel I/O Locations

The DLAP-5200 Series provides the following front and side panel access features.

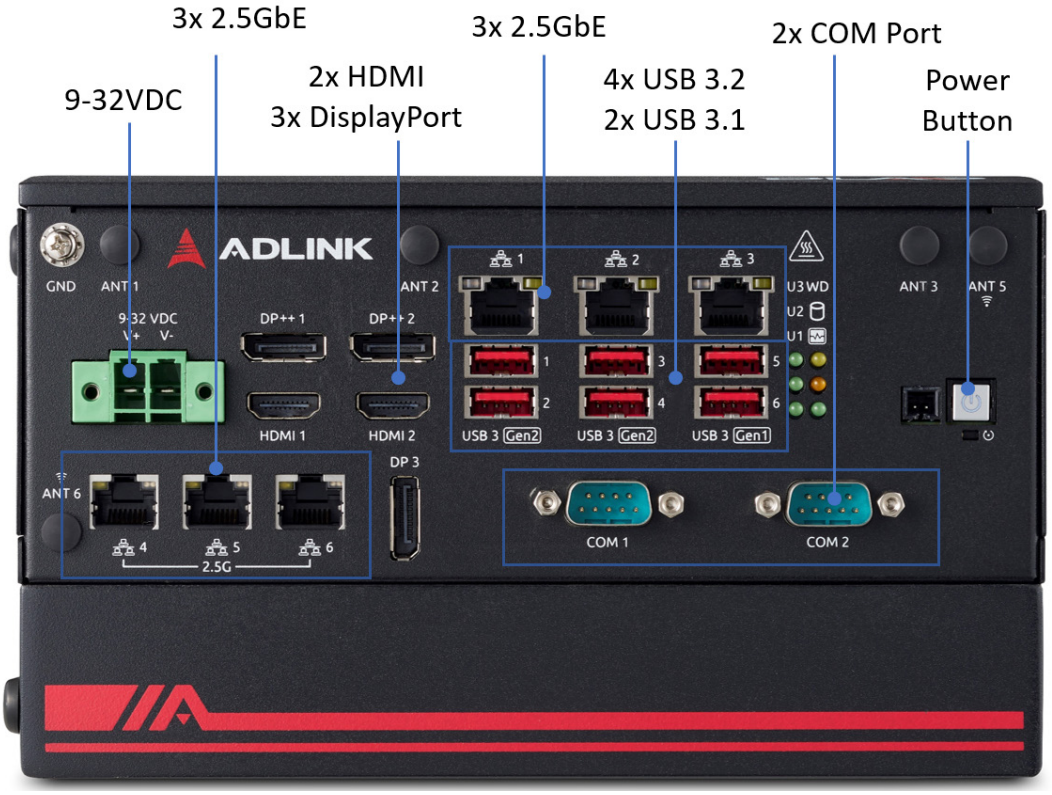


Figure 4: Front panel I/O

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3.3 Internal I/O Connectors

3.3.1 Mainboard Top/Bottom Connector Locations

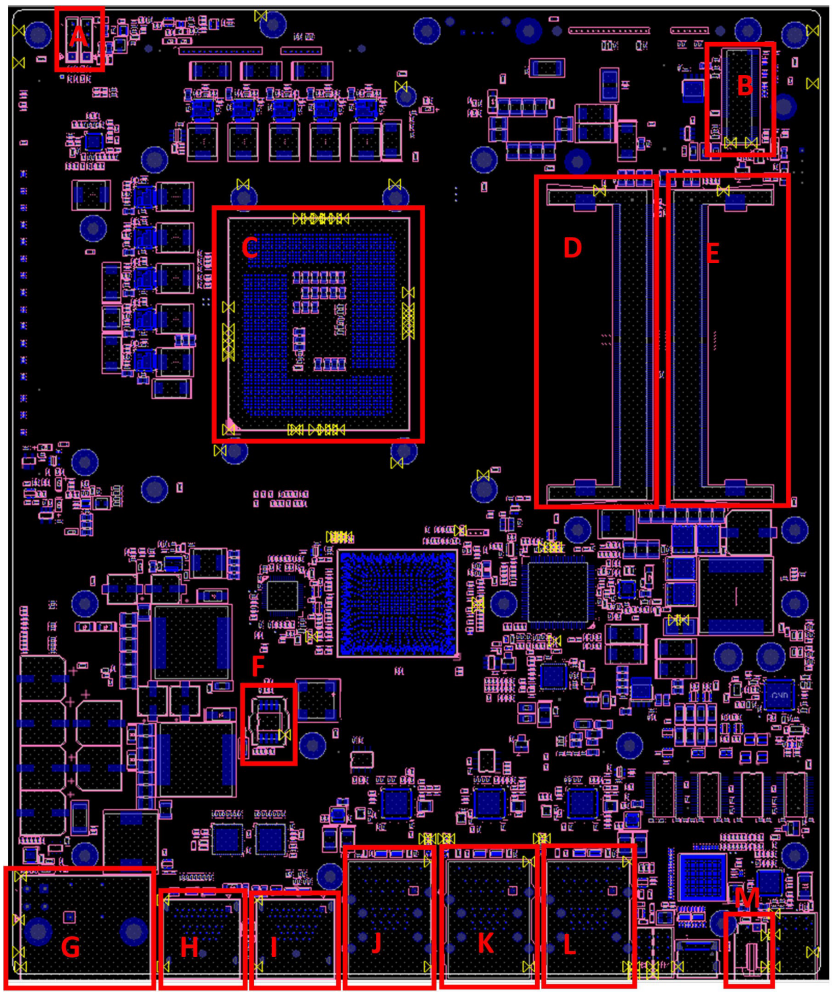


Figure 5: Mainboard top connectors

Table 1: Mainboard top connector legend

A	Power FW Update Connector (Debug)
B	M.2 M Key
C	CPU Socket
D	DDR5 SODIMM1
E	DDR5 SODIMM3
F	Flash ROM Socket (Debug)
G	DCIN Connector
H	HDMI + DP
I	HDMI + DP
J	RJ45 + USB 3.0 x2
K	RJ45 + USB 3.0 x2
L	RJ45 + USB 3.0 x2
M	External Power Connector

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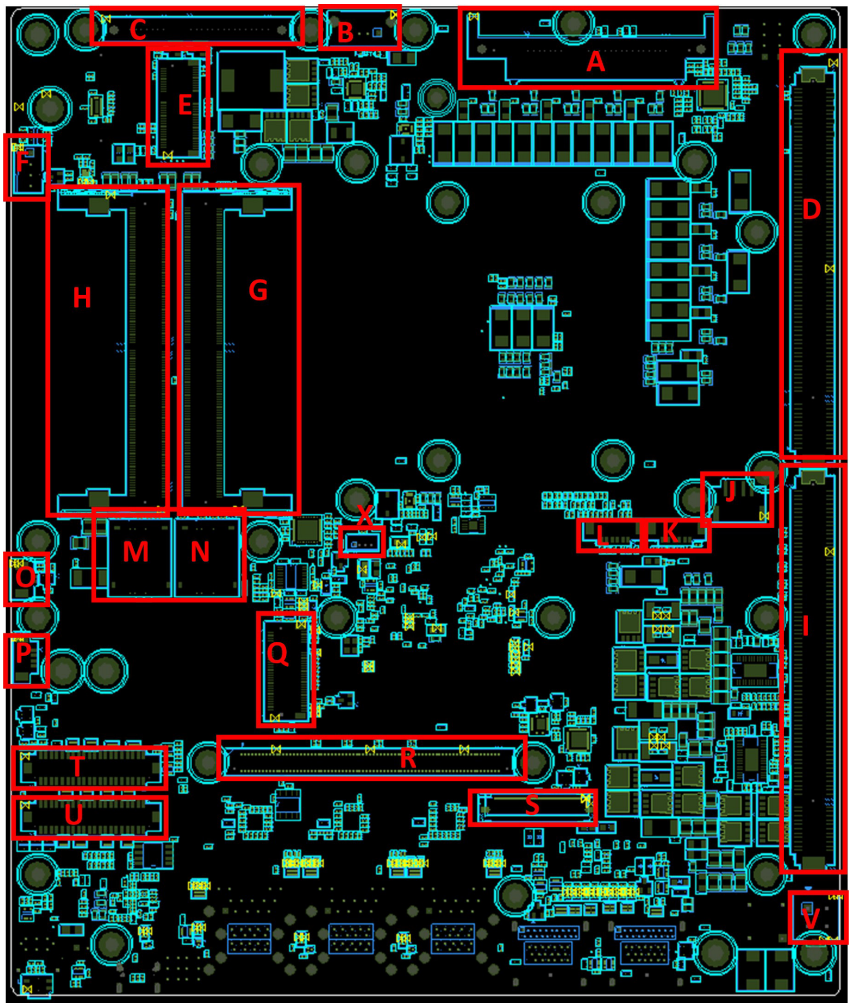


Figure 6: Mainboard bottom connectors

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Table 2: Mainboard bottom connector legend

A	SATA connector for 2.5” SSD
B	USB 2.0 dongle
C	SATA connector for 2.5” SSD
D	PCIE GEN5 slot for BP Board
E	M.2 E Key
F	System Fan Connector
G	DDR5 SODIMM2
H	DDR5 SODIMM4
I	PCIE GEN4 + Power slot for BP Board
J	External Power Connector for AFM Board
K	I2C Wafer 2 (I2C1)
L	I2C Wafer 1 (I2C0)
M	SIM connector for M.2
N	SIM connector for M.2
O	RTC Battery Connector
P	Wafer for Speaker Out
Q	M.2 B Key
R	B2B Connector for AFM Board
S	DB40 Connector (Debug)
T	Wafer for COM5, 6 + DIDO + MIC + HP
U	Wafer for COM1, 2, 3, 4
V	External Power Connector for BP Board
X	External Power/Reset button

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4 Connectors and Jumpers

4.1 Front/Side I/O Connectors and Jumpers

4.1.1 Antenna Ports

Antenna ports ANT1-4 support 5G (PN: 30-60192-0000-A0), while ports ANT5-6 support Wi-Fi 2.4GHz/5GHz (PN: 30-60125-1000- B0).

4.1.2 Power Button

The power button is a non-latched push button with a blue LED indicator.

System Status	Power LED
System turns on	Light ON
Suspend (S3)	Blinking
Hibernation (S4)	Blinking
Power OFF (S5)	Light OFF

4.1.3 LED Indicators

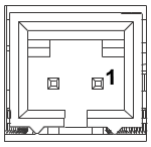
In addition to the power button LED, six LEDs on the front panel indicate the following.

LED Indicator	Location	Color	Description
Watchdog (WD)	Top Right	Yellow	Indicates watchdog timer status. When watchdog timer starts, the LED will be on until system boot up success. When system does not boot up and the timer is expired, the system will automatically reboot.
Hard disk drive (HD)	Middle Right	Orange	When blinking, indicates the SATA HDD is active.
Diagnostic (DG)	Bottom Right	Green	If lit continuously, indicates no physical storage is connected. If blinking, indicates no memory is installed on either SODIMM socket.
User define (U1)	Top Left	Green	User define LED
User define (U2)	Middle Left	Green	User define LED
User define (U3)	Bottom Left	Green	User define LED

4.1.4 External Power Button Connector

The DLAP-5200 Series provides an external power button connector to remotely power on/off the platform.

Pin	Signal
1	PWR_BTN-L
2	GND



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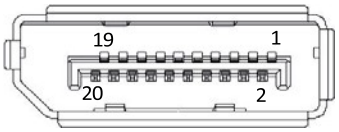
4.1.5 Reset Button

The reset button executes a hard reset for the DLAP-5200 Series.

4.1.6 DisplayPort Connectors

Two DisplayPort connectors on the front panel can connect to VGA, DVI, HDMI and DisplayPort monitors via DisplayPort to VGA adapter cable, DisplayPort to DVI adapter cable, or DisplayPort to HDMI adapter cable and DisplayPort cable.

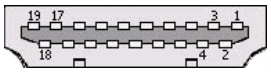
Pin	Signal	Pin	Signal
1	CN_DDPx0+	11	GND
2	GND	12	CN_DDPx3-
3	CN_DDPx0-	13	CN_DDPx_AUX_SEL
4	CN_DDPx1+	14	CN_DDPx_CONFIG2
5	GND	15	CN_DDPx_AUX+
6	CN_DDPx1-	16	GND
7	CN_DDPx2+	17	CN_DDPx_AUX-
8	GND	18	CN_DDPx_HPD
9	CN_DDPx2-	19	GND
10	CN_DDPx3+	20	+V3.3_DDPx_PWR_CN



4.1.7 HDMI Connector

The DLAP-5200 Series provides one HDMI connector for connection to an external monitor.

Pin #	Signal	Pin #	Signal
1	CON_HDMI_TX2_P	2	GND
3	CON_HDMI_TX2_N	4	CON_HDMI_TX1_P
5	GND	6	CON_HDMI_TX1_N
7	CON_HDMI_TX0_P	8	GND
9	CON_HDMI_TX0_N	10	CON_HDMI_CLK_P
11	GND	12	CON_HDMI_CLK_N
13	N/C	14	N/C
15	DVI_SCL_SINK	16	DVI_SDA_SINK
17	GND	18	P_+5V0_HDMI_CN
19	CON_HDMI_HPD_5V0		



4.1.8 USB 3.2 Ports

The DLAP-5200 Series provides four USB 3.2 Gen 2 and two USB 3.2 Gen 1 ports supporting Type-A USB 3.2 connection on the front panel. All USB 3.2 ports are compatible with SuperSpeed Gen2, high-speed, full-speed, and low-speed USB devices. USB1 and USB2 Provide 1.6A current. USB3,4 share 1.6A current, USB5,6 share 1.6A current. The full 1.6A current is available if only one port is used.

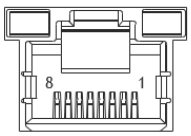
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4.1.9 Ethernet Ports

6x Ethernet ports on the front panel support Intel I225- LM supports 2.5Gps.

Pin #	10BASE-T/100BASE-TX	1000BASE-T
1	TX+	LAN_TX0+
2	TX-	LAN_TX0-
3	RX+	LAN_TX1+
4	—	LAN_TX2+
5	—	LAN_TX2-
6	RX-	LAN_TX1-
7	—	LAN_TX3+
8	—	LAN_TX3-

Speed LED Green/Orange	Active/Link Yellow



Active/Link LED Indicators

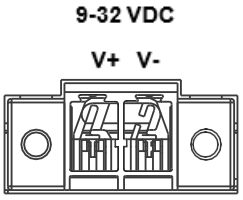
LED Color	Status	Description
Yellow	OFF	Ethernet port is disconnected.
	ON	Ethernet port is connected with no activity.
	Flashing	Ethernet port is connected and active.

Speed LED Indicators

LED Color	Status	Description
Green/Orange	OFF	10/100 Mbps
	Green	1000 Mbps
	Orange	2500 Mbps

4.1.10 DC Power Input

Pin	Signal
1	V+ (DC_IN)
2	V- (DGND)



Refer to section 5.2: Attach DC Power Connector to install the DC Power Connector to the DC Power Input.

Use an approved power source as certified by IEC or UL. If you need an adaptor, optional accessories, or further assistance, con- tact ADLINK for further information. The maximum operational altitude is 2000 m with an output that meets LPS and SELV (ES1) circuits.

Power Source Rating

	Voltage	Current
DC Power Source	9 to 32V DC	16A to 4.2A min.
AC-to-DC Adapter	24V DC	6.25A min.

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WARNING:

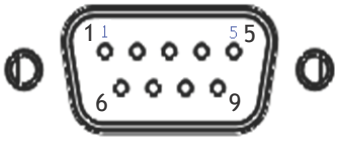
Before providing DC power, ensure the voltage and polarity pro- vided are compatible with the DC input. Improper input voltage and/or polarity can cause system damage.

AVERTISSEMENT: Avant de fournir de l'énergie DC à cet appar- eil, veuillez vous assurer que la tension et la polarité fournies sont compatibles avec l'entrée DC. Une tension d'entrée et/ou polarité incorrectes peuvent causer des dommages au système.

4.1.11 COM Port Connectors

The DLAP-5200 Series provides four COM ports through D-sub 9- pin connectors. The COM1 & COM2 ports support RS-232/422/ 485 modes by BIOS setting, while COM3 and COM4 support only RS-232.

Pin	Signal Name		
	RS-232	RS-422	RS-485
1	DCD#	TXD422-	485DATA-
2	RXD	TXD422+	485DATA+
3	TXD	RXD422+	N/S
4	DTR#	RXD422-	N/S
5	GND	N/S	N/S
6	DSR#	N/S	N/S
7	RTS#	N/S	N/S
8	CTS#	N/S	N/S
9	RI#	N/S	N/S

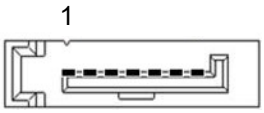


4.2 Internal I/O Connectors and Jumpers

4.2.1 SATA Connector

The SATA connector supports transfer up to 6.0Gb/s (600MB/s).

Pin	Signal
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND

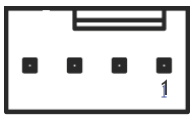


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4.2.2 12V Fan Connector

DC 12V fan module power supply is provided through the connector, to which the optional fan module connects when installed in the chassis.

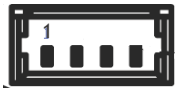
Pin	Signal
1	FAN_GND
2	12V IN PWR
3	FAN TACH
4	FAN PWM



4.2.3 External Power/Reset Header

An internal header is provided for the Power and Reset buttons.

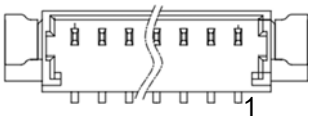
Pin	Signal
1	PWR_BTN-L
2	GND
3	GND
4	RESET_BTN-L



4.2.4 I2C Connector

There are two I2C connectors supporting 3.3v or 5v.

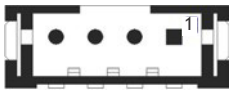
Pin	Signal (I2C0)	Signal (I2C1)
1	P_+5V0_S	P_+5V0_S
2	P_+3V3_S	P_+3V3_S
3	PCH_I2C0_SDA_S	PCH_I2C1_SDA_S
4	PCH_I2C0_SCL_S	PCH_I2C1_SCL_S
5	I2C0_GPIO	I2C1_GPIO
6	GND	GND



4.2.5 12V Power for Additional AFM MXM Carrier Board

One 12V power pin header provides additional AFM MXM carrier board power for an MXM card, if used.

Pin	Signal	Pin	Signal
1	P_+12V_B	3	GND
2	P_+12V_B	4	GND



NOTE:

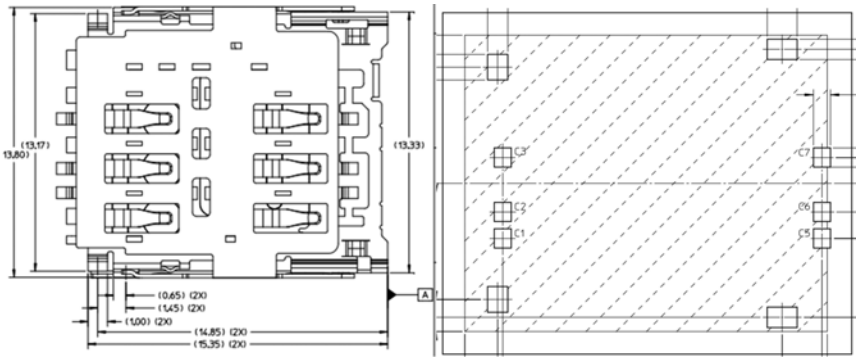
For additional information regarding the AFM MXM carrier board, please contact your ADLINK representative.

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4.2.6 Micro-SIM Slot

The Micro-SIM slot supports an M.2 B Key.

Pin	Signal
1	SIM PWR
2	REST#
3	CLK
4	GND
5	VPP
6	DATA



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5 Getting Started

5.1 Package Contents

Before unpacking, check the shipping carton for any damage. If the shipping carton and/or contents are damaged, inform your dealer immediately. Retain the shipping carton and packing mate- rials for inspection. Obtain authorization from your dealer before returning any product to ADLINK. Ensure that the following items are included in the package.

- DLAP-5200 controller
- Accessory pack
- Screw pack for wall-mounting and SSD installation

5.2 Attach DC Power Connector

Locate the DC power connector shown below that is included in the Accessory Box. Insert it into the DC Power Input “A” and secure it to the chassis using the captive screws.



Connect a DC power source as described in **Section 4.1.10: DC Power Input** to the inputs of the DC power connector, making sure to use the correct input voltage and polarity.



WARNING:

Before providing DC power, ensure the voltage and polarity provided are compatible with the DC input. Improper input voltage and/or polarity can be responsible for system damage.

AVERTISSEMENT: Avant de connecter à une source de courant continu, veuillez vous assurer de la polarité de la tension confor- mément à l'entrée CC du PC. Une tension et/ou une polarité incorrectes peuvent causer des dommages irréversibles sur le système.

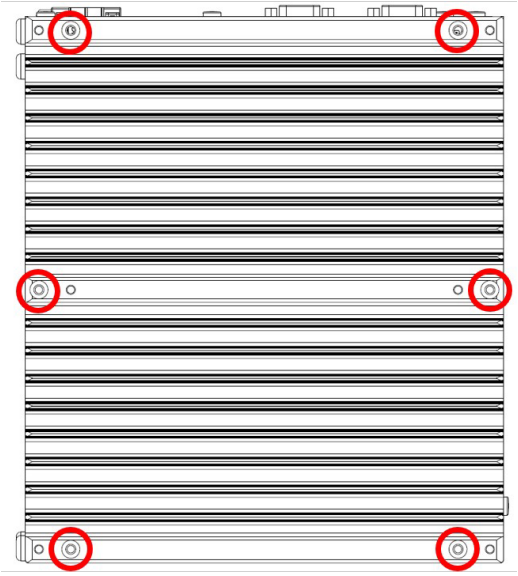
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5.3 Mounting the DLAP-5200 Series

5.3.1 Install a Solid-State Drive (SSD)

Follow these steps to install a solid-state drive (SSD).

1. Use a Phillips-head screwdriver to remove the bottom cover foot pads and screws as shown.

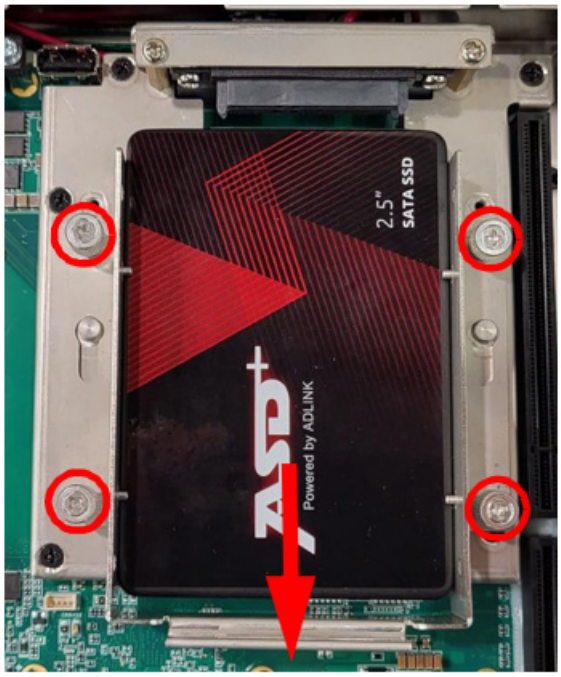


2. Lift off the rear heatsink to remove it.



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3. Loosen the 4 set screws, and then pull the SSD tray to remove it.

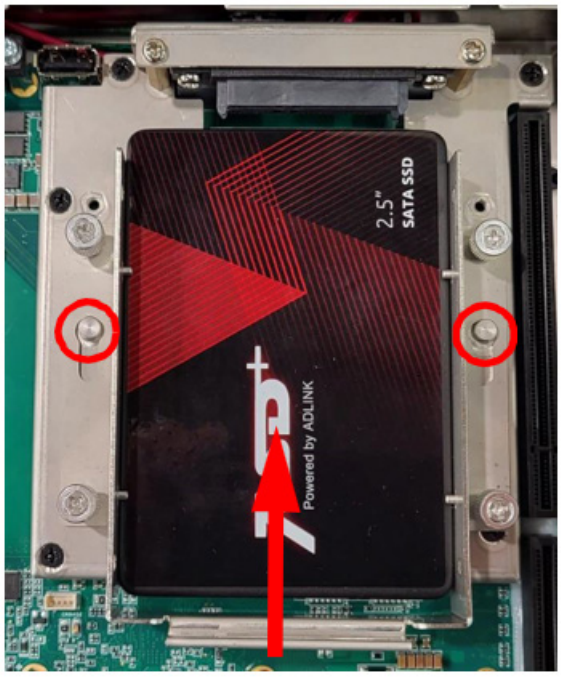


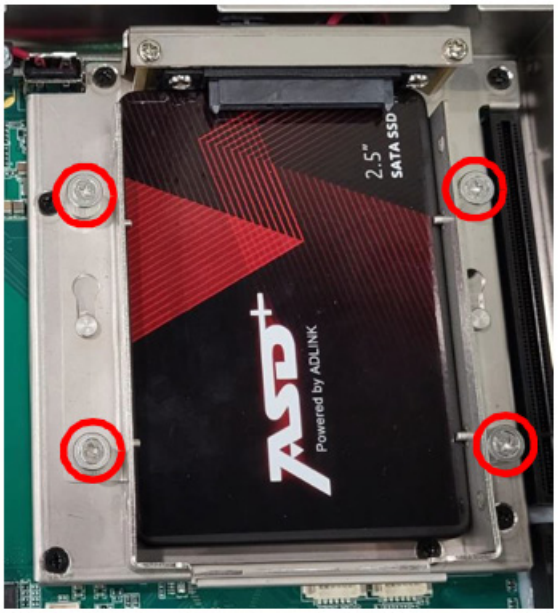
4. Use the included M3 screws to attach a 2.5” SSD to the tray.



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5. Align the SSD bracket using the guide pins, and then gently push the drive assembly into the SATA connector on the PCB. Tighten the 4 setscrews to lock the SSD into place.

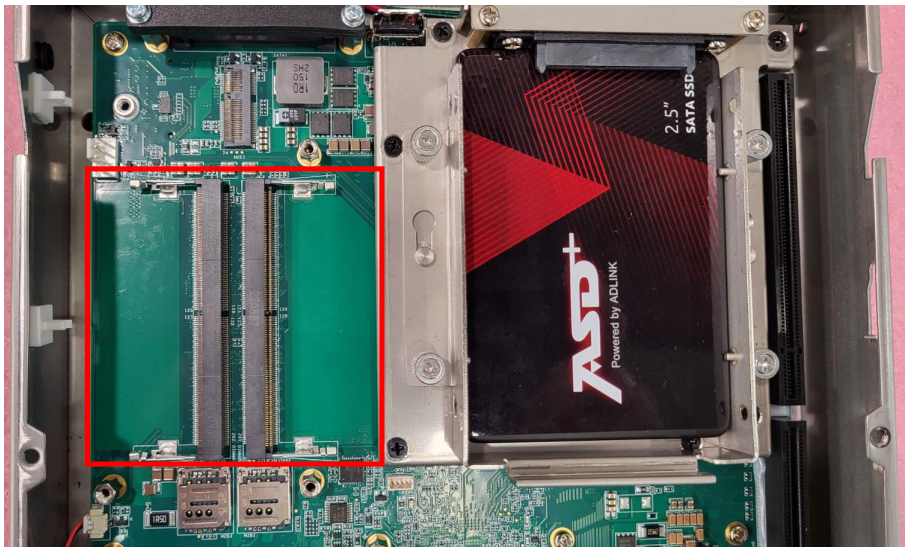




5.3.2 Install SODIMM Memory

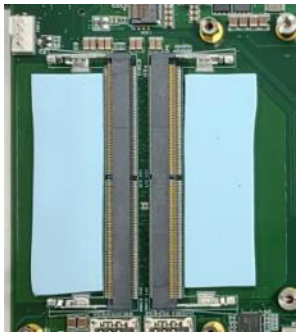
Follow these steps to install SODIMM memory.

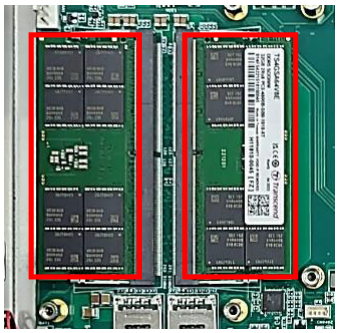
1. Make sure the SODIMM connectors are clear of any thermal pads.



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2. Attach the included thermal pads to the main board under the location of the SODIMM modules. Make sure the thermal pads DO NOT contact the connector pins. Install the SODIMM modules.

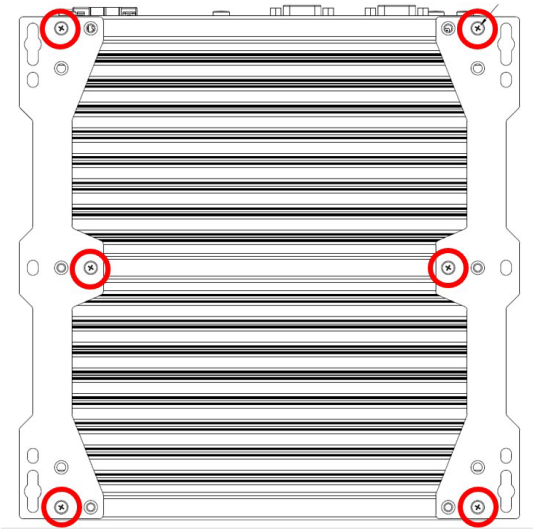




5.3.3 Install the wall-Mount Brackets

Follow these steps to install the wall-mount bracket.

1. Use the 6 M4 screws shipped with the controller to attach the 2 included wall mount brackets to the chassis as shown.



NOTE:

Refer to Chapter 3, Mechanical Layout, for dimensions on wall mounting hole placement.

5.4 Driver Installation

Download the drivers for your system from the product page at https://www.adlinktech.com/products/Industrial_PCs_Fanless_Embedded_PCs/IPCSystems/DLAP-5200_Series

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6 Appendix A Power



NOTE:

The information in this Appendix is for power budget planning and design purposes only. Actual power consumption may differ based on final application.

6.1 Power Consumption Reference

Power consumption as follows is based on lab data in which 24V DC is applied and current is measured by the DC power supply. The power consumption (W) is calculated as the product of applied voltage (V) and the current (A).

Platforms tested for this data have available external I/O interfaces and are attached to supported devices such as DP and HDMI monitors, keyboard/mouse, USB dummy load, COM loopback, with an internal hard disk drive installed.

Information is presented for reference only. Actual power consumption will vary with different attached devices and operating system.

System	Power Off	System Idle	Processor Full Load	System Full Load	Recommended Power Supply
DLAP-5209 Intel Core i9-12900E	3.7W	59.4W	101W	147W	280W



NOTE:

- Sufficient power supply for the entire system is required to meet these specifications. At least 100W at 24V input is recommended.
- Heat generated by add-on PCI/PCIe adapters affects thermal stability. Additional heat dissipation is required when the system operates at high temperatures or in harsh environments with add-on adapters.

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7 Appendix B BIOS Setup

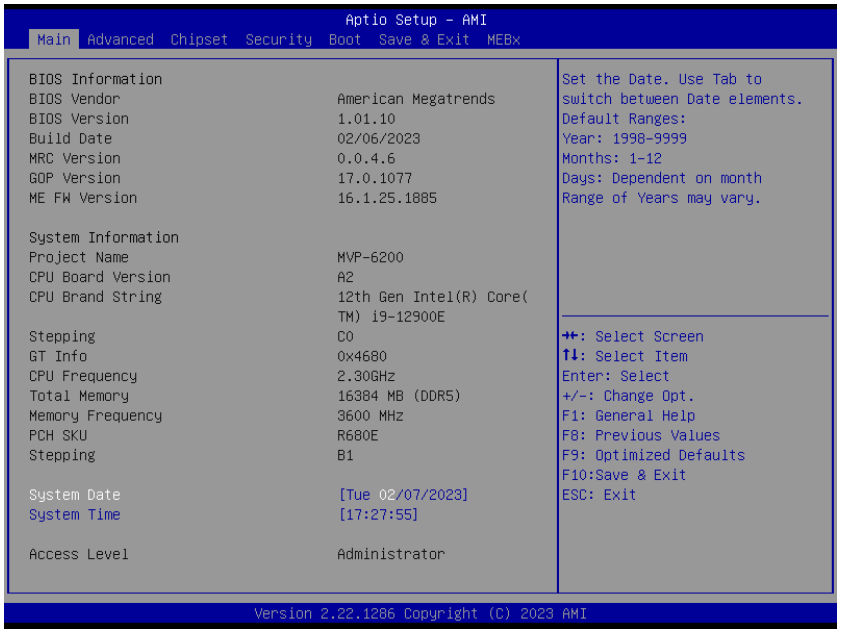
The Basic Input/Output System (BIOS) is a program that provides a basic level of communication between the processor and peripherals. In addition, the BIOS also contains codes for various advanced features applied to the DLAP-5200. The BIOS Setup program includes menus for configuring settings and enabling features of the DLAP-5200. Most users do not need to use the BIOS Setup program, as the DLAP-5200 ships with default settings that work well for most configurations.

Enter BIOS Setup by selecting DEL when the system is powered on the POST (Power On Self Test) message is displayed. The DLAP-5200 controller supports one-time Boot Menu allowing selection of boot device. Enter the Boot Menu by selecting F7 at POST.

NOTE:

- BIOS options listed are for reference only.
- Different configurations can affect BIOS behavior.
- Displayed material may reflect only the BIOS version corresponding to initial release and may differ from that of the purchased motherboard.
- Users are welcome to download the latest BIOS version from our official website.

7.1 Main



Contains basic system information for the DLAP-5200.

WARNING:

Changing BIOS settings may lead to incorrect controller behavior and possible inability to boot.

BIOS Information

Shows current system BIOS Vendor, BIOS Version, Build Date, MRC Version, GOP Version and ME FW Version.

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System Information

Shows current system Project Name, CPU Board Version, CPU Branding String, CPU Stepping, GT Info, CPU Frequency, Total Memory, Memory Frequency, PCH SKU and Stepping.

System Time/System Date

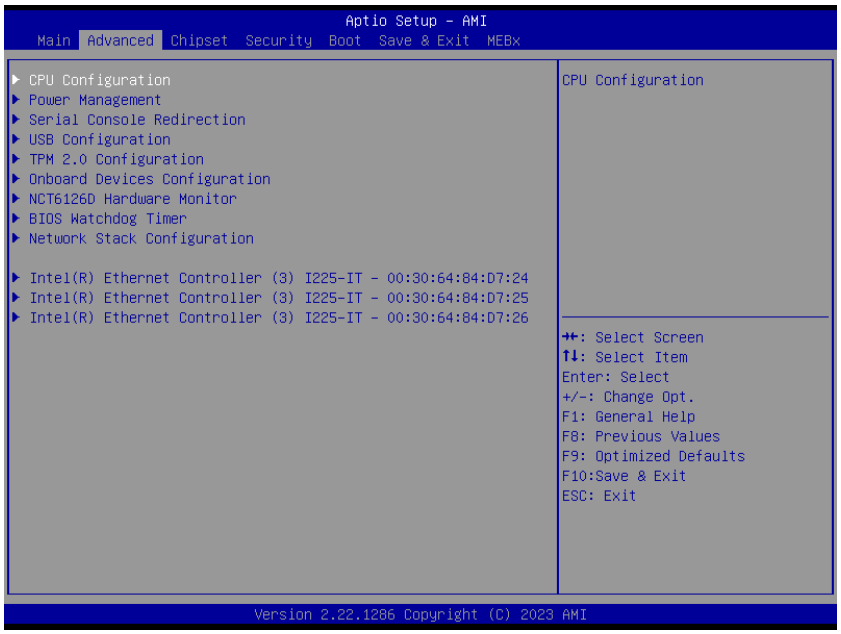
Allows adjustment of system time and date, as follows.

1. Highlight System Time or System Date using the up and down <Arrow> keys.
2. Enter new values using the keyboard and select <Enter>.
3. Select < Tab > to move between fields.

NOTE:

The date must be entered in MM/DD/YY format, and the time in HH:MM:SS. The time is in 24-hour format. For example, 5:30 A.M. appears as 05:30:00, and 5:30 P.M. as 17:30:00.

7.2 Advanced



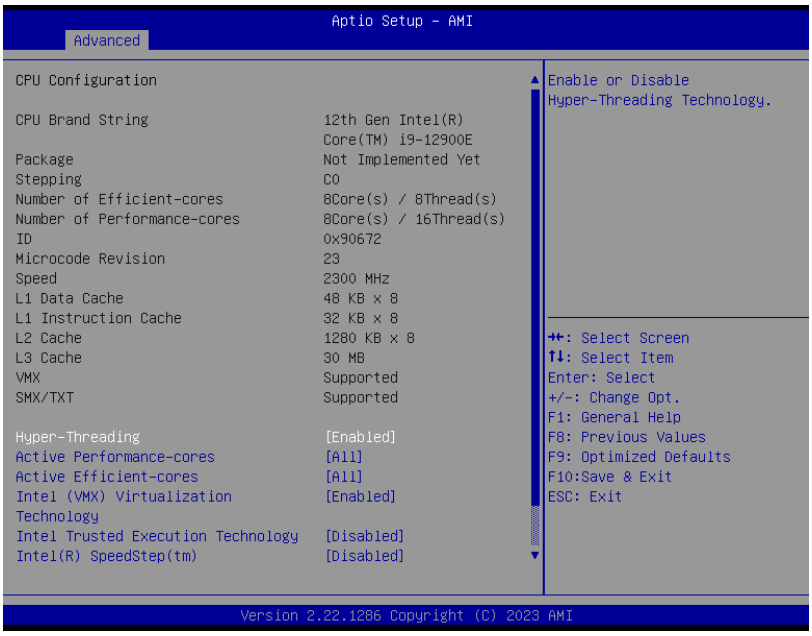
CAUTION:

Setting incorrect or conflicting values in Advanced BIOS Setup may cause system malfunction.

Accesses advanced options of the DLAP-5200.

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7.2.1 CPU Configuration



Hyper-Threading

Enabled for Windows and Linux (optimized for Hyper-Threading Technology) and Disabled for other OS (not optimized for Hyper- Threading Technology). When Disabled only one thread per enabled core is enabled.

Active Performance-Cores

Number of P-cores to enable in each processor package. Note: Number of Cores and E-cores are looked at together. When both are {0,0}, Pcode will enable all cores.

Active Efficient-Cores

Number of E-cores to enable in each processor package. Note: Number of Cores and E-cores are looked at together. When both are {0,0}, Pcode will enable all cores.

Intel (VMX) Virtualization Technology

When enabled, a VMM can utilize the additional hardware capabil- ities provided by Vanderpool Technology.

Intel Trusted Execution Technology

Enables utilization of additional hardware capabilities provided by Intel(R) Trusted Execution Technology. Changes require a full power cycle to take effect.

Intel® SpeedStep™

Allows more than two frequency ranges to be supported.

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Intel® Speed Shift Technology

Enables/disables Intel® Speed Shift Technology support. Enabling will expose the CPPC v2 interface to allow hardware controlled P- states.

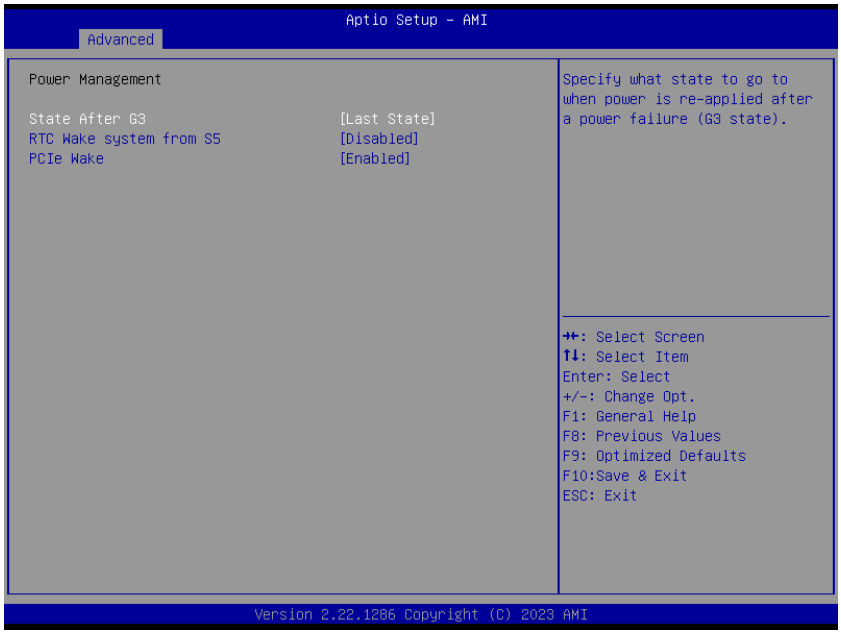
C States

Enable/Disable CPU Power Management. Allows CPU to go to C states when it is not 100% utilized.

TCC Activation Offset

Offset from factory set TCC activation temperature at which the Thermal Control Circuit must be activated. TCC will be activated at: TCC Activation Temp - TCC Activation Offset. TCC Activation Offset range is 0 to 63.

7.2.2 Power Management



State After G3

Specifies the state to go to when power is re-applied after a power failure (G3 state).

RTC Wake system from S5

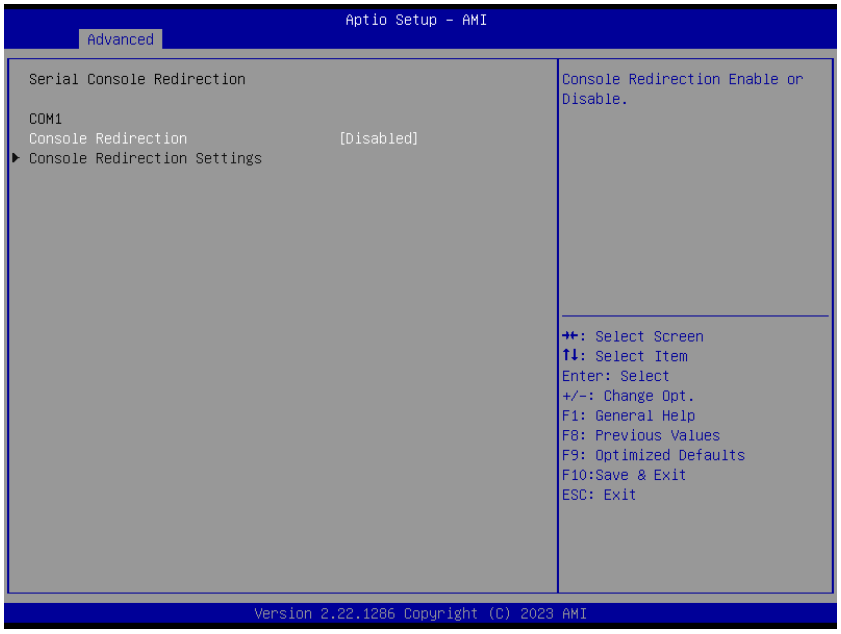
Enable or disable System wake on alarm event. Select FixedTime for the system to wake at the hr::min::sec specified. Select DynamicTime for the system to wake at the current time + Increase minute(s).

PCIe Wake

Enables/disables PCI Express wake capability.

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7.2.3 Serial Console Redirection



COM1

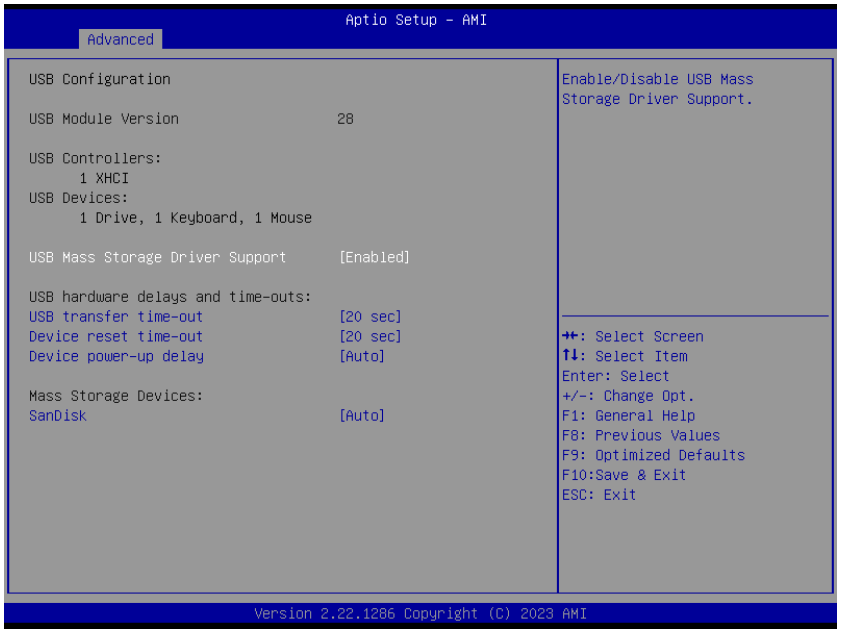
Console Redirection

Enables/disables SIO COM1 Console Redirection.

Console Redirection Settings

Specify how the host computer and the remote computer (in use) exchange data. Both computers should have the same or compatible settings.

7.2.4 USB Configuration



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USB Mass Storage Driver Support

Enables/disables USB mass storage driver support.

USB transfer time-out

The time-out value for Control, Bulk, and Interrupt transfers.

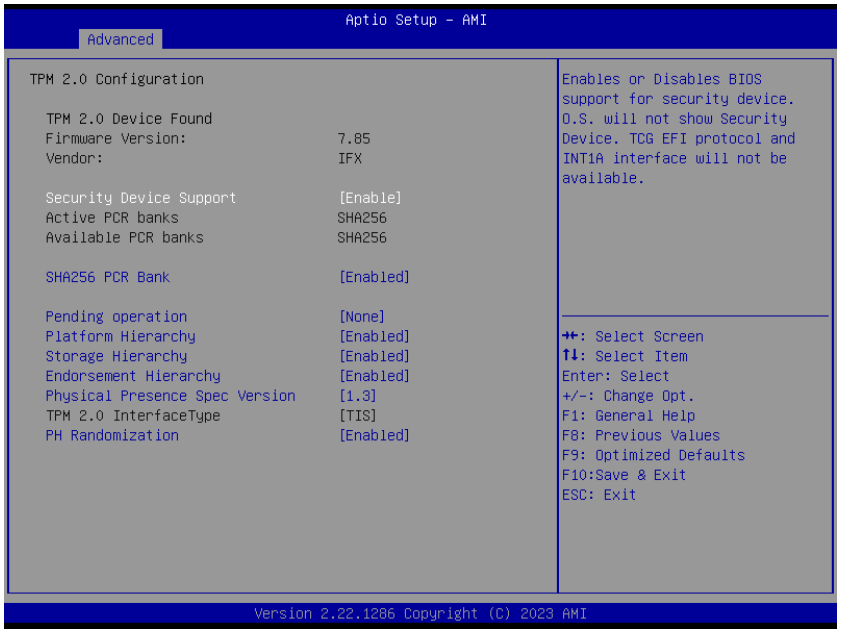
Device reset time-out

USB mass storage device Start Unit command time-out.

Device power-up delay

Maximum time the device will take before it properly reports itself to the Host Controller, where Auto uses default value for Root port 100ms, and for Hub port the delay is taken from Hub descriptor.

7.2.5 TPM 2.0 Configuration



Security Device Support

Enables/disables BIOS support for security device. OS will not show Security Device. TCG EFI protocol and INT1A interface will not be available.

SHA256 PCR Bank

Enables/disables SHA256 PCR Bank.

Pending Operation

Schedules Operation for the Security Device. Computer will reboot during restart in order to change State of Security Device.

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Platform Hierarchy

Enables/disables Platform Hierarchy.

Storage Hierarchy

Enables/disables Storage Hierarchy.

Endorsement Hierarchy

Enables/disables Endorsement Hierarchy.

Physical Presence Spec Version

Directs OS to support PPI Spec Version 1.2 or 1.3. Some HCK tests may not support 1.3.

TPM 2.0 Interface Type

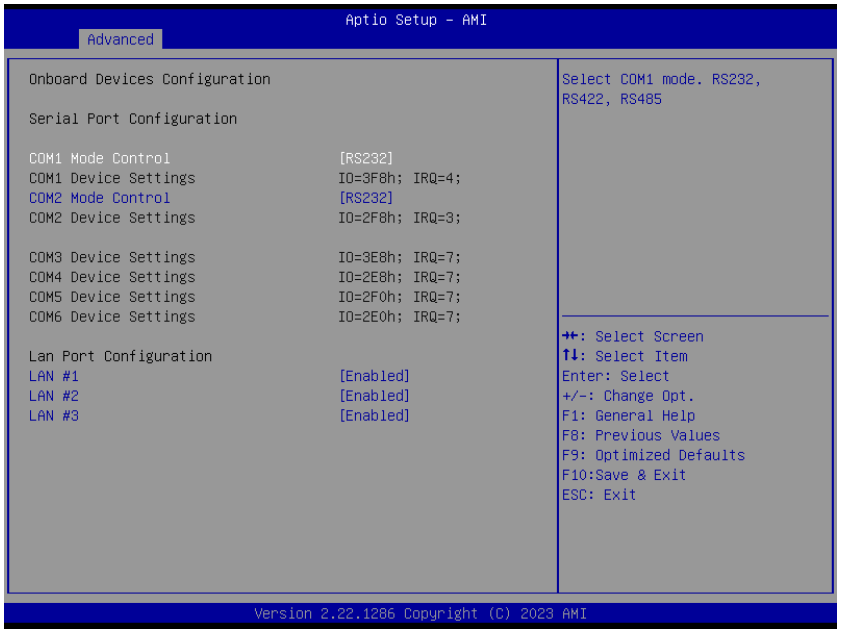
Select the Communication Interface to TPM 20 Device.

PH Randomization

Enables or Disables Platform Hierarchy randomization. **DO NOT ENABLE THIS QUESTION IN PRODUCTION PLATFORMS. THIS IS FOR DEVELOPMENT TESTING.** OVERRIDE Change-

PlatformAuth ELINK for production platforms supporting TXT.

7.2.6 Onboard Devices Configuration



COM1 Mode Control

Selects COM1 mode from RS232, RS422 or RS485.

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COM2 Mode Control

Selects COM2 mode from RS232, RS422 or RS485.

LAN #1 (Intel I225)

Enables/disables I225 LAN1.

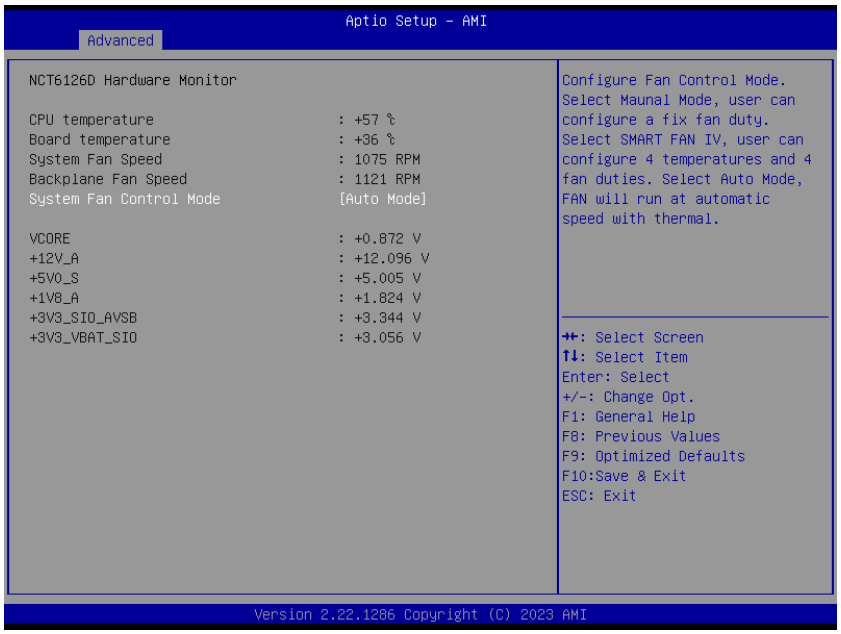
LAN #2 (Intel I225)

Enables/disables I225 LAN2.

LAN #3 (Intel I225)

Enables/disables I225 LAN3.

7.2.7 NCT6126D HW Monitor



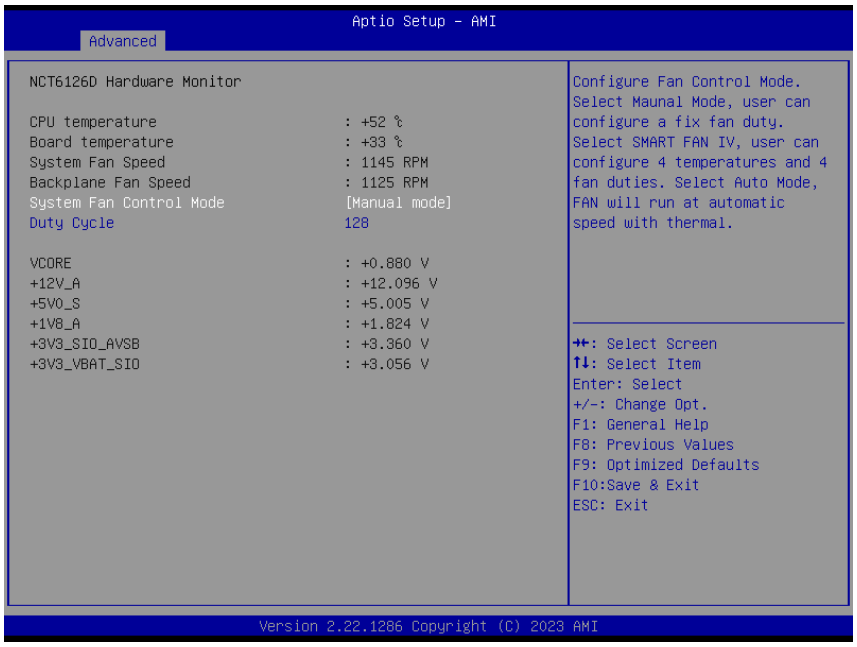
Displays CPU/Board Temperatures, System/Backplane fan speeds, and various voltages.

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7.2.7.1 System Fan Control Mode

Configures fan control mode from Manual Mode, Auto Mode, and SMART FAN IV.

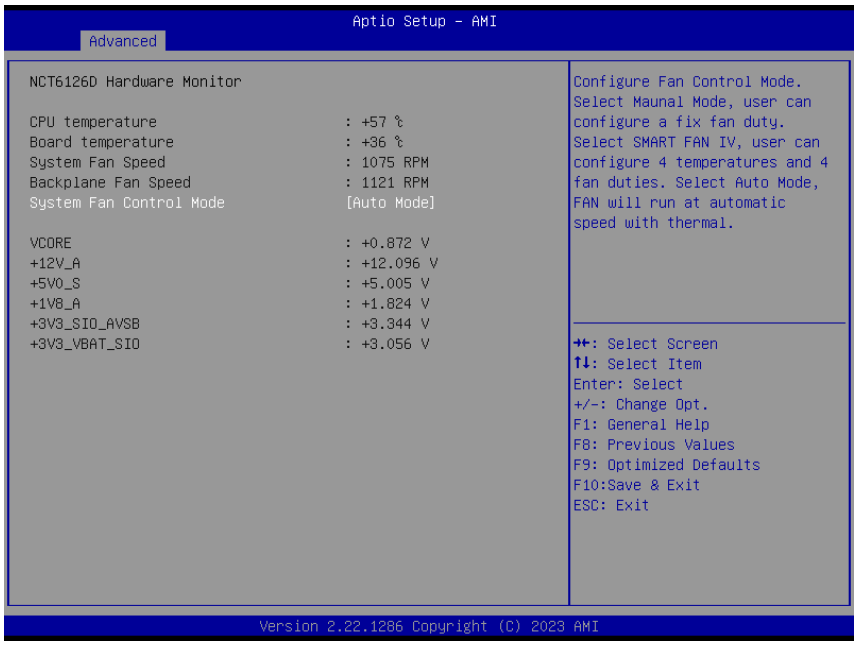
7.2.7.1.1 Manual Mode



Allows configuration of a set fan duty cycle.

7.2.7.1.2 Auto Mode

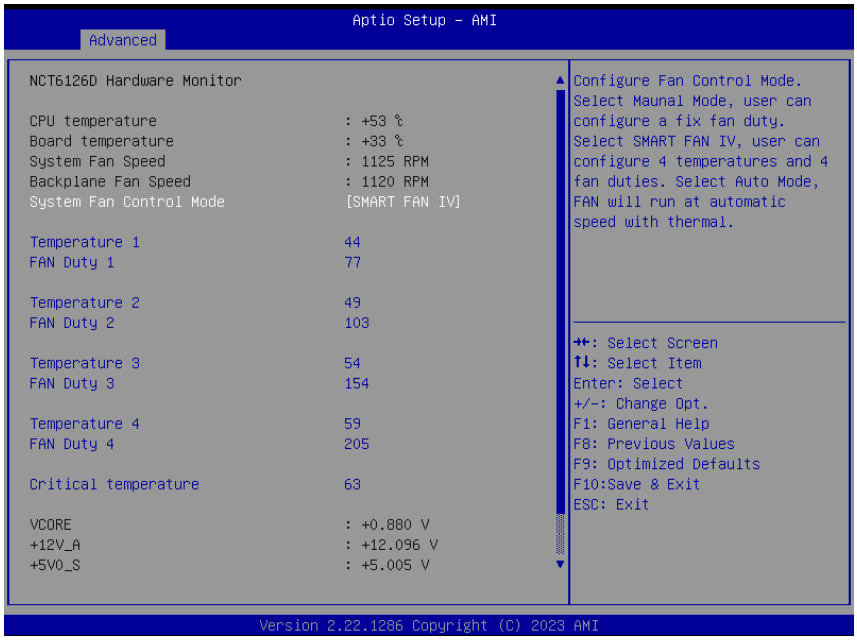
Fan runs at automatic speed for optimal thermal solution.



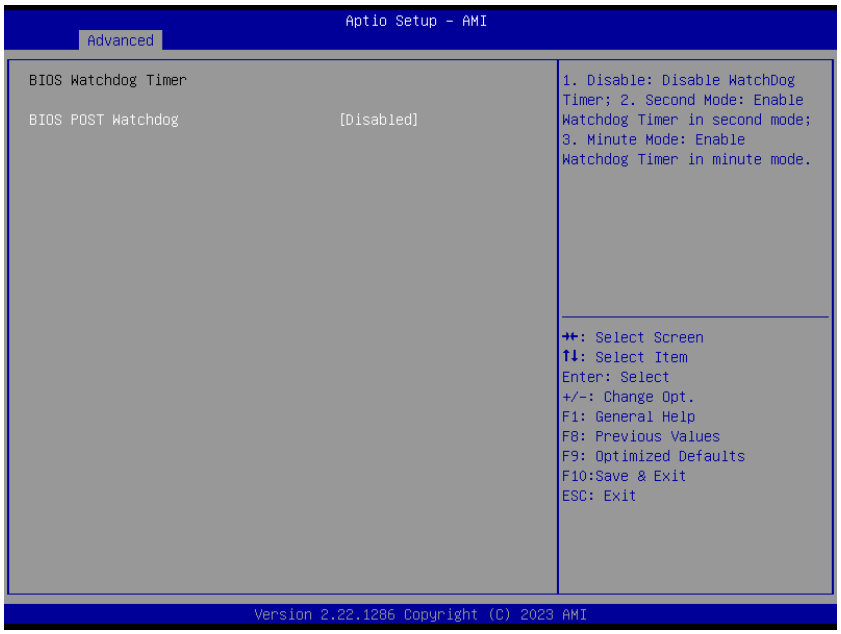
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7.2.7.1.3 SMART FAN IV

Four temperatures and four fan duties can be user-configured.



7.2.8 BIOS Watchdog Timer

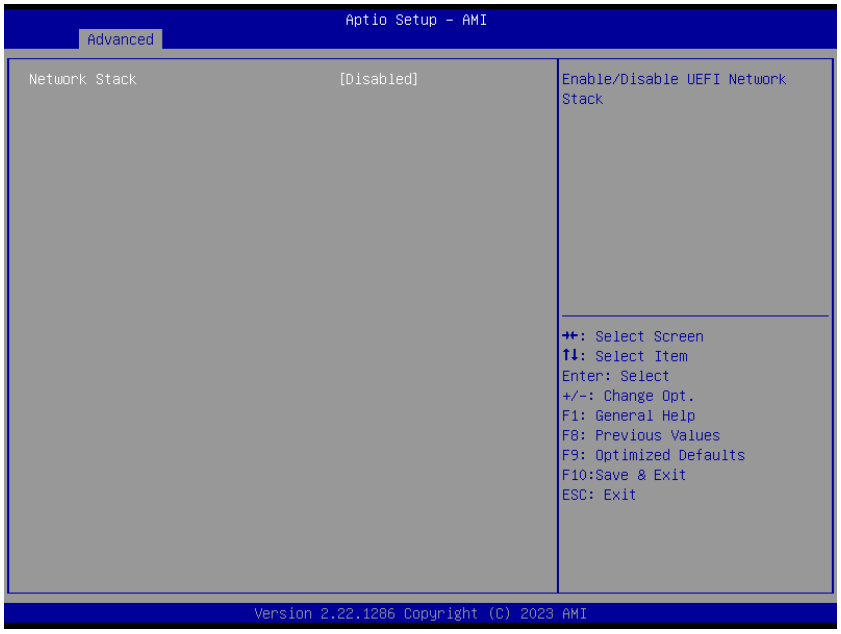


BIOS POST Watchdog

Disable disables Watchdog Timer, Second Mode enables Watch- dog Timer in second mode, Minute Mode enables Watchdog Timer in minute mode.

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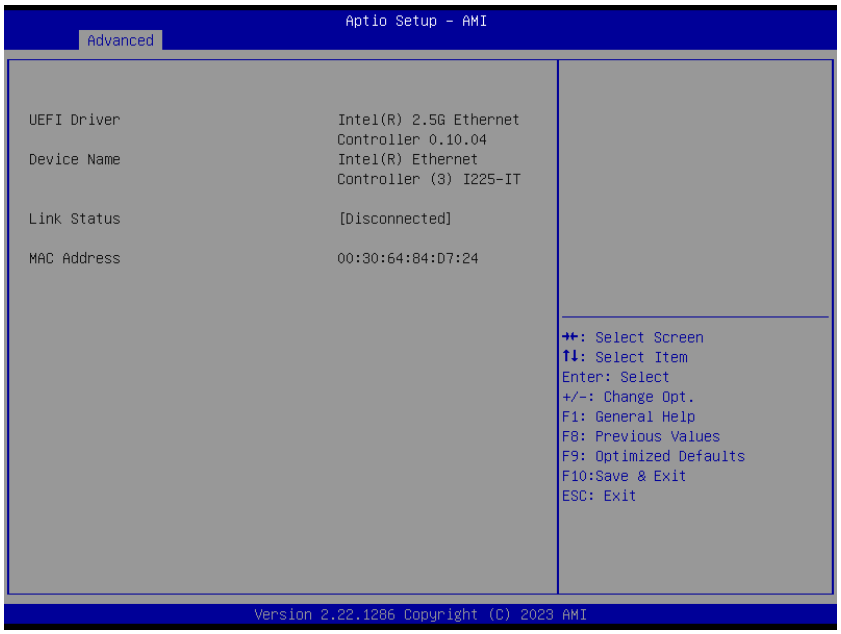
7.2.9 Network Stack Configuration



Network Stack

Enables/disables UEFI Network Stack.

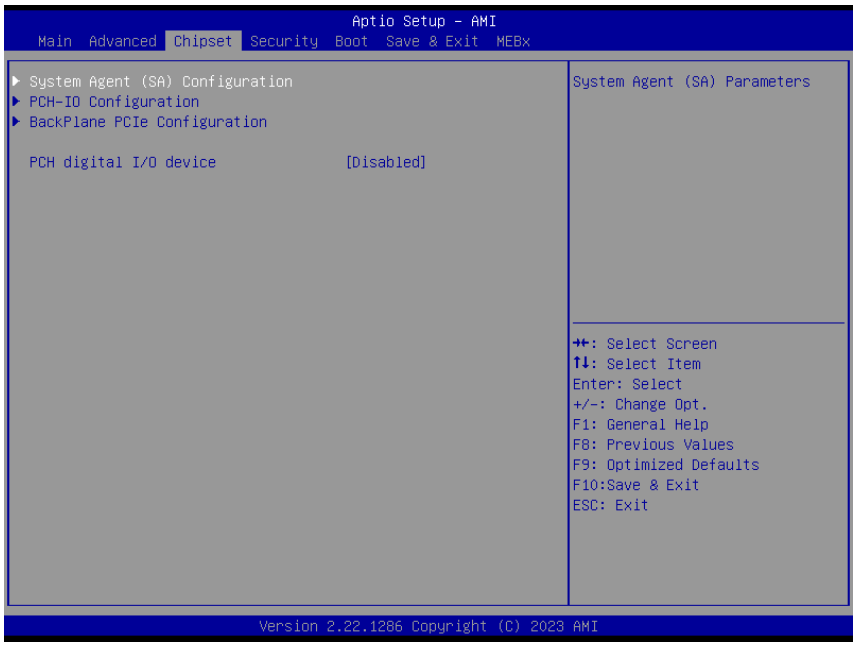
7.2.10 Intel® Ethernet Controller (3) I225-IT



Displays UEFI Driver, Device Name, Link Status and MAC Address.

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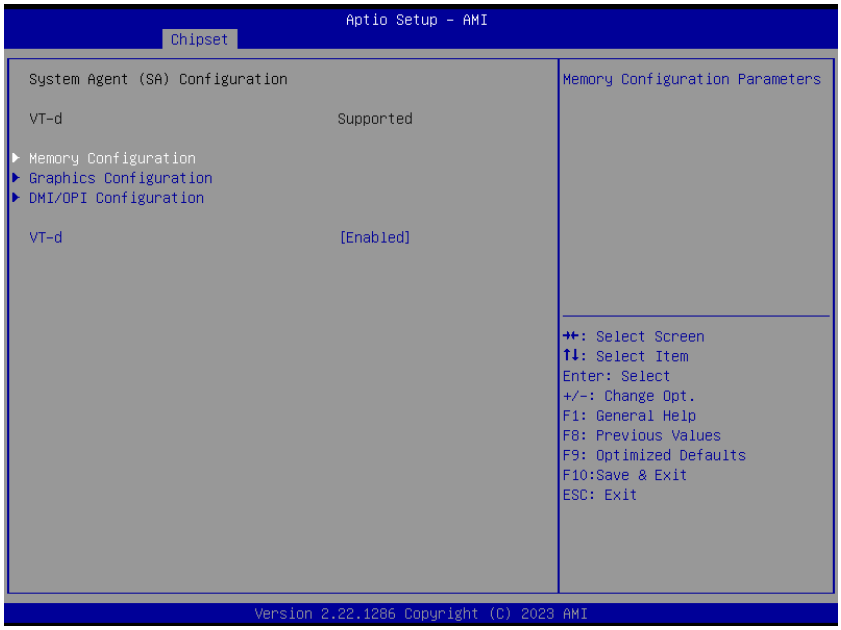
7.3 Chipset



WARNING:

Setting incorrect or conflicting values in Chipset BIOS Setup may cause system malfunction.

7.3.1 System Agent (SA) Configuration

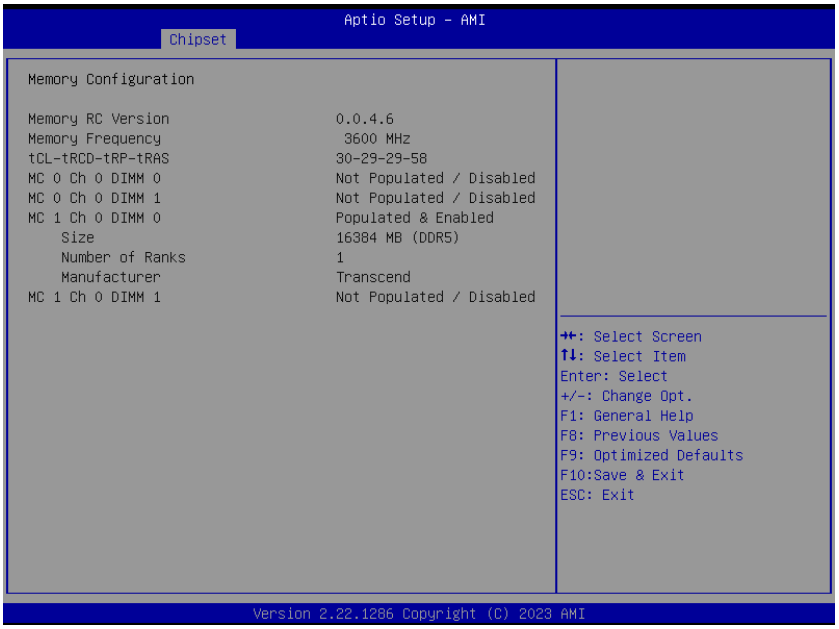


System Agent (SA) Configuration

Displays and configures VT-d capability and provides settings for Memory, Graphics, and DMI/OPI configuration.

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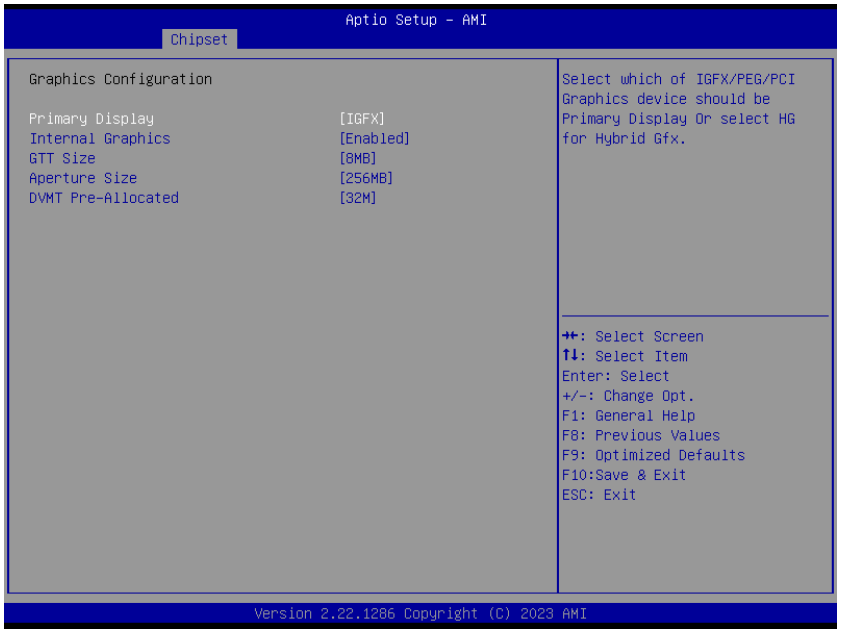
7.3.1.1 Memory Configuration



Memory Configuration

Displays memory speed, size, maker, and configuration.

7.3.1.2 Graphics Configuration



Primary Display

Select which of IGFX/PEG/PCI Graphics device should be the Primary Display.

Internal Graphics

Keeps IGFX enabled based on Setup options.

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GTT Size

Selects GTT Size.

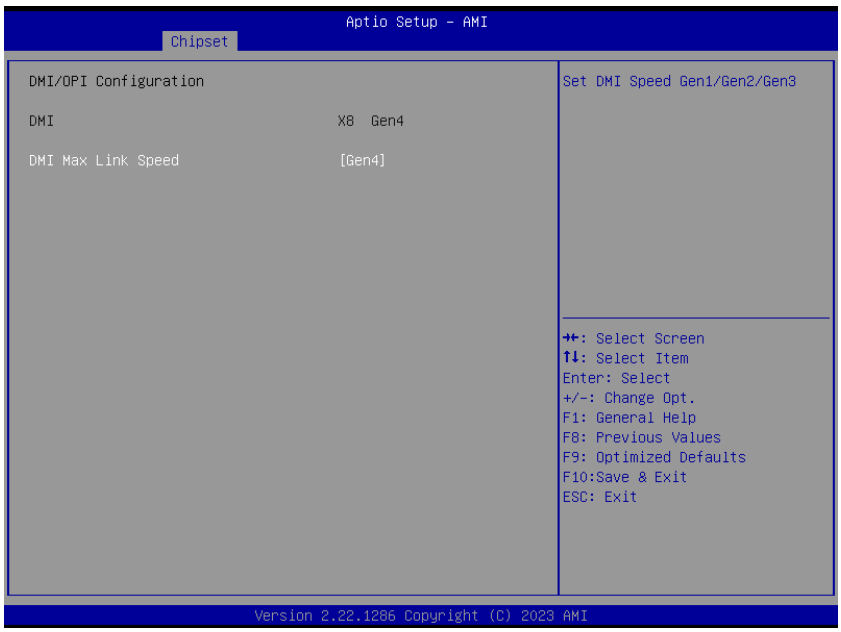
Aperture Size

Selects Aperture Size.

DVMT Pre-Allocated

Selects DVMT 5.0 Pre-Allocated (Fixed) Graphics Memory size used by the Internal Graphics Device.

7.3.1.3 DMI/OPI Configuration



DMI/OPI Configuration

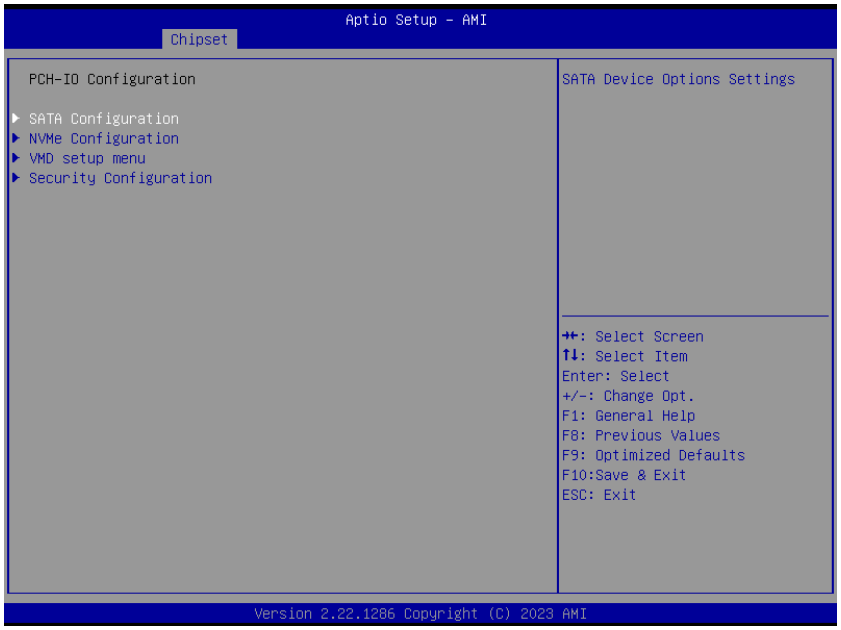
Displays DMI link speed and width.

DMI Max Link Speed

Sets DMI Speed to Gen1/Gen2/Gen3/Gen4.

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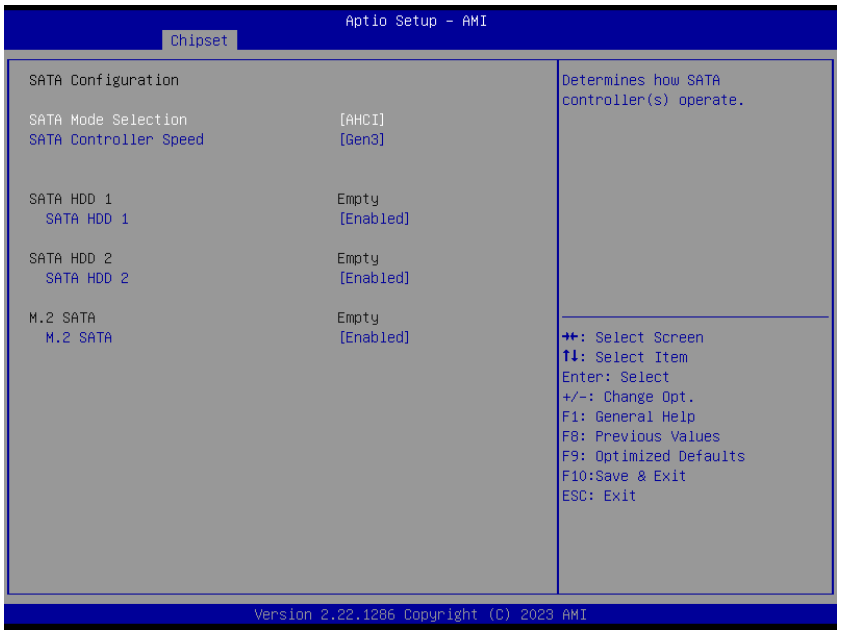
7.3.2 PCH-IO Configuration



PCH-IO Configuration

Configures SATA, NVMe, VMD, and Secruity.

7.3.2.1 SATA Configuration



SATA Mode Selection

Determines how SATA controller(s) operate.

SATA Controller Speed

Sets maximum speed the SATA controller can support.

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SATA HDD 1

Enables/disables SATA HDD 1.

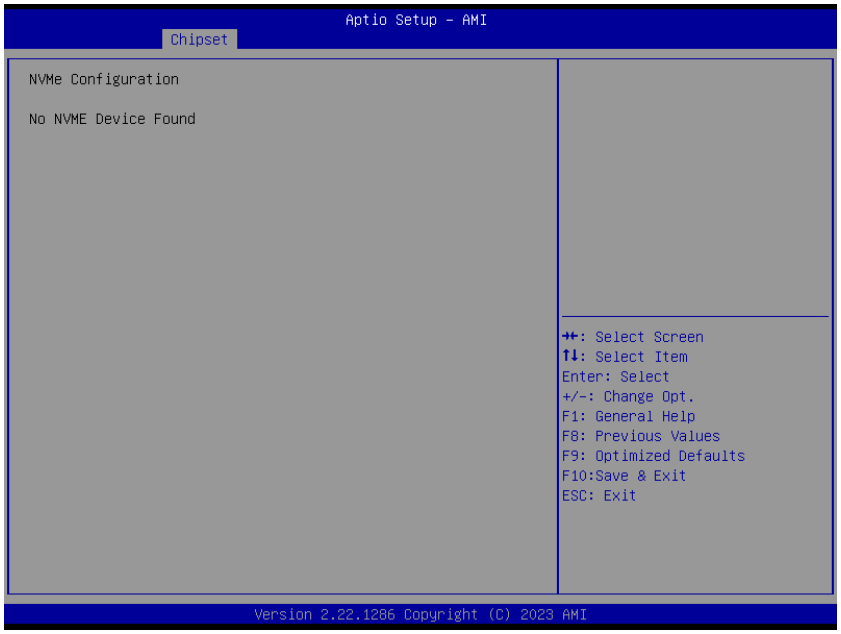
SATA HDD 2

Enables/disables SATA HDD 2.

M.2 SATA

Enables/disables M.2 SATA.

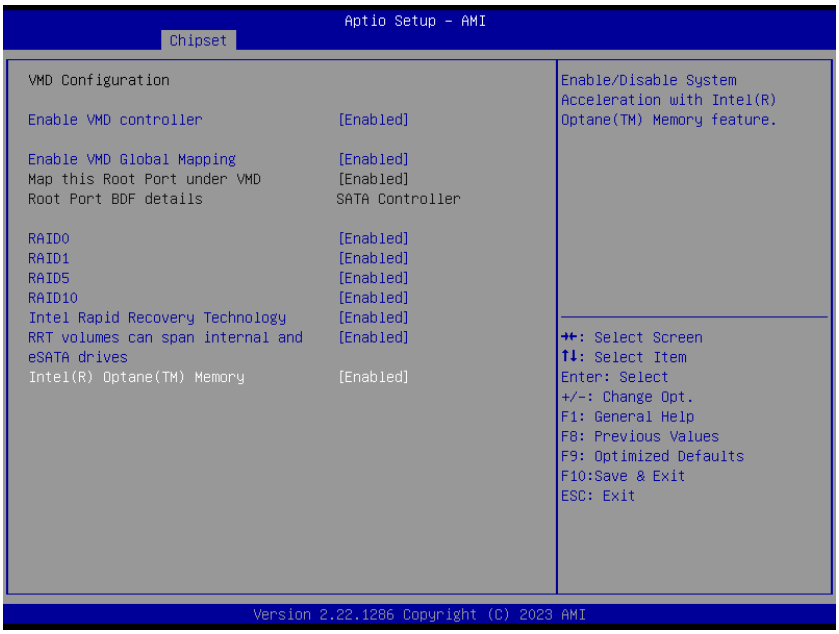
7.3.2.2 NVMe Configuration



Displays NVMe device status.

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7.3.2.3 VMD Setup Menu



Enable VMD controller

Enable/Disable VMD controller.

Enable VMD Global Mapping

Enable/Disable VMD Global Mapping.

RAID0

Enable/Disable RAID0 support.

RAID1

Enable/Disable RAID1 support.

RAID5

Enable/Disable RAID5 support.

RAID10

Enable/Disable RAID10 support.

Intel Rapid Recovery Technology

Enable/Disable Intel Rapid Recovery Technology.

RRT volumes can span internal and eSATA drives

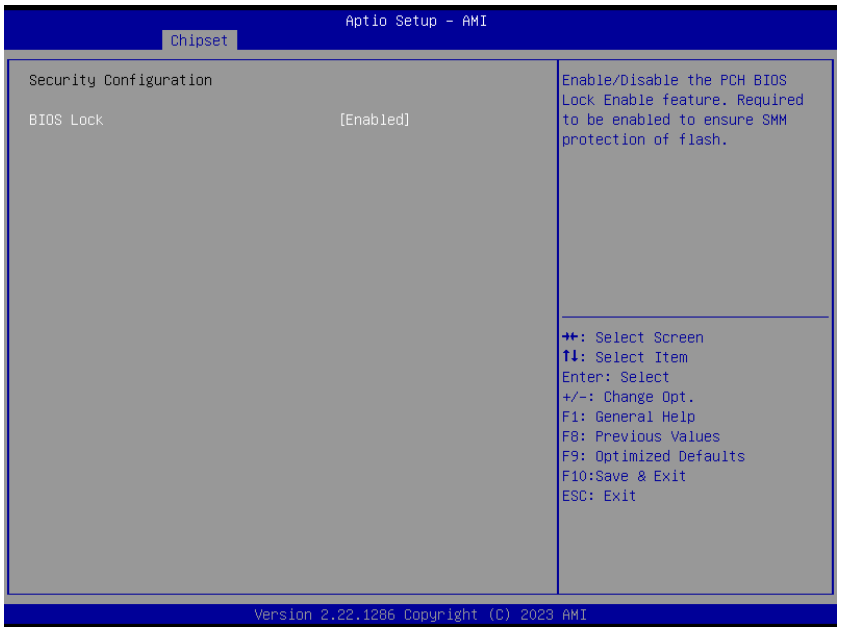
Enable/Disable RRT volumes can span internal and eSATA drives.

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Intel® Optane™ Memory

Enable/Disable System Acceleration with Intel® Optane™ Memory feature.

7.3.2.4 Security Configuration



BIOS Lock

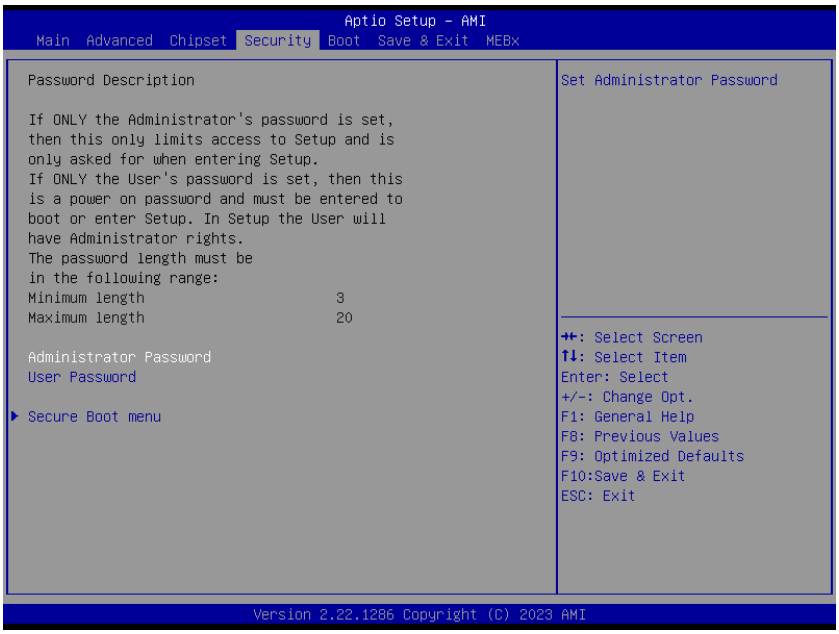
Enables/disables PCH BIOS Lock Enable. Required to be enabled to ensure SMM flash protection.

7.3.3 PCH Digital I/O Device

Disable/Enable ACPI GPIO Device support for PCH digital I/O and user LED.

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7.4 Security



If only the Administrator password is set, access is limited and the password requested on Setup. If User password is set, it acts as a power-on password and must be entered to boot or enter setup.

Administrator Password

Sets Administrator Password.

User Password

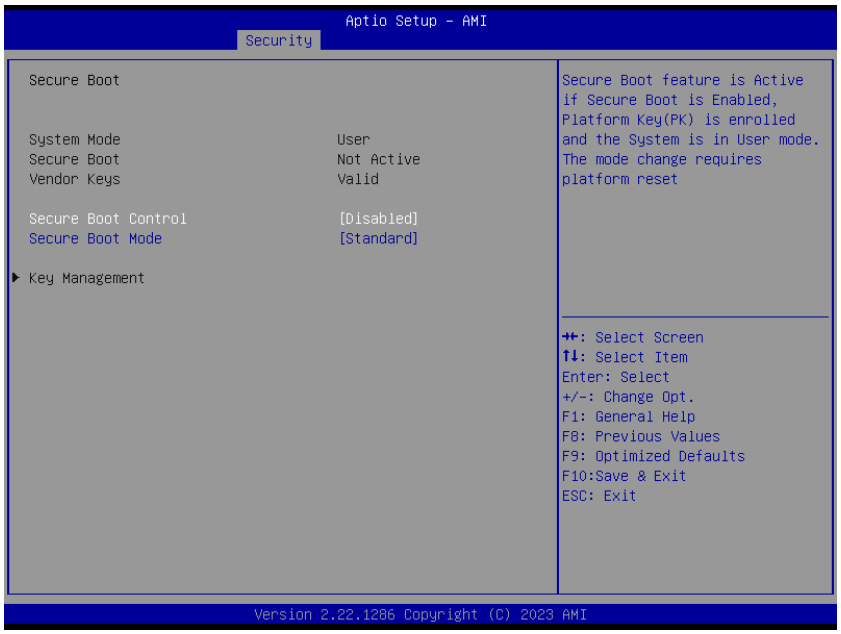
Sets User Password.

Secure Boot Menu

Configures Secure Boot.

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7.4.1 Secure Boot Menu



Secure Boot Control

Active if Secure Boot is Enabled, Platform Key (PK) is enrolled and the System is in User mode. Mode change requires platform reset.

Secure Boot Mode

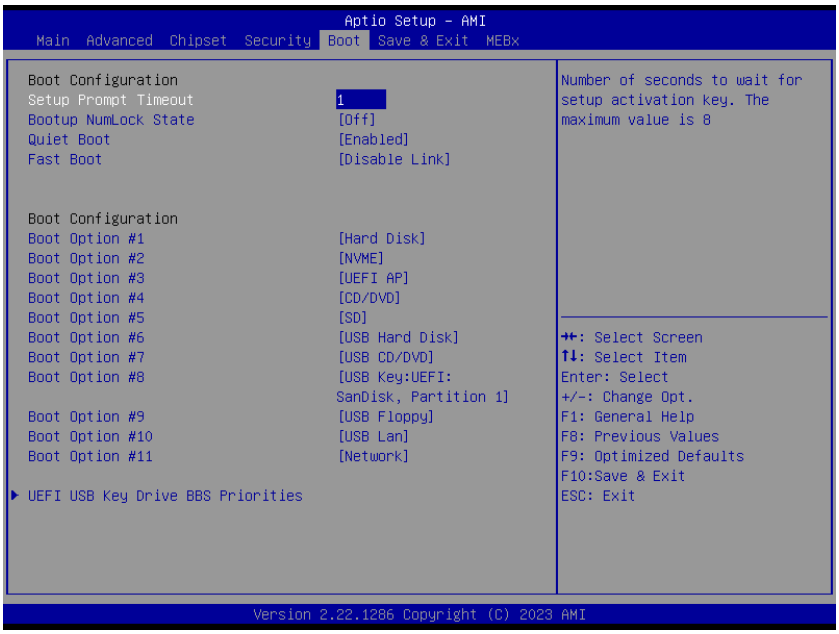
Selects from Standard or Custom. In Custom, Secure Boot Policy variables can be configured by a physically present user without full authentication.

Key Management

Enables expert users to modify Secure Boot Policy variables with- out full authentication.

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7.5 Boot



Setup Prompt Timeout

Number of seconds to wait for setup activation key. The maximum value is 8.

Bootup Num-Lock State

Sets keypad Number Lock status following boot.

Quiet Boot

Option	Description
Disabled	Directs BIOS to display POST messages
Enabled	Directs BIOS to display the OEM logo.

Fast Boot

Option	Description
Disabled	Directs BIOS to perform all POST tests.
Enabled	Directs BIOS to skip certain POST tests to boot faster.

While enabling Fast Boot can reduce system ready time, some prerequisites can reduce effectiveness.

Boot Configuration

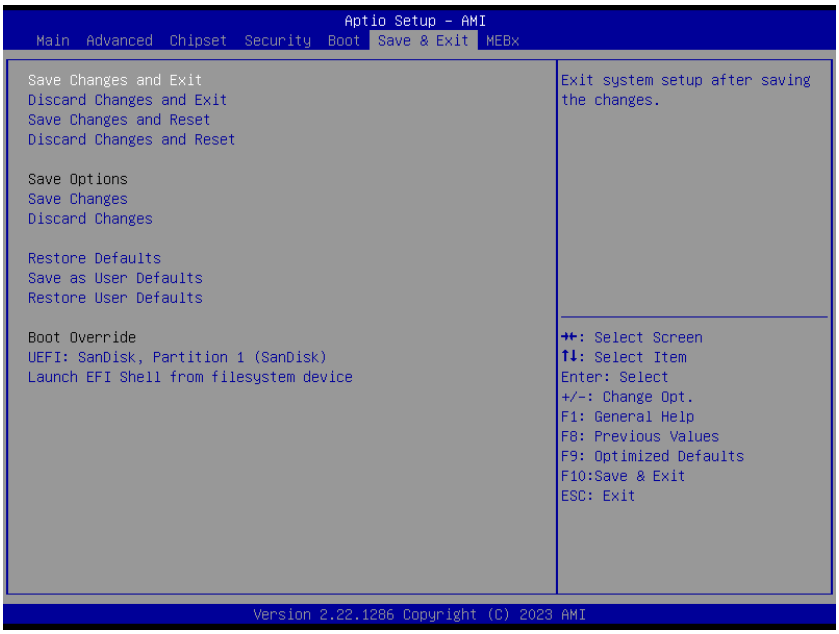
Specifies the priority of boot devices, all of which are detected during POST and displayed. Target Boot Option # and click to select the desired device.

UEFI USB Hard Drive Disk Drives BBS Priorities

Specifies Boot Device Priority sequence from available UEFI USB Drives.

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7.6 Save & Exit



Save Changes and Exit

Exits system setup after saving the changes.

Discard Changes and Exit

Exits system setup without saving any changes.

Save Changes and Reset

Resets the system setup after saving changes.

Discard Changes and Reset

Resets system setup without saving any changes.

Save Changes

Saves Changes so far to any of the setup options.

Discard Changes

Discards Changes so far to any of the setup options.

Restore Defaults

Sets all BIOS options to default settings, designed for maximum system stability but less than maximum performance. Select Restore Defaults if the computer encounters system configuration problems.

Save as User Defaults

Saves all changes so far as User Defaults.

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Restore User Defaults

Restores user defaults to all setup options.

Launch EFI Shell from filesystem device

Attempts to launch EFI Shell application (Shell.efi) from one of the available filesystem devices.

7.7 MEBx

Intel® AMT

Enable/Disable Intel® AMT support.

Intel® AMT Configuration

Select Intel® AMT configurations in BIOS.

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8 Appendix C Watchdog Timer (WDT) Function Library

This appendix describes use of the watchdog timer (WDT) function library for the DLAP-5200 controller. The watchdog timer is a hardware mechanism provided to reset the system if the operating system or an application stalls. After starting, the watchdog timer in the application must be periodically reset before the timer expires. Once the watchdog timer expires, a hardware-generated signal is sent to reset the system.

8.1 WDT with API/Windows

The WDT API library files and a demo program (incl. source code) can be downloaded from: https://www.adlinktech.com/products/Industrial_PCs_Fanless_Embedded_PCs/IPCSystems/DLAP-5200_Series

To use the WDT function library for the DLAP-5200, include the header file WDT.h and linkage library WDT.lib in the C++ project.

InitWDT

Initializes the watchdog timer function. Must be called before the invocation of any other WDT function.

Syntax

C/C++

```
BOOL InitWDT()
```

Parameters

None

Return codes

`TRUE` if watchdog timer is successfully initialized.

`FALSE` if watchdog timer fails to initialize.

SetWDT

Sets the timeout value of the watchdog timer. There are two parameters for this function to indicate the timeout ticks and unit. ResetWDT or StopWDT should be called before the expiration of watchdog timer, or the system will reset.

Syntax

C/C++

```
BOOL SetWDT(BYTE tick, BYTE unit)
```

Parameters

`tick`

Specify the number of ticks for watchdog timer. A valid value is 1 - 60.

`unit`

Specifies the timeout ticks of the watchdog timer.

Value	Description
0	The unit for one tick is 1 second. For example, when one tick is specified as 15 and the unit as 0, the timeout value is 15 seconds.
1	The unit for one tick is one minute. For example, when one tick is specified as 60 and the unit as 1, the timeout value is 60 minutes.

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Return codes

TRUE if timeout value of watchdog timer is successfully set.

FALSE if timeout value of watchdog timer is failed to set.

StartWDT

Start the watchdog timer function. Once the StartWDT is invoked, the watchdog timer starts. ResetWDT or StopWDT should be called before the expiration of watchdog timer, or the system will reset.

Syntax

C/C++

```
BOOL StartWDT()
```

Parameters

None

Return codes

TRUE if watchdog timer is successfully started.

FALSE if watchdog timer is failed to start.

ResetWDT

Reset the watchdog timer. The invocation of ResetWDT allows restoration of the watchdog timer to the initial timeout value specified in SetWDT function. ResetWDT or StopWDT should be called before the expiration of the watchdog timer, or the system will reset.

Syntax

C/C++

```
BOOL ResetWDT()
```

Parameters

None

Return codes

TRUE if watchdog timer is successfully reset.

FALSE if watchdog timer fails to reset.

StopWDT

Stops the watchdog timer.

Syntax

C/C++

```
BOOL StopWDT()
```

Parameters

None

Return codes

TRUE if watchdog timer is successfully stopped.

FALSE if watchdog timer fails to stop.

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8.2 WDT with DOS/Linux

Under Linux, please program the WDT function using the eSPI IO registers according to the sample program as follows.

```
#include <dos.h>
#include <stddef.h>
#include <stdio.h>
/* Config NCT6126D to enter config mode */
EnterConfig(void)
{
outp(0x4E, 0x87);
outp(0x4E, 0x87);
}
/* Config to exit config mode */
ExitConfig(void)
{ outp(0x4E, 0xAA);
 }
/* Read byte */
unsigned char r_reg(unsigned char regoffset)
{ outp(0x4E, regoffset);
return inp(0x4F); }
/* Write byte */
void w_reg(unsigned char regoffset, unsigned char data)
{ outp(0x4E, regoffset); outp(0x4F, data);
}
main(void)
{
unsigned int    count;
/* print program title  */
printf("-----------------DLAP-5200 WDT Demo------------
---- \n");

   printf("Init and config GPIO
   ports<<<<<<<<<<<<<<<<<<<<<<<<<\n");
   printf("------------------------------------------------------------
   ----\n");
   EnterConfig();
   /* config WDT registers */
   w_reg(0x07,0x08);
   /* enable keyboard interrupt to reset WDT timeout
```

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```
   value */
   w_reg(0xF2,r_reg(0xF2)|0x40);
   /* set unit as second */
   w_reg(0xF0,r_reg(0xF0)&~0x04);
   /* start the Watchdog */
   w_reg(0x30,0x01);
   /* set timeout value as 30 seconds */
   /* WDT start automatically while timeout value is set
   */
   w_reg(0xF1,0x1E);

   printf("------------------------------------------------------------
   - --- \n");
   printf("WDT is set and counting down
   now.<<<<<<<<<<<<<<<<<<\n");
   printf("------------------------------------------------------------
   - --- \n");
   for(count=30;count!=0;count--)
   { printf("Countdown %2d :  .\n",count);
   /* reset WDT timeout value to 10 seconds */
   /* w_reg(0x73,0x0A); */
   sleep(1);
   }
   /* disable WDT */
   /* WDT stop while timeout value is set to zero */
   w_reg(0x30,0x00);

printf("WDT is disable. Program is terminating.");
ExitConfig();
return 0;
}
```

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9 Appendix D Digital Input/Output Function Library

DI/O provides input/output to support inter-device communication. Simple programming guides allow easy transmission of digital signals between the system and attached peripherals.

DI/O with API/Windows

Matrix DI/O API library files and a demo program (incl. source code) can be downloaded from http://www.adlinktech.com.

To use the DI/O function library for DLAP-5200 series, include the header file awl.h and linkage library awl.lib in the C++ project. DI/O functions are as follows.

AwlDioGetValue

Reads the digital logic state of a digital input line.

Syntax

C/C++

```
int __stdcall AwlDioGetValue(int Index)
```

Parameter(s)

Index

Indexes the digital logic state of DLAP-5200 digital input channels 1 to 8 (bit 0 to 7)

Return codes

```
0: Operation Success
-1: Operation Failed
```

AwlDioSetValue

Sets the digital logic state of the digital output line.

AwlDioGetValue

Reads the digital logic state of a digital input line.

Syntax

C/C++

```
int __stdcall AwlDioGetValue(int Index , int Value)
```

Parameter(s)

Index

Indexes the digital logic state of DLAP-5200 digital input channels 1 to 8 (bit 0 to 7)

Value

Sets the digital logic state of DLAP-5200 digital output channels 1 to 8 (bit 0 to 7) to 0 or 1.

Return codes

```
0: Operation Success
-1: Operation Failed
```

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Safety Instructions

For user safety, please read and follow all instructions, Warnings, Cautions, and Notes marked in this manual and on the associated device before handling/operating the device, to avoid injury or damage.

S'il vous plaît prêter attention stricte à tous les avertissements et mises en garde figurant sur l'appareil , pour éviter des blessures ou des dommages.

- Read these safety instructions carefully.
- Keep the User's Manual for future reference.
- Read the Specifications section of this manual for detailed information on the recommended operating environment.
- The device can be operated at an ambient temperature of 50ºC.
- When installing/mounting or uninstalling/removing device; or when removal of a chassis cover is required for user servicing:
  - Turn off power and unplug any power cords/cables.
  - Reinstall all chassis covers before restoring power.
- To avoid electrical shock and/or damage to device:
  - Keep device away from water or liquid sources.
  - Keep device away from high heat or humidity.
  - Keep device properly ventilated (do not block or cover ventilation openings).
  - Always use recommended voltage and power source settings.
  - Always install and operate device near an easily accessible electrical outlet.
  - Secure the power cord (do not place any object on/over the power cord).
  - Only install/attach and operate device on stable surfaces and/or recommended mountings.
- If the device will not be used for long periods of time, turn off and unplug from its power source.
- Never attempt to repair the device, which should only be serviced by qualified technical personnel using suitable tools.
- A Lithium-type battery may be provided for uninterrupted backup or emergency power.



Risk of explosion if battery is replaced with one of an incorrect type; please dispose of used batteries appropriately.

Risque d'explosion si la pile est remplacée par une autre de type incorrect. Veuillez jeter les piles usagées de façon appropriée.

- The device must be serviced by authorized technicians when:
  - The power cord or plug is damaged.
  - Liquid has entered the device interior.
  - The device has been exposed to high humidity and/or moisture.
  - The device is not functioning or does not function according to the User's Manual.
  - The device has been dropped and/or damaged and/or shows obvious signs of breakage.
- Disconnect the power supply cord before loosening the thumbscrews and always fasten the thumbscrews with a screwdriver before starting the system up.
- It is recommended that the device be installed only in a server room or computer room where access is:
  - Restricted to qualified service personnel or users familiar with restrictions applied to the location, reasons therefore, and any precautions required.

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- Only afforded by the use of a tool or lock and key, or other means of security, and controlled by the authority responsible for the location.

- If PoE (Power over Ethernet) is enabled for the device, the system can ONLY be deployed indoors. Unless otherwise noted, the PoE system is NOT designed to withstand the rigors of outdoor use.

	**BURN HAZARD** Touching this surface could result in bodily injury. To reduce risk, allow the surface to cool before touching. ***RISQUE DE BRÛLURES*** *Ne touchez pas cette surface, cela pourrait entraîner des blessures.*

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Please visit the Contact page at www.adlinktech.com for information on how to contact the ADLINK regional office nearest you.

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