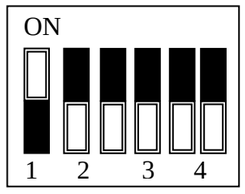
HSL-AO4-U HSL 4 channels Analog Output

Specifications

- 4-CH single-ended analog outputs
- LED indicator: Power and Link
- Slave ID consumption: 2[1]
- Output range: ±12 V
- Driving capacity: ± 20 mA
- Offset error: ± 1.5 mV max
- Gain error: ± 0.1 % of max output
- D/A resolution: 16-bits
- Transmission Mode: Full / Half Duplex
- Transmission Speed: 3 / 6 / 12 Mbps
- HSL Bus max. communication time (12M / FD): 60.67 µs/command
- DAC max. update rate: 300 kS/s
- Settling time (-12V to +12V step): 10µs max
- Power supply requirement: +24V_{DC} (±10%)
- Operating temperature: 0 to 60°C
- Storage temperature: -20 to 80°C
- Power consumption: 4 W

DIP Switch Setting

➢ SW1



ON = 1

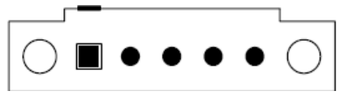
100000	Address 1, 2
010000	Address 2, 3
…	…
011111	Address 62, 63
111111	Not used

OFF = 0

Pin Definitions

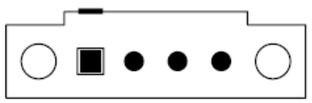
➢ CN1

Pin #	Signal Name	Description
1	E24V	External Power, +24VDC +/-10%
2		
3	EGND	External Ground
4		
5	FG	Field Ground



➢ CN2, CN3

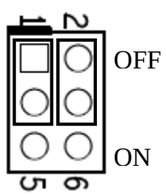
Pin #	Signal Name	Description
1	AO0 / AO2	Analog Output Channel 0 / 2
2	AG	Analog Ground
3	AO1 / AO3	Analog Output Channel 1 / 3
4	AG	Analog Ground



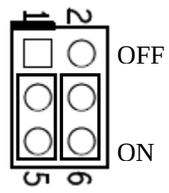
Jumper Setting

➢ JP1 (Terminator setting)

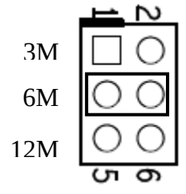
Not the last module (Default)



The last module

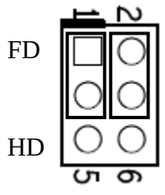


➢ JP3 (Transmission Speed setting)

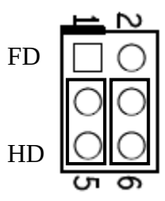


➢ JP2 (Transmission Mode setting)

Full Duplex (Default)



Half Duplex



[1] Each block of two IDs supports one module. If the slave ID of one module is set as “n”, the other should be set as “n+1”.

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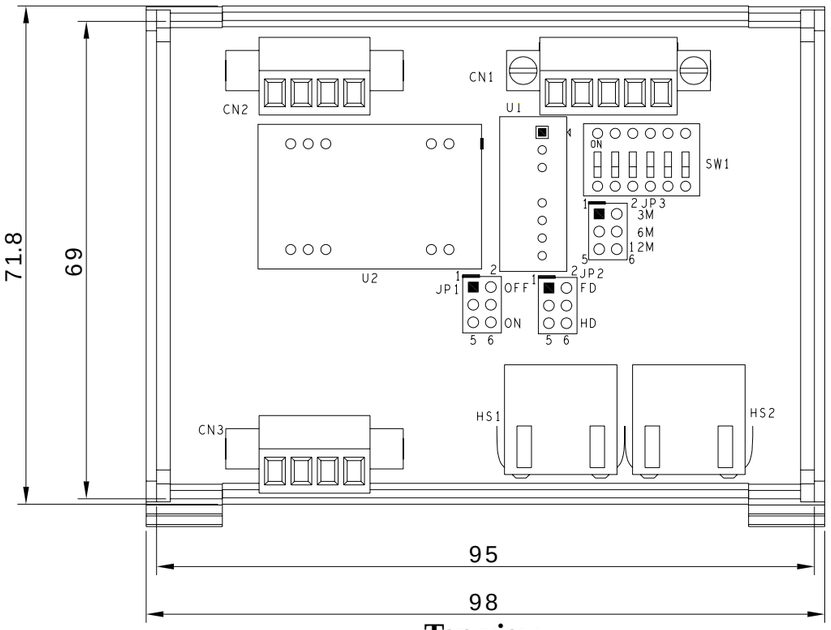
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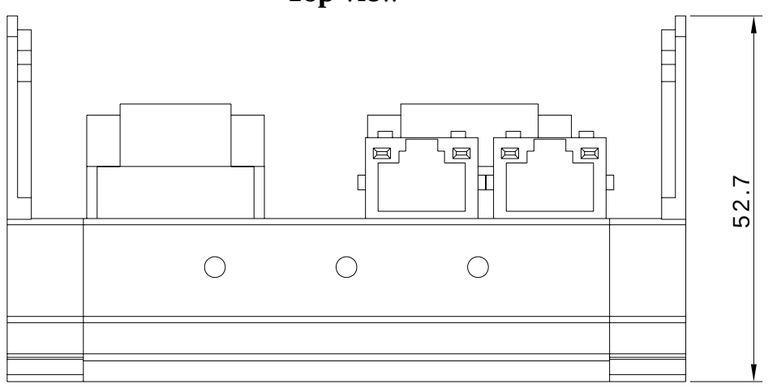
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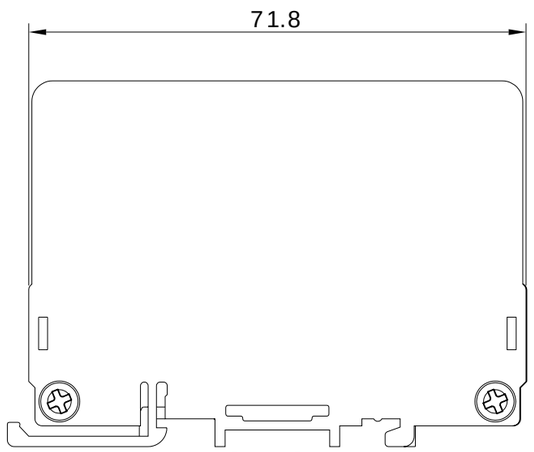
Dimensions



Top view



Front view



Right side view

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