# HSL HSL-DO32-US 32-CH Discrete Output Module for HSL System

## Pin Definitions

P1:

| No. | Name | I/O | Function | No. | Name | I/O | Function |
|---|---|---|---|---|---|---|---|
| 1 | IO +24V | I | Digital I/O Power | 2 | IO_GND | -- | Digital I/O Power Ground |
| 3 | IO +24V | I | Digital I/O Power | 4 | IO_GND | -- | Digital I/O Power Ground |
| 5 | IO +24V | I | Digital I/O Power | 6 | IO_GND | -- | Digital I/O Power Ground |
| 7 | DO0 | O | Discrete Output Channel 0 | 8 | DO1 | O | Discrete Output Channel 1 |
| 9 | DO2 | O | Discrete Output Channel 2 | 10 | DO3 | O | Discrete Output Channel 3 |
| 11 | DO4 | O | Discrete Output Channel 4 | 12 | DO5 | O | Discrete Output Channel 5 |
| 13 | DO6 | O | Discrete Output Channel 6 | 14 | DO7 | O | Discrete Output Channel 7 |
| 15 | DO8 | O | Discrete Output Channel 8 | 16 | DO9 | O | Discrete Output Channel 9 |
| 17 | DO10 | O | Discrete Output Channel 10 | 18 | DO11 | O | Discrete Output Channel 11 |
| 19 | DO12 | O | Discrete Output Channel 12 | 20 | DO13 | O | Discrete Output Channel 13 |
| 21 | DO14 | O | Discrete Output Channel 14 | 22 | DO15 | O | Discrete Output Channel 15 |
| 23 | IO +24V | I | Digital I/O Power | 24 | IO_GND | -- | Digital I/O Power Ground |
| 25 | IO +24V | I | Digital I/O Power | 26 | IO_GND | -- | Digital I/O Power Ground |

P2:

| No. | Name | I/O | Function | No. | Name | I/O | Function |
|---|---|---|---|---|---|---|---|
| 1 | IO +24V | I | Digital I/O Power | 2 | IO_GND | -- | Digital I/O Power Ground |
| 3 | IO +24V | I | Digital I/O Power | 4 | IO_GND | -- | Digital I/O Power Ground |
| 5 | IO +24V | I | Digital I/O Power | 6 | IO_GND | -- | Digital I/O Power Ground |
| 7 | DO16 | O | Discrete Output Channel 16 | 8 | DO17 | O | Discrete Output Channel 17 |
| 9 | DO18 | O | Discrete Output Channel 18 | 10 | DO19 | O | Discrete Output Channel 19 |
| 11 | DO20 | O | Discrete Output Channel 20 | 12 | DO21 | O | Discrete Output Channel 21 |
| 13 | DO22 | O | Discrete Output Channel 22 | 14 | DO23 | O | Discrete Output Channel 23 |
| 15 | DO24 | O | Discrete Output Channel 24 | 16 | DO25 | O | Discrete Output Channel 25 |
| 17 | DO26 | O | Discrete Output Channel 26 | 18 | DO27 | O | Discrete Output Channel 27 |
| 19 | DO28 | O | Discrete Output Channel 28 | 20 | DO29 | O | Discrete Output Channel 29 |
| 21 | DO30 | O | Discrete Output Channel 30 | 22 | DO31 | O | Discrete Output Channel 31 |
| 23 | IO +24V | I | Digital I/O Power | 24 | IO_GND | -- | Digital I/O Power Ground |
| 25 | IO +24V | I | Digital I/O Power | 26 | IO_GND | -- | Digital I/O Power Ground |

RJ1-RJ2:

| No. | Name | Function |
|---|---|---|
| 1 | NA | NA |
| 2 | NA | NA |
| 3 | TX+ | RS-422/485 Transmission Line, Positive |
| 4 | RX- | RS-422/485 Receiving Line, Negative |
| 5 | RX+ | RS-422/485 Receiving Line, Positive |
| 6 | TX- | RS-422/485 Transmission Line, Negative |
| 7 | NA | NA |
| 8 | NA | NA |

POWER 1:

| No. | Name | I/O | Function |
|---|---|---|---|
| 1 | EX +24V | I | External Power Supply Input (+24V DC+5%) |
| 2 | IO +24V | I | Digital I/O Power (+24V DC+5%) |
| 3 | EX_GND | -- | External Power Supply Ground |
| 4 | IO_GND | -- | Digital I/O Power Ground |
| 5 | F_GND | -- | Shielding Ground |

JUMPER

| JP1(When Reset) | |
|---|---|
| 1-2 | Clear Data (Default) |
| 2-3 | Keep Data |

| JP2 | |
|---|---|
| 1-2 | 3M |
| 3-4 | 6M (Default) |
| 5-6 | 12M |

| JP3: Terminator Resistor | |
|---|---|
| 1-3<br>2-4 | ON |
| 3-5<br>4-6 | OFF (Default) |

| JP4 | |
|---|---|
| 1-3<br>2-4 | Full Duplex (Default) |
| 3-5<br>4-6 | Half Duplex |

| JP5 | |
|---|---|
| 1-3<br>2-4 | Separate Power<br>Separate GND |
| 3-5<br>4-6 | Common Power<br>Common GND<br>(Default) |

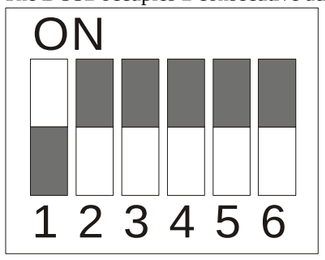
## Specifications

| Transistor Output | | | | | |
|---|---|---|---|---|---|
| Model | Number | Type | Switching Capacity max. | Response Time | |
| | | | | ON | OFF |
| -N | 32 | NPN Sinking | -90mA at $24V_{DC}$ | 2 ms max. | 2 ms max. |

LED Indication: Active DC power (Red); Link (Green); Discrete Output (Yellow)

## DIP Switch Setting

The DO32 occupies 2 consecutive address allocations beginning with an odd address. Bit **1** is fixed to **ON**.




ON = 1

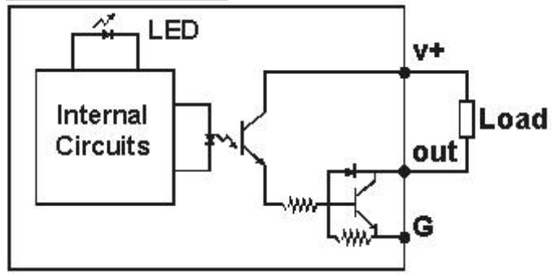
| | |
|---|---|
| 100000 | address 1, 2 |
| 110000 | address 3, 4 |
| … | … |
| 110111 | address 59, 60 |
| 101111 | address 61, 62 |

OFF = 0



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**-N NPN Sinking Output**




## Ordering Information

HSL-DO32-US-N: 32-CH Discrete NPN Sinking Output Module

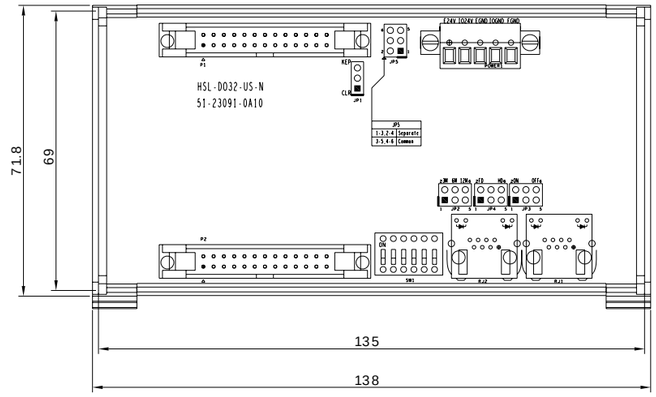
## ADLINK on the Internet

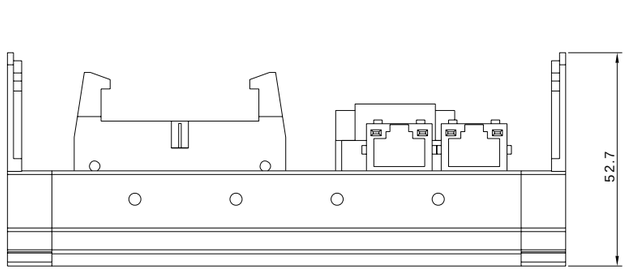
Homepage: http://www.adlinktech.com
Service: service@adlinktech.com

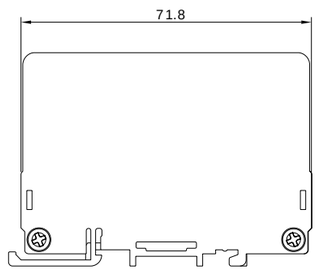
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## Dimensions

US Module with case (138 x 71.8 x 52.7 mm)






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