Quick Reference Guide

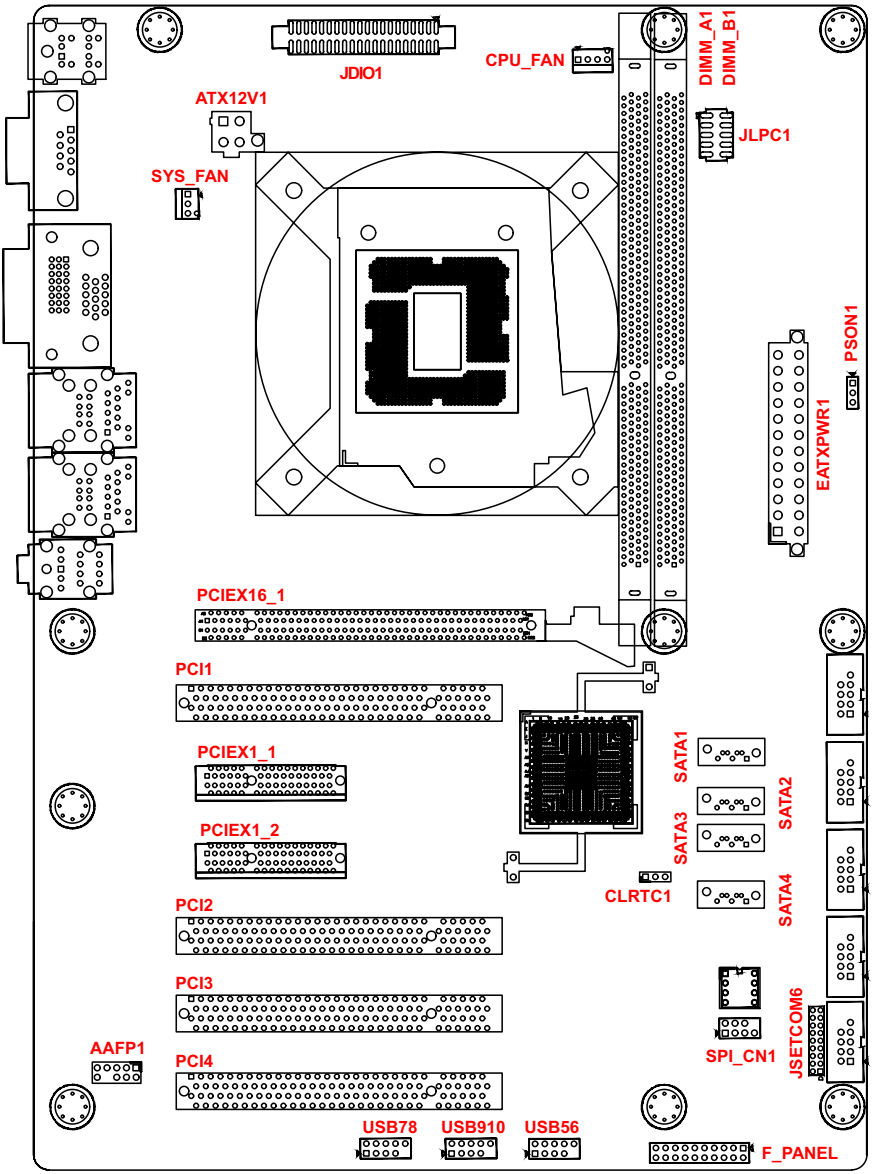
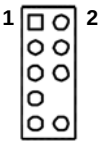


Figure 1: IMB-M40H Board Layout

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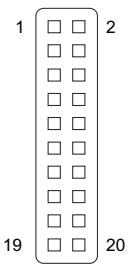
Front Panel Audio Pin Header (AAFP1)

Pin #	Signal	Pin #	Signal
1	MIC2_L	2	AGND
3	MIC2_R	4	FP_PRES#
5	LIN2_R	6	SRTN1
7	SENSE A	8	NC
9	LIN2_L	10	SRTN2

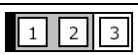
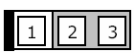


System Panel Pin Header (F_PANEL)

Pin #	Signal	Function	Pin Group
1	WDSPK	Speaker signal	Chassis Speaker
3	NC		
5	NC		
7	P5V	Power	
9	NC		
11	GND	Ground	Key Lock
13	KEYLOCK	Keyboard lock	
15	PLED	Power LED signal	Power LED
17	NC		
19	P5V	Power LED pull-up	
2	GND	Ground	RESET Button
4	RESETBT	RESET signal	
6	NC		
8	GND	Ground	Power On Button
10	POWERBT	Power-on signal	
12	NC		
14	NC		
16	HDDLED	Hard Disk LED signal	Hard Disk LED
18	P3V3	Hard Disk LED pull-up	
20	NC		



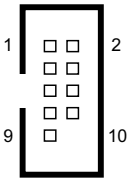
Clear CMOS (CLRTC1)

RTC status	Connection	CLCMOS
Normal	1 – 2	
Clear CMOS	2 – 3	

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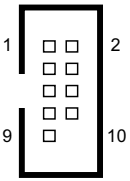
Serial Port Connectors - RS-232 (COM2~6)

Pin #	Signal	Function
1	DCD	Data Carrier Detect
2	DSR	Data Set Ready
3	RXD	Receive Data
4	RTS	Request to Send
5	TXD	Transmit Data
6	CTS	Clear to Send
7	DTR	Data Terminal Ready
8	RI	Ring Indicate
9	GND	Ground
10	NC	Key

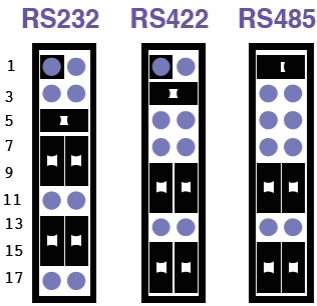


Serial Port Connector - RS-422/485 (COM6)

Pin #	RS-422	RS-485
1	TX-	DATA-
2	N/A	N/A
3	TX+	DATA+
4	N/A	N/A
5	RX+	N/A
6	N/A	N/A
7	RX-	N/A
8	N/A	N/A
9	GND	GND
10	Key	Key



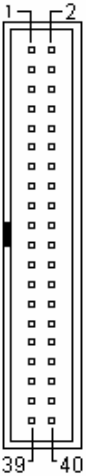
COM6 Mode Jumper Settings (JSETCOM6)



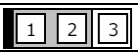
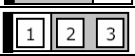
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Digital IO Connector (JDIO1)

Pin #	Signal	Pin #	Signal
1	SIO_GPIO0	2	DIO_GPIO0
3	SIO_GPIO1	4	DIO_GPIO1
5	SIO_GPIO2	6	DIO_GPIO2
7	SIO_GPIO3	8	DIO_GPIO3
9	SIO_GPIO4	10	DIO_GPIO4
11	SIO_GPIO5	12	DIO_GPIO5
13	SIO_GPIO6	14	DIO_GPIO6
15	SIO_GPIO7	16	DIO_GPIO7
17	SIO_GPIO8	18	DIO_GPIO8
19	SIO_GPIO9	20	DIO_GPIO9
21	SIO_GPIO10	22	DIO_GPIO10
23	SIO_GPIO11	24	DIO_GPIO11
25	SIO_GPIO12	26	DIO_GPIO12
27	SIO_GPIO13	28	DIO_GPIO13
29	SIO_GPIO14	30	DIO_GPIO14
31	SIO_GPIO15	32	DIO_GPIO15
33	SMB_CLK_RESUME	34	SMB_DATA_RESUME
35	GND	36	+5V_DUAL
37	GND	38	+5V_DUAL
39	GND	40	+5V_DUAL



AT/ATX Mode Jumper (PSON1)

Status	Connection	JCLRT_C2
AT Mode	1 – 2	
ATX Mode	2 – 3 (default)	

USB 2.0 Pin Headers (USB56/78/910)

Pin #	Signal	Pin #	Signal
1	+5V	2	+5V
3	USB0-	4	USB1-
5	USB0+	6	USB1+
7	GND	8	GND
9	Key	10	NC



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