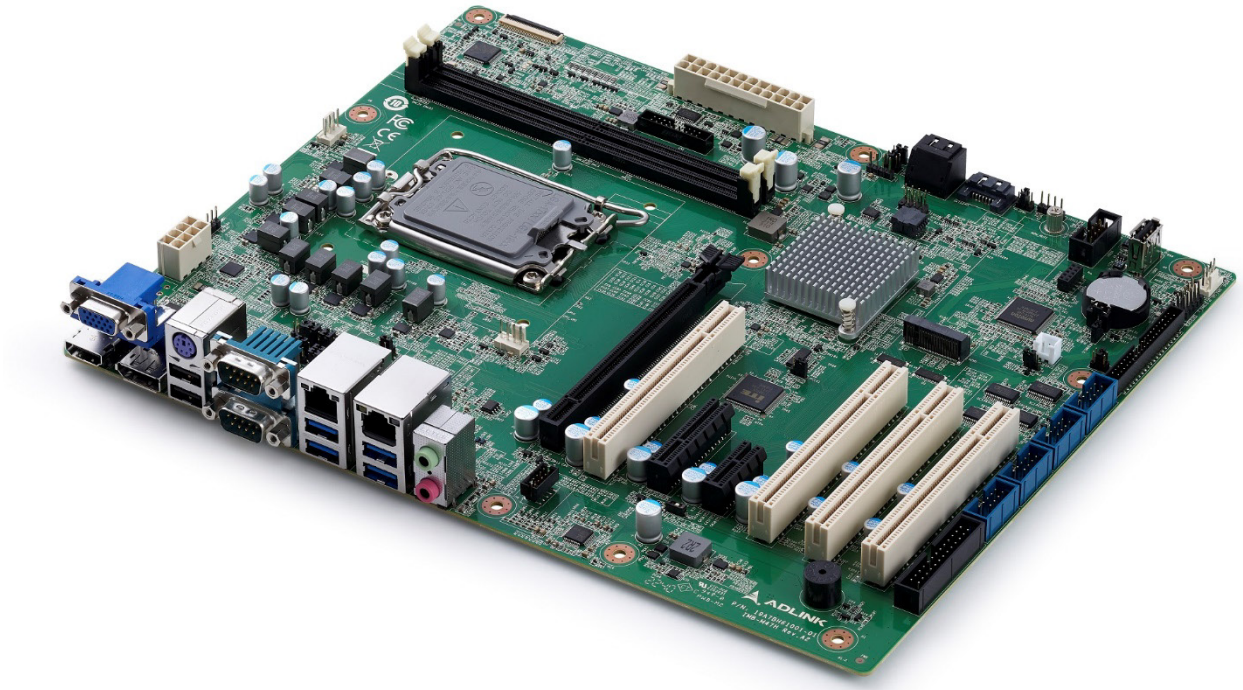
IMB-M47H

User's Manual

Industrial ATX Motherboard with Intel® Core™ 200S Series
and 14th/13th/12th Gen Core™ Processors and Intel® H610E Chipset



Manual Rev.:	Rev. 1.1
Revision Date:	October 27, 2026
Part Number:	50M-13014-1010

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Preface

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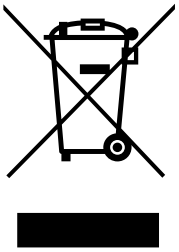
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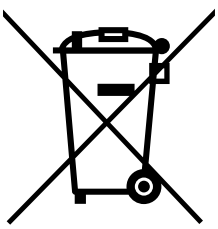
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Revision History

Revision	Description	Date	By
1.0	Initial release	2023-01-13	RA
1.1	Intel processors and CPU list updated	2026-10-27	AL

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1 Introduction

The IMB-M47H is an ATX motherboard supporting Intel® Core™ 200S Series and 14/13/12th Gen Core™ i9/i7/i5/i3 processors, an Intel® H610E chipset with 7 PCIe/ PCI slots to provide a cost-competitive embedded computing solution. With high-speed data transfer interfaces up to PCIe Gen 5, USB 3.2 Gen 1 x1 (5 Gbps), and SATA 6 Gb/s, dual-channel DDR5 memory up to 64 GB in two DIMM slots for industrial automation applications, the IMB-M47H offers a significant competitive advantage for embedded computing applications. The rugged I/O design enhances system flexibility with robust device compatibility, durable connectivity, and extreme environment readiness.

1.1 Packing List

- IMB-M47H ATX motherboard
- Rear I/O shield
- M.2 screw kit

1.2 Optional Accessories

- CPU cooler for 65W CPU (Part Number: 32-20976-0000-A0)
- CPU cooler for 125W CPU (available by request only)
- SATA port cable (7-pin) (Part Number: 30-10057-7000)
- 2-port USB 2.0 port cable with bracket (Part Number: 30-25010-3010)
- 1-port LPT port cable with bracket (Part Number: 30-25019-2000)
- 2-port COM port cable with bracket (Part Number: 30-25003-3000)
- Y cable for PS/2 KB & mouse (Part Number: 30-01016-2000)

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2 Specifications

2.1 Core System

- **CPU**: Intel® Core™ 200S Series and 14/13/12th Gen Core™ i9/i7/i5/i3 Desktop Processor

Family	Number	CPU Core	Frequency(GHz)	Cache(MB)	Power(W)	Since BIOS
Bartlett Lake-S	273PE	12	2.3GHz	36MB	65	2.05.10
Bartlett Lake-S	273PTE	12	1.4GHz	36MB	45	2.05.10
Bartlett Lake-S	253PE	10	2.5GHz	33MB	65	2.05.10
Bartlett Lake-S	253PTE	10	1.8GHz	33MB	45	2.05.10
Bartlett Lake-S	223PE	8	2.9GHz	24MB	65	2.05.10
Bartlett Lake-S	223PTE	8	2.3GHz	24MB	45	2.05.10
Bartlett Lake-S	213PE	8	2.7GHz	24MB	65	2.05.10
Bartlett Lake-S	213PTE	8	2.1GHz	24MB	45	2.05.10
Bartlett Lake-S	251E	24	2.1 GHz	36MB	65	2.04.10
Bartlett Lake-S	251TE	24	1.4 GHz	36MB	45	2.04.10
Bartlett Lake-S	221E	14	2.7 GHz	24MB	65	2.04.10
Bartlett Lake-S	221TE	14	1.8 GHz	24MB	45	2.04.10
Bartlett Lake-S	211E	10	2.7 GHz	20MB	65	2.04.10
Bartlett Lake-S	211TE	10	1.7 GHz	20MB	45	2.04.10
Bartlett Lake-S	201E	4	3.6 GHz	12MB	60	2.04.10
Bartlett Lake-S	201TE	4	2.9 GHz	12MB	45	2.04.10
Raptor Lake-S Refresh	i9-14900	24	2.0GHz	36MB	65	1.00.10
Raptor Lake-S Refresh	i9-14900T	24	1.1GHz	36MB	35	1.00.10
Raptor Lake-S Refresh	i7-14700	20	2.1GHz	33MB	65	1.00.10
Raptor Lake-S Refresh	i7-14700T	20	1.3GHz	33MB	35	1.00.10
Raptor Lake-S Refresh	i5-14500	14	2.6GHz	24MB	65	1.00.10
Raptor Lake-S Refresh	i5-14500T	14	1.7GHz	24MB	35	1.00.10
Raptor Lake-S Refresh	i5-14400	10	2.5GHz	20MB	65	1.00.10
Raptor Lake-S Refresh	i3-14100	4	3.5GHz	12MB	60	1.00.10
Raptor Lake-S Refresh	i3-14100T	4	2.7GHz	12MB	35	1.00.10
Raptor Lake-S Refresh	300	2	3.9GHz	6MB	46	1.00.10
Raptor Lake-S Refresh	300T	2	3.4GHz	6MB	35	1.00.10
Raptor Lake-S	i9-13900E	24	1.8GHz	36MB	65	1.00.10
Raptor Lake-S	i9-13900	24	2.0GHz	36MB	65	1.00.10
Raptor Lake-S	i9-13900TE	24	1.0GHz	36MB	35	1.00.10
Raptor Lake-S	i7-13700E	16	1.9GHz	30MB	65	1.00.10
Raptor Lake-S	i7-13700	16	2.1GHz	30MB	65	1.00.10
Raptor Lake-S	i7-13700TE	16	1.1GHz	30MB	35	1.00.10
Raptor Lake-S	i5-13500E	14	2.4GHz	24MB	65	1.00.10

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Raptor Lake-S	i5-13500	14	2.5GHz	24MB	65	1.00.10
Raptor Lake-S	i5-13500TE	14	1.3GHz	24MB	35	1.00.10
Raptor Lake-S	i5-13400E	10	2.4GHz	20MB	65	1.00.10
Raptor Lake-S	i5-13400	10	2.5GHz	20MB	65	1.00.10
Raptor Lake-S	i3-13100E	4	3.3GHz	12MB	60	1.00.10
Raptor Lake-S	i3-13100	4	3.4GHz	12MB	60	1.00.10
Raptor Lake-S	i3-13100TE	4	2.4GHz	12MB	35	1.00.10
Alder Lake-S	i9-12900E	16	2.3GHz	30MB	65	1.00.10
Alder Lake-S	i9-12900	16	2.4GHz	30MB	65	1.00.10
Alder Lake-S	i9-12900TE	16	1.1GHz	30MB	35	1.00.10
Alder Lake-S	i7-12700E	12	2.1GHz	25MB	65	1.00.10
Alder Lake-S	i7-12700	12	1.6GHz	25MB	65	1.00.10
Alder Lake-S	i7-12700TE	12	1.4GHz	25MB	35	1.00.10
Alder Lake-S	i5-12500E	6	2.9GHz	18MB	65	1.00.10
Alder Lake-S	i5-12500	6	3.0GHz	18MB	65	1.00.10
Alder Lake-S	i5-12500TE	6	1.9GHz	18MB	35	1.00.10
Alder Lake-S	i5-12400	6	2.5GHz	18MB	65	1.00.10
Alder Lake-S	i3-12100E	4	3.2GHz	12MB	60	1.00.10
Alder Lake-S	i3-12100	4	3.3GHz	12MB	60	1.00.10
Alder Lake-S	i3-12100TE	4	2.1GHz	12MB	35	1.00.10
Alder Lake-S	G7400E	2	3.6GHz	6MB	46	1.00.10
Alder Lake-S	G7400TE	2	3.0GHz	6MB	35	1.00.10
Alder Lake-S	G6900E	2	3.0GHZ	4MB	46	1.00.10
Alder Lake-S	G6900TE	2	2.4GHz	4MB	35	1.00.10

- **Chipset:** Intel® H610E Chipset
- **Memory:** 2x 288-pin DDR5 non-ECC sockets, dual-channel DDR5 4800 MHz, up to 64 GB
- **BIOS:** AMI® UEFI BIOS, 256 Mb SPI Flash Memory
- **Hardware Monitor**
  - +3.3 V voltage
  - +5 V voltage
  - +12 V voltage
  - CPU temperature
  - System temperature
  - CPU fan speed
  - System fan speed

2.2 I/O Interface

- **Expansion Slots**
  - 1x PCIe x16 Gen5
  - 1x PCIe x4 Gen3
  - 1x PCIe x1 Gen3

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  - 4x PCI
- **LAN**
  - LAN1: I219-LM supports 1Gbps / 100Mbps / 10Mbps
  - LAN2: I225-V supports 2.5Gps / 1Gbps / 100Mbps / 10Mbps
- **SATA:** 3x SATA 6.0 Gb/s connectors
- **USB**
  - 4x USB 3.2 Gen 1 x1 ports (5Gbps, rear I/O)
  - 2x USB 2.0 ports (rear I/O)
  - 1x USB 2.0 vertical Type A port for internal dongles
  - 2x USB 2.0 via header
- **COM:** 2x RS-232/422/485 (rear I/O), 4x RS-232 pin headers
- **Parallel Port:** 1x LPT pin header
- **PS/2 Combo Port:** 1x PS/2 keyboard and mouse connector (rear I/O)
- **DIO:** 32-bit GPIO
- **M.2 Connector**: 1x M.2 M-key, 2280 (supports PCIe x4 Gen3 (signal set by SW1), SATA)
- **TPM**: TPM 2.0

2.3 Graphics

- **VGA:** 1x VGA connector (rear I/O), resolution up to 1920 x 1200 @ 60Hz
- **DP1.4a:** 1x DP connector (rear I/O), resolution up to 3840 x 2160 @ 60Hz
- **HDMI 2.0b:** 1x HDMI® connector (rear I/O) resolution up to 3840 x 2160 @ 30Hz

2.4 Audio

- **Audio Codec:** Realtek® ALC888S
- **Interfaces**: 1x Mic-in and 1x Line-out connector (rear I/O)

2.5 Mechanical and Environmental

- **Form Factor:** ATX 305 mm x 244 mm (W x L)
- **Operating Temperature:** 0°C to 60°C
- **Storage Temperature:** -40°C to 85°C
- **Humidity:** 60 °C @ 95% RH, non-condensing

2.6 Certification

- CE & FCC Class B

2.7 Operating Systems

- Microsoft® Windows® 10, 64-bit
- Ubuntu LTS 20.04 (32/64-bit)

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2.8 Functional Block Diagram

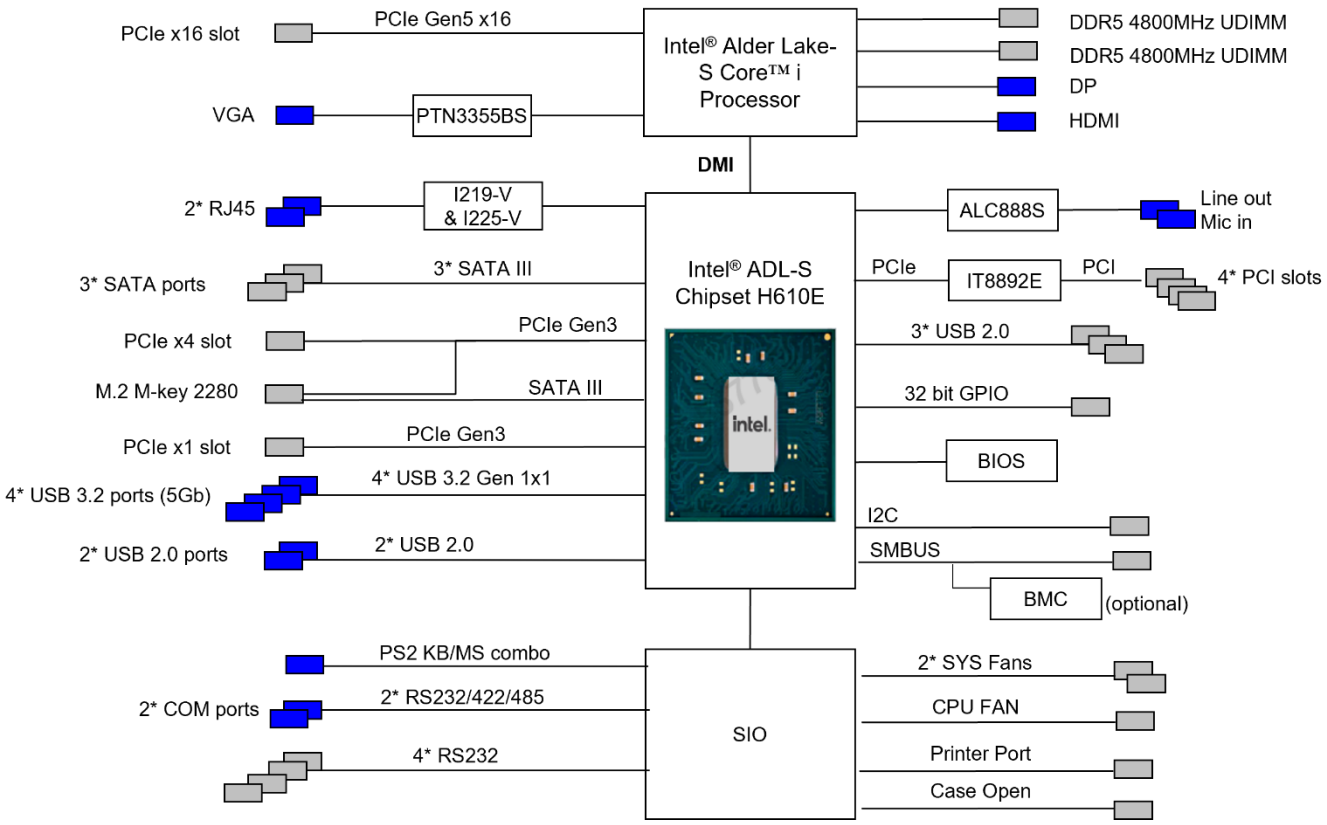


Figure 1: Functional Block Diagram

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3 Mechanical Layout

3.1 IO Panel Connector Locations

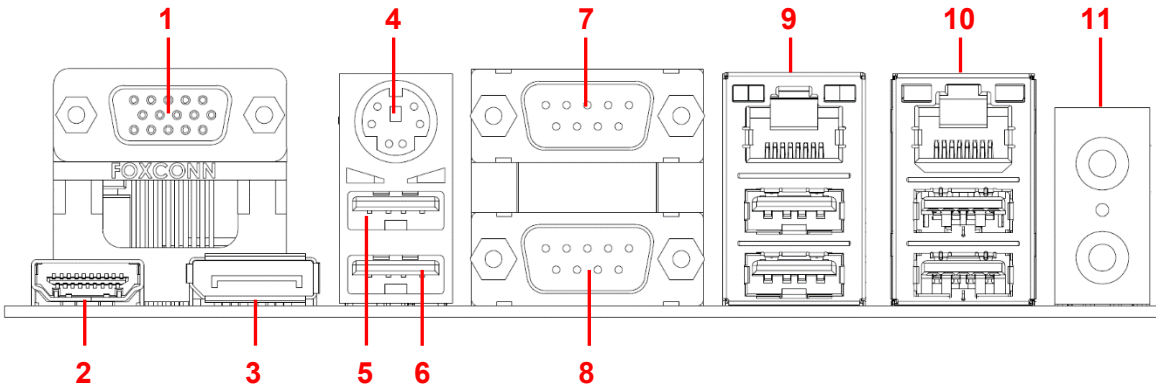


Figure 2: IO Panel Connector Locations

Table 1: IO Panel Connector Definitions

IO Panel Connectors	
Item	**Description**
1	VGA Connector
2	HDMI Port
3	DisplayPort
4	KB/MS PS/2 Port
5	USB 2.0 Port #7
6	USB 2.0 Port #8
7	COM1 RS-232/422/485
8	COM2 RS-232/422/485
9	LAN1 + USB 3.2 Gen.1 Port #1, #2
10	LAN2 + USB 3.2 Gen.1 Port #3, #4
11	Line-Out / MIC-In Port

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3.2 Onboard Connectors, Jumpers and Switch Locations

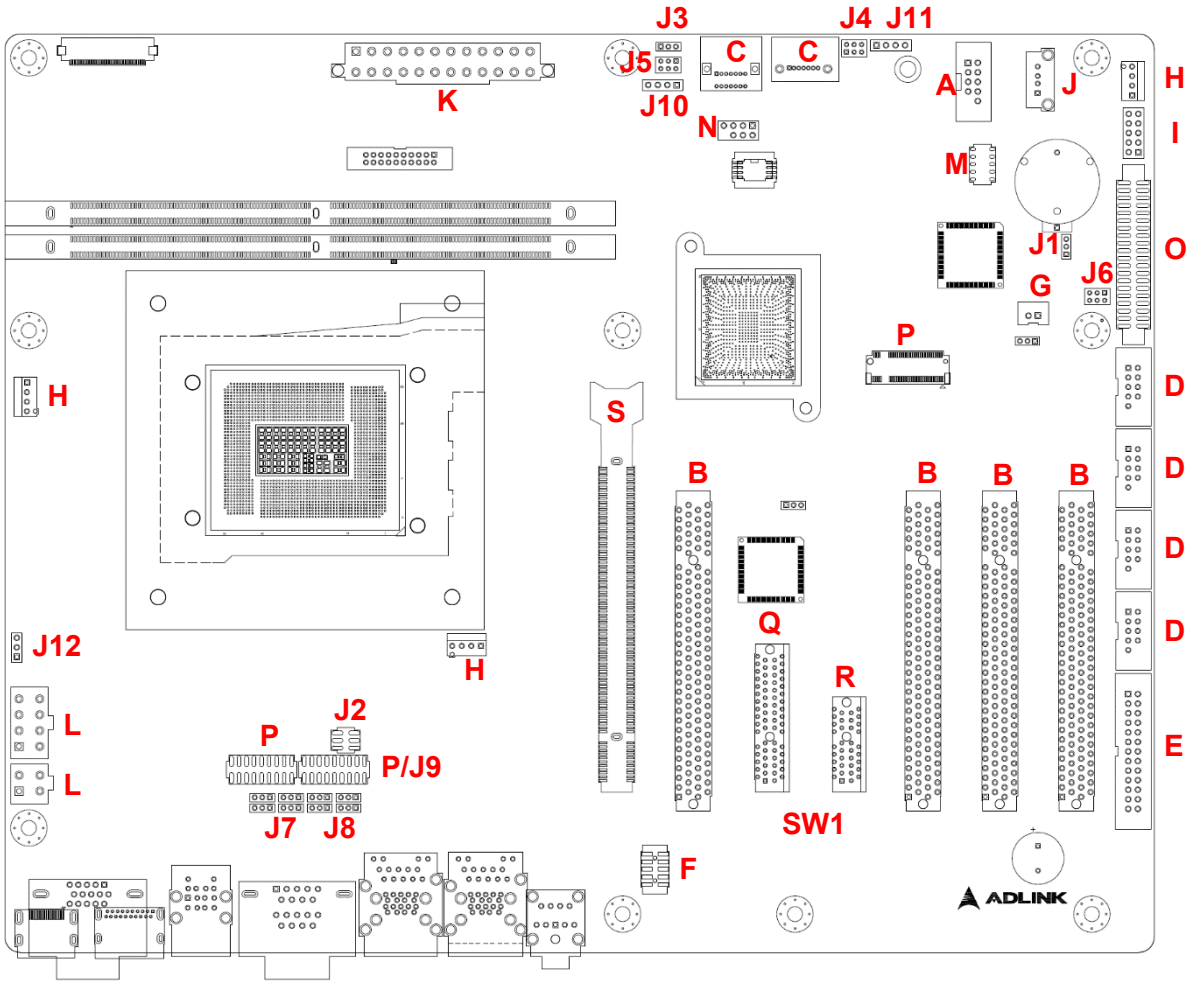


Figure 3: Onboard Connector Locations

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Table 2: Onboard Connector Definitions

Onboard Connectors		
Item	**Description**	**Remarks**
A	USB 2.0 Box Header	USB56
B	PCI Slot	PCI1-4
C	SATA Connectors	SATA1-3
D	Serial Port Header	COM3-6
E	Parallel Port Header	LPT1
F	Front Panel Audio Header	FP_AUDIO1
G	Chassis Intrusion Header	JCASE1
H	CPU FAN Header (4-pin)	CPU_FAN1, SYS_FAN1, SYS_FAN2
I	Front Panel Header	F_PANEL1
J	Vertical USB2.0 Connector	USB9
K	Main Power Supply Connector	ATX1
L	Processor Power Supply Connector	ATX12V1, ATX12V2
M	Debug Header	ESPI1
N	SPI Headers	SPI1
O	GPIO	JDIO1
P	M.2 2280 Slot	M2M1
Q	PCIe x4 Slot	PCIEX4_1
R	PCIe x1 Slot	PCIEX1_1
S	PCIe x16 Slot	PCIEX16_1

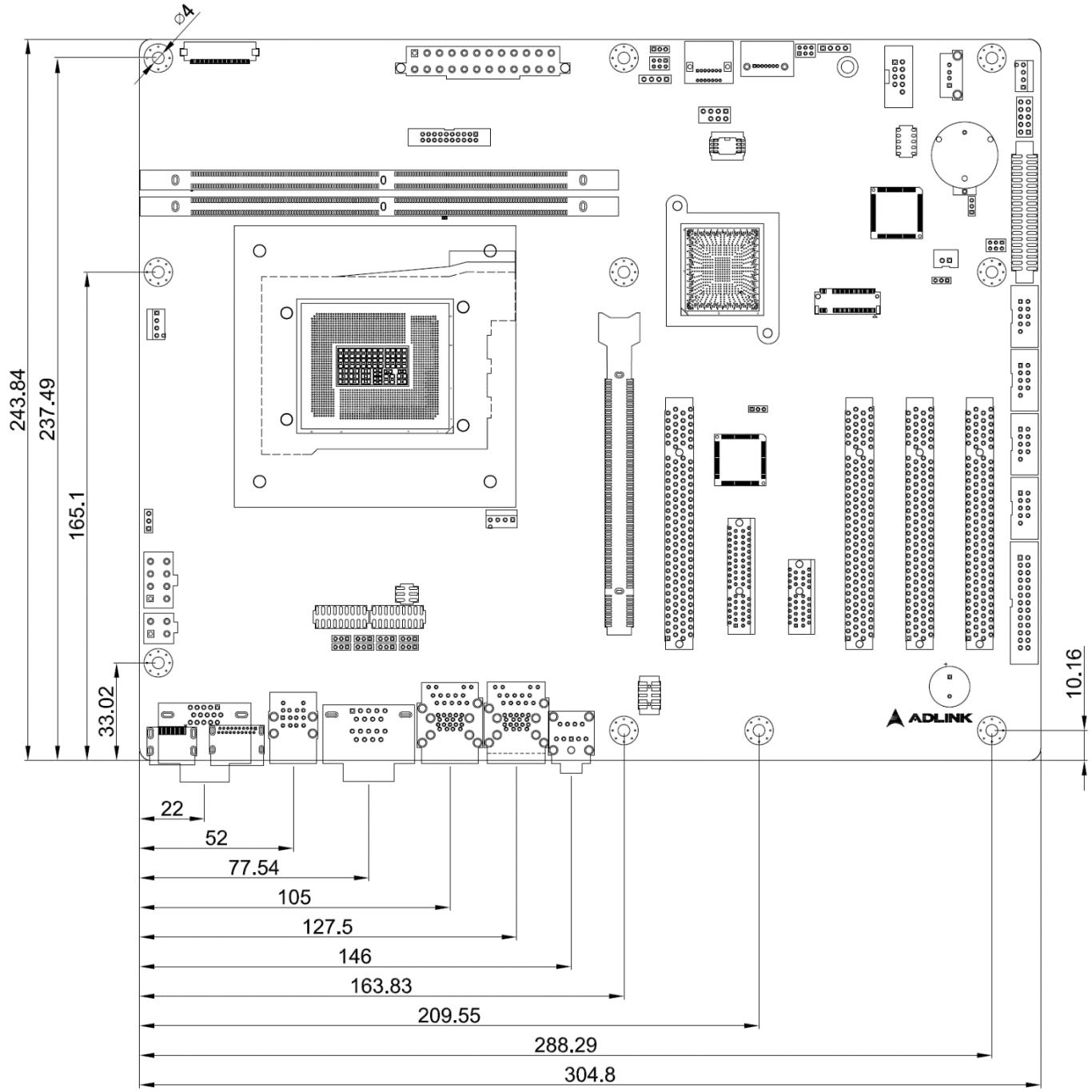
Table 3: Onboard Jumper and Switch Definitions

Onboard Jumpers and Switch		
Item	**Description**	**Remarks**
J1	Clear CMOS	CLCMOS1
J2	COM Port 1 voltage	JCOMPWR1
J3	AT/ATX mode	JPSON1
J4	JI2C1 header power	JPW1
J5	JSMB2 header power	JPW2
J6	JDIO1 header power	JPW3
J7	COM1 master/slave and terminal selection	
J8	COM2 master/slave and terminal selection	
J9	JSETCOM1/2	JSETCOM1/2
J10	JSMB2	JSMB2
J11	JI2C1	JI2C1
J12	JSMB1	JSMB1
SW1	SW1	SW1

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3.3 Mechanical Dimensions

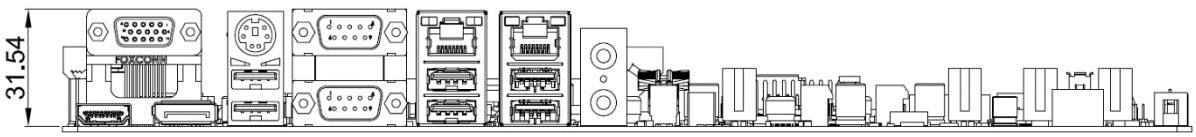
Top View



Dimensions: mm

Figure 4: Mechanical Dimensions - Board

Side View



Dimensions: mm

Figure 5: Mechanical Dimensions - IO Panel

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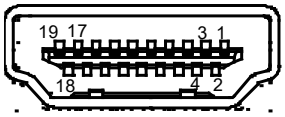
4 Connector Pinouts

See 3.1 Connector Locations on page 7 for connector locations.

4.1 IO Panel Connectors

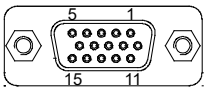
4.1.1 HDMI Connector

Pin	Signal	Pin	Signal
1	HDMI1_CON_DP2	2	GND
3	HDMI1_CON_DN2	4	HDMI1_CON_DP1
5	GND	6	HDMI1_CON_DN1
7	HDMI1_CON_DP0	8	GND
9	HDMI1_CON_DN0	10	HDMI1_CON_CKP
11	GND	12	HDMI1_CON_CKN
13	NC	14	NC
15	HDMI1_DDC_CLK	16	HDMI1_DDC_DATA
17	GND	18	+5V_HDMI
19	HDMI1_CON_HPD		



4.1.2 VGA Connector

Pin	Signal	Pin	Signal
1	VGA_CON_RED	2	VGA_CON_GREEN
3	VGA_CON_BLUE	4	NC
5	GND	6	GND
7	GND	8	GND
9	+5V_VGA	10	GND
11	NC	12	VGA_DDCDAT
13	VGA_CON_HS	14	VGA_CON_VS
15	VGA_DDCCLK		

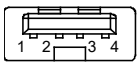


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4.1.3 USB Connectors

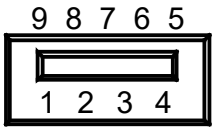
USB 3.0, USB 2.0

Pin	Signal
1	+5 VDC
2	USB D-
3	USB D+
4	GND



USB2.0

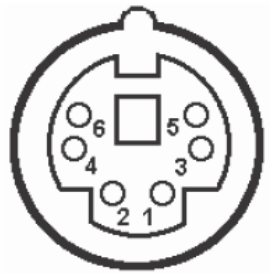
Pin	Signal
1	+5V_USB12
2	USB_CM_N1
3	USB_CM_P1
4	GND
5	USB3_RX_CM_N1
6	USB3_RX_CM_P1
7	GND
8	USB3_TX_CM_N1
9	USB3_TX_CM_P1



USB3.0

4.1.4 PS/2 Combo Connector

Pin	Signal
1	KB_DAT
2	MS_DAT
3	GND
4	+5V_DUAL
5	KB_CLK
6	MS_CLK

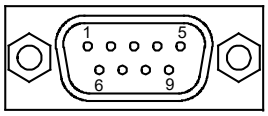


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4.1.5 COM 1-2 Stacked Connector

Top connector COM1, bottom connector COM2

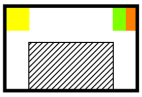
Pin	Signal		
	RS-232	RS-422	RS-485
1	DCD#	Tx-	Tx/Rx-
2	RxD	Tx+	Tx/Rx+
3	TxD	Rx+	N/A
4	DTR#	Rx-	N/A
5	GND	N/A	N/A
6	DSR#	N/A	N/A
7	RTS#	N/A	N/A
8	CTS#	N/A	N/A
9	RI#	N/A	N/A



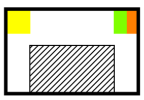
4.1.6 Ethernet Connectors (LAN1, LAN2)

Dual Ethernet controllers based on Intel® i219LM/i225V, support PXE and WOL over both LANs.

LED Indication of RJ45 Connector (LAN1 I219)				
LED	**Position**	**Color**	**LED State**	**NIC State**
Link/ Activity	Left of connector	Yellow	On	Active Connection
			Blinking	Transmit/Receive Activity
			Off	LAN link is not established
Link Speed	Right of connector	Green/ Orange	Orange	1000 Mbps
			Green	100 Mbps
			Off	10 Mbps



LED Indication of RJ45 Connector (LAN2 i225)				
LED	**Position**	**Color**	**LED State**	**NIC State**
Link/ Activity	Left of connector	Yellow	On	Active Connection
			Blinking	Transmit/Receive Activity
			Off	LAN link is not established
Link Speed	Right of connector	Green/ Orange	Green	1000 Mbps
			Orange	2500 Mbps
			Off	10/100 Mbps



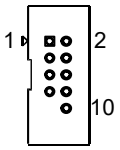
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4.2 Onboard Connectors

See 3.2 Onboard Connectors, Jumpers and Switch Locations on page 8 for connector locations.

4.2.1 USB2.0 Box Header (USB56)

Pin	Signal	Pin	Signal
1	+5V DC	2	+5V DC
3	D -	4	D -
5	D +	6	D +
7	Ground	8	Ground
9	KEY (no pin)	10	No Connect/OC

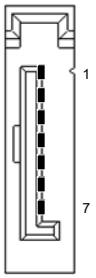


4.2.2 PCI Connector (PCI1-PCI4)

The PCI connector on Expansion Slot #2, #5~#7 supports single-slot and a bus master PCI expansion cards.

4.2.3 SATA Connectors (SATA1-SATA3)

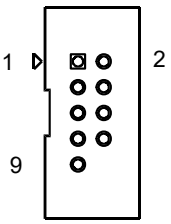
Pin	Signal	Description
1	GND	Ground
2	TXP	Transmit diff data - positive
3	TXN	Transmit diff data - negative
4	GND	Ground
5	RXN	Receive diff data - negative
6	RXP	Receive diff data - positive
7	GND	Ground



4.2.4 Serial Port Header (COM3-COM6)

Serial Port RS-232

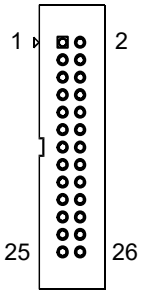
Pin	Signal	Description	Pin	Signal	Description
1	DCD#	Data Carrier Detect	2	DSR#	Data Set Ready
3	RXD	Receive Data	4	RTS#	Request To Send
5	TXD#	Transmit Data	6	CTS#	Clear To Send
7	DTR#	Data Terminal Ready	8	RI#	Ring Indicator
9	GND	GND	10	KEY	No Pin



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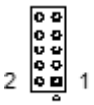
4.2.5 Parallel Port Header (LPT1)

Pin	Signal	Pin	Signal
1	STB#	2	AFD#
3	DATA0	4	ERR#
5	DATA1	6	INIT#
7	DATA2	8	SLIN#
9	DATA3	10	GND
11	DATA4	12	GND
13	DATA5	14	GND
15	DATA6	16	GND
17	DATA7	18	GND
19	ACK#	20	GND
21	BUSY	22	GND
23	PE	24	GND
25	SLCT	26	NC



4.2.6 Front Panel Audio Header (FP_AUDIO1)

Pin	Signal	Pin	Signal
1	[Port 2] SENSE_RETURN_B	2	NC
3	Line-In Port2 Left	4	NC
5	Line-In Port2 Right	6	GND_A
7	MIC-In Port2 Left	8	SIDE OUT Right
9	MIC-In Port2 Right	10	SIDE OUT Left



4.2.7 Chassis Intrusion Header (JCASE1)

Pin	Signal	Description
1	CASEOP#	Case open signal
2	GND	Ground



4.2.8 CPU FAN Header (4-pin) (CPU_FAN1, SYS_FAN1, SYS_FAN2)

Pin	Signal	Description
1	GND	Ground
2	+12 V	FAN Power
3	Tach	FAN Tachometer
4	PWM	FAN PWM

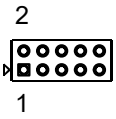


The fan header supports +12 V at 1 A maximum.

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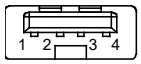
4.2.9 Front Panel Header (F_PANEL1)

Pin	Signal	In/Out	Description	Pin	Signal	In/Out	Description
HDD Activity LED				Power LED			
1	HDD_LED+	Out	Hard disk LED pull-up to +3.3 V	2	PLED_PWR	Out	Power LED pull-up to +3.3_DUAL
3	HDD_LED#	Out	Hard disk active LED	4	SUPLED	Out	Front panel active LED
5	Ground		Ground	6	PANSWIN#	In	Power switch
7	FP_RESET#	In	Reset switch	8	Ground		Ground
9	NC			10	NC		



4.2.10 Vertical USB 2.0 Connector (USB9)

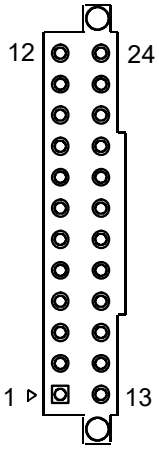
Pin	Signal
1	+5V_DUAL
2	D -
3	D +
4	GND



USB2.0

4.2.11 Main Power Supply Connector (ATX1)

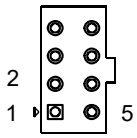
Pin	Signal	Pin	Signal
1	+3.3 V	13	+3.3 V
2	+3.3 V	14	-12 V
3	Ground	15	Ground
4	+5 V	16	PS-ON# (power supply remote on/off)
5	Ground	17	Ground
6	+5 V	18	Ground
7	Ground	19	Ground
8	PWRGD (Power Good)	20	No connect
9	+5 V (Standby)	21	+5 V
10	+12 V	22	+5 V
11	+12 V	23	+5 V
12	No connect	24	Ground



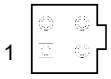
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4.2.12 Processor Power Supply Connector (ATX12V1) (ATX12V2)

Pin	Signal	Pin	Signal
1	Ground	5	+12V
2	Ground	6	+12V
3	Ground	7	+12V
4	Ground	8	+12V

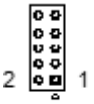


Pin	Signal	Pin	Signal
1	Ground	3	+12V
2	Ground	4	+12V



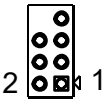
4.2.13 Debug Header (ESPI1)

Pin	Signal	Pin	Signal
1	ESPI_DEG_CLK_R	2	ESPI_DEG_IO1
3	ESPI_RESET#_R	4	ESPI_DEG_IO0
5	ESPI_CS#	6	+3.3V
7	ESPI_DEG_IO3	8	GND
9	ESPI_DEG_IO2	10	+3.3V_DUAL



4.2.14 SPI Headers (SPI1)

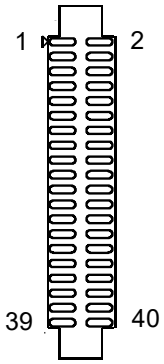
Pin	Signal	Pin	Signal
1	VCC3	2	GND
3	SPI_CS#	4	SPI_CLK
5	SPI_MISO	6	SPI_MOSI
7	HOLD#	8	Key



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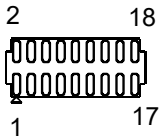
4.2.15 GPIO (JDIO1)

Pin	Signal	Pin	Signal
1	DIO1	2	DIO17
3	DIO2	4	DIO18
5	DIO3	6	DIO19
7	DIO4	8	DIO20
9	DIO5	10	DIO21
11	DIO6	12	DIO22
13	DIO7	14	DIO23
15	DIO8	16	DIO24
17	DIO9	18	DIO25
19	DIO10	20	DIO26
21	DIO11	22	DIO27
23	DIO12	24	DIO28
25	DIO13	26	DIO29
27	DIO14	28	DIO30
29	DIO15	30	DIO31
31	DIO16	32	DIO32
33	NC	34	NC
35	NC	36	NC
37	NC	38	NC
39	GND	40	POWER



4.2.16 JSETCOM1/JSETCOM2

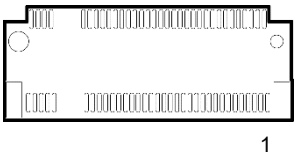
Pin	Signal	Pin	Signal
1	UART1_RXD	2	COM1_RXD485
3	UART1_RXD	4	COM1_RXD422
5	UART1_RXD	6	COM1_RXD232
7	COM1_DCD#	8	COM1_TXD
9	COM1_CN_DCD#	10	COM1_CN_TX
11	TDX485N1	12	RXD485P1
13	COM1_RXD	14	COM1_DTR#
15	COM1_CN_RX	16	COM1_CN_DTR#
17	TDX485P1	18	RXD485N1



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4.2.17 M.2 2280 Slot (M2M1)

Pin	Signal	Pin	Signal
1	GND	2	+3.3V_DUAL
3	GND	4	+3.3V_DUAL
5	M2_PCIE3_RX_N8	6	x
7	M2_PCIE3_RX_P8	8	x
9	GND	10	M2_PCH_SSD_LED#
11	M2_PCIE3_TX_C_N8	12	+3.3V_DUAL
13	M2_PCIE3_TX_C_P8	14	+3.3V_DUAL
15	GND	16	+3.3V_DUAL
17	M2_PCIE3_RX_N7	18	+3.3V_DUAL
19	M2_PCIE3_RX_P7	20	X
21	GND	22	X
23	M2_PCIE3_TX_C_N7	24	X
25	M2_PCIE3_TX_C_P7	26	X
27	GND	28	X
29	M2_PCIE3_RX_N6	30	X
31	M2_PCIE3_RX_P6	32	X
33	GND	34	X
35	M2_PCIE3_TX_C_N6	36	X
37	M2_PCIE3_TX_C_P6	38	SSD_SATA_DEVSLP_R
39	GND	40	X
41	SATA_PE_RX_N0	42	X
43	SATA_PE_RX_P0	44	X
45	GND	46	X
47	SATA_PE_TX_N0	48	X
49	SATA_PE_TX_P0	50	PCIE_M2M_RST#
51	GND	52	M2M_CLKREQ#_R
53	CK_100M_N11	54	M2_PCH_WAKE#
55	CK_100M_P11	56	x
57	GND	58	x
Mechanical Key (M-Key)			
67	X	68	SUSCLK_R
69	M2_SSD_PEDET	70	+3.3V_DUAL
71	GND	72	+3.3V_DUAL
73	GND	74	+3.3V_DUAL
75	GND		

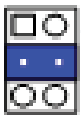
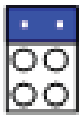
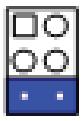


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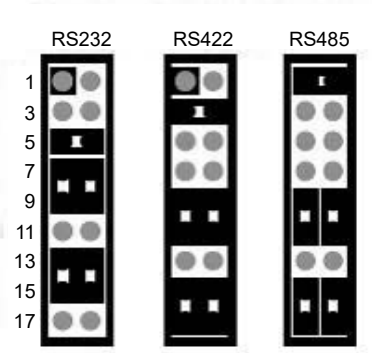
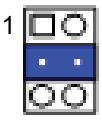
4.3 Jumper and Switch Settings

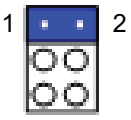
See 3.2 Onboard Connectors, Jumpers and Switch Locations on page 8 for jumper and switch locations.

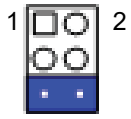
Table 4: Jumper and Switch Definitions

Jumper Block		
Item	Description	Remarks
JCMOS1	Clear CMOS. One 1x3 2.0mm pin header 1-2 (default) = Normal, 2-3 = Clear CMOS	Normal (Default)  1 Clear CMOS  1
JCOMPWR1	COM Power selection. One 2x3 2.0mm pin header 1-2 = +12V, 3-4 (default) = Ring, 5-6 = +5V	Ring 1  2 +12V 1  2 +5V 1  2
JPSON1	AT/ATX mode selection. One 1x3 2.0mm pin header 1-2 = AT Mode; 2-3 (default) = ATX mode	AT mode  1 ATX mode (Default)  1

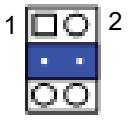
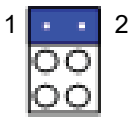
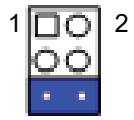
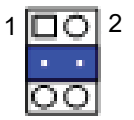
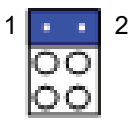
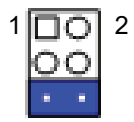
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<table>
<tr><th colspan="3">Jumper Block</th></tr>
<tr><td>JSETCOM1
JSETCOM2</td><td>One 2x9 2.0mm pin header
RS232: 5-6, 7-9, 8-10, 13-15, 14-16 (default)
RS422: 3-4, 9-11, 10-12, 15-17, 16-18
RS485: 1-2, 9-11, 10-12, 15-17, 16-18</td><td>
</td></tr>
<tr><td>COM1_S1-S4
master/slave and terminal selection</td><td><table>
<tr><th>COM1</th><th>S1</th><th>S2</th><th>S3</th><th>S4</th></tr>
<tr><td>RS-232</td><td>2-3</td><td>2-3</td><td>2-3</td><td>2-3</td></tr>
<tr><td>RS-485</td><td>1-2</td><td>1-2</td><td>2-3</td><td>2-3</td></tr>
<tr><td>RS-422</td><td>1-2</td><td>1-2</td><td>1-2</td><td>1-2</td></tr>
</table></td><td>(Default)
</td></tr>
<tr><td>COM2_S1-S4
master/slave and terminal selection</td><td><table>
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<tr><td>RS-232</td><td>2-3</td><td>2-3</td><td>2-3</td><td>2-3</td></tr>
<tr><td>RS-485</td><td>1-2</td><td>1-2</td><td>2-3</td><td>2-3</td></tr>
<tr><td>RS-422</td><td>1-2</td><td>1-2</td><td>1-2</td><td>1-2</td></tr>
</table></td><td>(Default)
</td></tr>
<tr><td>JPW1</td><td>JI2C1 header power.
One 2X3 2.0mm pin header
NC (default)/+5V_DUAL/+3.3V_DUAL</td><td>NC (Default)


+5V_DUAL


+3.3V_DUAL

</td></tr>
</table>

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Jumper Block		
JPW2	Power selection for JSMB1 power pin. One 2x3 2.0mm pin header NC (default)/+5V/+3.3V	NC (Default)  +5V  +3.3V 
JPW3	Power selection for JDIO1 power pin. One 2x3 2.0mm pin header NC (default)/+5V/+12V	NC (Default)  +5V  +12V 
JSMB2	One 1x4 2.0mm pin header. Pin 1: Power with JPW1 Pin 2: SMB_DAT_MAIN Pin 3: SMB_CLK_MAIN Pin 4: GND	1 
JI2C1	One 1x4 2.0mm pin header. Pin 1: Power with JPW2 Pin 2: I2C_DATA Pin 3: I2C_CLK Pin 4: GND	1 

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<table>
<tr><th colspan="3">Jumper Block</th></tr>
<tr><td>JSMB1</td><td>One 1x3 2.54mm pin header for CPU PWM firmware burn-in. Use debug only.

Pin 1: VR_+VCORE_SCL
Pin 2: GND
Pin 3: VR_+VCORE_SDAL</td><td></td></tr>
<tr><td>SW1</td><td>PCIEX4 Slot & M.2 Config. SW1.

<table><tr><th></th><th>SW1 (1)</th><th>SW1 (2)</th></tr><tr><td>SW1</td><td>M.2 (default)
(SWITCH ON)</td><td>PCIEX4
(SWITCH OFF)</td></tr></table></td><td></td></tr>
</table>

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5 Driver Installation

Download the requisite drivers for your system from the IMB-M47H product page at:

https://www.adlinktech.com/Products/Industrial_Motherboards_SBCs/ATXMotherboards/IMB-M47H

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6 System Resources

6.1 System Memory Map

Table 5: System Memory Map

Address Range	Address Range	Size	Description
(4GB-2MB)	FFE00000 – FFFFFFFF	2 MB	High BIOS Area
(4GB-18MB) – (4GB-17MB-1)	FEE00000 – FEEFFFFF	1 MB	MSI Interrupts
(4GB-20MB) – (4GB-19MB-1)	FEC00000 – FECFFFFF	1 MB	APIC Configuration Space
15MB – 16MB	F00000 – FFFFFF	1 MB	ISA Hole
1MB -15MB	100000 - EFFFFF	14MB	Main Memory
0K –1MB	00000 – FFFFFF	1MB	DOS Compatibility Memory

6.2 I/O Map

Table 6: IO Map

Hex Range	Device
0000-0CF7	PCI Express Root Complex
0D00-FFFF	PCI Express Root Complex
0020-0021	Programmable interrupt controller
0024-0025	Programmable interrupt controller
0028-0029	Programmable interrupt controller
002C-002D	Programmable interrupt controller
0030-0031	Programmable interrupt controller
0034-0035	Programmable interrupt controller
0038-0039	Programmable interrupt controller
003C-003D	Programmable interrupt controller
00A0-00A1	Programmable interrupt controller
00A4-00A5	Programmable interrupt controller
00A8-00A9	Programmable interrupt controller
00AC-00AD	Programmable interrupt controller
00B0-00B1	Programmable interrupt controller
00B4-00B5	Programmable interrupt controller
00B8-00B9	Programmable interrupt controller
00BC-00BD	Programmable interrupt controller
04D0-04D1	Programmable interrupt controller
04D0-04D1	Programmable interrupt controller
002E-002F	Motherboard resources
004E-004F	Motherboard resources
0061-0061	Motherboard resources

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Hex Range	Device
0063-0063	Motherboard resources
0065-0065	Motherboard resources
0067-0067	Motherboard resources
0070-0070	Motherboard resources
0080-0080	Motherboard resources
0092-0092	Motherboard resources
00B2-00B3	Motherboard resources
0680-069F	Motherboard resources
FFFF-FFFF	Motherboard resources
1800-18FE	Motherboard resources
164E-164F	Motherboard resources
0040-0043	System timer
0050-0053	System timer
0070-0077	System CMOS/real time clock
00F0-00F0	Numeric data processor
02E0-02E7	COM5
02E8-02EF	COM4
02F8-02FF	COM2
0378-037F	Printer Port
03E0-03E7	COM6
03E8-03EF	COM3
03F8-03FF	COM1
E000-EFFF	Chipset PCI Express Root Port
E000-EFFF	PCI-to-PCI Bridge
F000-F03F	Intel HD Graphic 630
F040-F05F	SMBUS
F090-F097	Standard SATA AHCI Controller
F080-F083	Standard SATA AHCI Controller
F060-F07F	Standard SATA AHCI Controller

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6.3 Interrupt Request (IRQ) Lines

6.3.1 IRQ Lines PIC Mode

Table 7: IRQ Lines PIC Mode

IRQ#	Device
0	System timer
3	COM2
4	COM1
5	COM3, COM4
8	System CMOS/real time clock
10	COM5, COM6
13	Numeric data processor
14	Intel Serial IO GPIO Host Controller
15	Intel Chipset Smbus
16	HD Audio Controller

Note: These IRQs can be used for PCI devices when onboard device is disabled.

6.3.2 IRQ Lines APIC Mode

Table 8 IRQ Lines APIC Mode

IRQ#	Typical Interrupt Resource	Connected to Pin	Available
0	System Counter	N/A	No
1	N/A	N/A	
2	N/A	N/A	
3	Serial Port 2 (COM2)	IRQ3 via SERIRQ / PIRQ	Note (1)
4	Serial Port 1 (COM1)	IRQ4 via SERIRQ / PIRQ	Note (1)
5	Serial Port3 (COM3)	IRQ5 via SERIRQ / PIRQ	Note (1)
5	Serial Port4 (COM4)	IRQ5 via SERIRQ / PIRQ	Note (1)
7	N/A	N/A	
8	Real-time clock	N/A	No
9	N/A	N/A	
10	Serial Port5 (COM5)	IRQ10 via SERIRQ / PIRQ	Note (1)
10	Serial Port6 (COM6)	IRQ10 via SERIRQ / PIRQ	Note (1)
12	N/A	N/A	
13	Math Processor	N/A	Note (1)
14	Intel IO GPIO Host Controller	N/A	Note (1)
16	High Definition Audio Controller	N/A	
54-511	Microsoft ACPI-Compliant System	N/A	Note (1)

Note: These IRQs can be used for PCI devices when onboard device is disabled.

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6.4 PCI Features

6.4.1 PCI Configuration Space Map

Table 9 PCI Configuration Space Map

Bus Number	Device Number	Function Number	Routing	Description
00h	00h	00h	N/A	Intel Host Bridge
00h	02h	00h	Internal	Intel VGA Controller
00h	14h	00h	Internal	Intel USB 3.0 XHCI
00h	14h	02h	Internal	Intel Data acquisition/signal process
00h	16h	00h	Internal	Intel Communication device
00h	17h	00h	Internal	Intel AHCI 1.0 controller
00h	1Dh	00h	Internal	Intel PCI-to-PCI bridge PCIe
00h	1Dh	03h	Internal	Intel PCI-to-PCI bridge PCIe
00h	1Fh	00h	Internal	Intel ISA bridge
00h	1Fh	03h	Internal	Intel HD Audio Device
00h	1Fh	04h	Internal	Intel SMBU
00h	1Fh	05h	Internal	Intel Controller
00h	1Fh	06h	Internal	Intel Ethernet Controller
01h	00h	00h	Internal	Intel Ethernet Controller PCIe
02h	00h	00h	Internal	ITE PCI to-PCI Bridge

Note: The bus number change if the PEG/PCIE port has a device.

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6.4.2 PCI Interrupt Routing Map

Table 10 PCI Interrupt Routing Map

INT Line	LpcBridge	High Definition Audio	SMBus	PCIE Root Port #9 (LAN1)	PCIE Root Port #5 (PCIE x4)	PCIE Root Port #12 (ITE8892)
Int0	INTA:16	INTA:16	INTA:16	INTA:16	INTA:16	INTD:19
Int1	INTB:17			INTB:17	INTB:17	INTA:16
Int2	INTC:18			INTC:18	INTC:18	INTB:17
Int3	INTD:19			INTD:19	INTD:19	INTC:18

INT Line	PCI Slot 1	PCI Slot 2	PCI Slot 3	PCI Slot 4	PCI Slot 5	PCIE Root Port #11 (LAN2)
Int0	INTA:17	INTA:18	INTA:19	INTA:16	INTA:17	INTC:18
Int1	INTB:18	INTB:19	INTB:16	INTB:17	INTB:18	INTD:19
Int2	INTC:19	INTC:16	INTC:17	INTC:18	INTC:19	INTA:16
Int3	INTD:16	INTD:17	INTD:18	INTD:19	INTD:16	INTB:17

6.5 SMBus Slave Addresses

Table 11 SMBus Slave Addresses

Device	Address
DIMM A	A0h
DIMM B	A4h

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7 BIOS Settings

7.1 Menu Structure

This section presents the primary menus of the BIOS Setup Utility. Use the following table as a quick reference for the contents of the BIOS Setup Utility. The subsections describe the submenus and options foreach menu item. The default options are presented in **bold**, and the function of each setting is described in the right hand column of the table.

Main	**Advanced**	**Chipset**
- BIOS Information - System Date - System Time	- CPU Configuration ► - Power & Performance ► - PCH-FW Configuration ► - Trusted Computing ► - ACPI Settings ► - NCT6126D Super IO Configuration ► - Hardware Monitor► - S5 RTC Wake Settings ► - Serial Port Console Redirection ► - USB Configuration ► - Network Stack Configuration ► - NVME Configuration ►	- System Agent (SA) Configuration ► - PCH-IO Configuration ►
Security	**Boot**	**Save & Exit**
- Administrator Password - User Password	- Boot Configuration ► - Fixed Boot Oder Priorities ►	- Save Options ► - Default Options ► - Boot Override ►

Notes:

► indicates a submenu
Gray text indicates info only

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7.2 Main Menu

The Main Menu provides read-only information about your system and also allows you to set the System Date andTime. Refer to the tables below the screen shot of this menu for details of the submenus and settings.

7.2.1 Main > BIOS Information

Feature	Options	Description
BIOS Vendor	American Megatrends	
Core Version	x.xx x.xx x64	
Compliancy	UEFI x.x; PI x.x	
Project Version	IMB-M47H_xxxxx	
Build Date and Time	mm/dd/yyyy hh:mm:ss	
Access Level	Administrator	
Memory Information		
Total Memory	xxxxx MB	
Memory Frequency	xxxx MT/s	
Power Type	[ATX Mode]	
System Date	mm/dd/yyyy	Sets the system date. Use <Tab> to switch between elements.
System Time	hh:mm:ss	Sets the system time. Use <Tab> to switch between elements.

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7.3 Advanced Menu

This menu contains the settings for most of the user interfaces in the system.

7.3.1 Advanced > CPU Configuration

Feature	Options	Description
Performance-core Information	Info Only	Displays the P-core Information
Frequency	Info Only	xxxx MHz
L1 Data Cache	Info Only	CPU L1 Data Cache
L1 Instruction Cache	Info Only	CPU L1 Instruction Cache
L2 Cache	Info Only	CPU L2 Cache
L3 Cache	Info Only	CPU L3 Cache
L4 Cache	Info Only	CPU L4 Cache
ID	Info Only	CPU ID
Brand String	Info Only	CPU Info
VMX	Info Only	CPU Virtual Machine Extension
SMX/TXT	Info Only	CPU Safer Mode Extension / Trusted Execution Technology
TXT Crash Code	Info Only	
TXT SPAD	Info Only	
Boot Guard Status	Info Only	
Boot Guard ACM Policy Status	Info Only	
Boot Guard SACM Information	Info Only	
Intel (VMX) Virtualization Technology	Disabled **Enabled**	When enabled, a VMM can utilize the additional hardware capabilities provided by Vanderpool Technology.
Active Performance-cores	**All**/1/2/3/4/5 depend on CPU sku	Number of P-cores to enable in each processor package. Note: Number of Cores and E-cores are looked at together. When both are {0,0}, Pcode will enable all cores.
Hyper-Threading	Disabled **Enabled**	Enables or Disables Hyper-Threading

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7.3.2 Advanced > Power & Performance > CPU – Power Management Control

Feature	Options	Description
Intel SpeedStep	Disabled **Enabled**	Allows more than two frequency ranges to be supported.
Turbo Mode	Disabled **Enabled**	Enable/Disable processor Turbo Mode.
C states	Disabled **Enabled**	Enables/Disables CPU Power Management. Allows CPU to go to C States when it's not 100%utilized.
Enhanced C-state	Disabled **Enabled**	Enables or Disables C1E. When enabled, CPU willswitch to minimum speed when all cores enter C- State.
Package C state limit	C0/C1 C2 C3 **Auto**	Maximum Package C State Limit Setting. CPU Default: Leaves to Factory default value. Auto: Initializes to deepest available Package C State Limit.

7.3.3 Advanced > PCH-FW Configuration

Feature	Options	Description
ME Firmware Version	Info Only	PCH Firmware Version
ME Firmware Mode	Info Only	PCH Firmware Mode
ME Firmware SKU	Info Only	PCH Firmware SKU
ME Firmware Status 1	Info Only	PCH Firmware Status
ME Firmware Status 2	Info Only	PCH Firmware Status
ME Firmware Status 3	Info Only	PCH Firmware Status
ME Firmware Status 4	Info Only	PCH Firmware Status
ME Firmware Status 5	Info Only	PCH Firmware Status
ME Firmware Status 6	Info Only	PCH Firmware Status
ME State	Disabled **Enabled**	When Disabled ME will be put into ME Temporarily Disabled Mode.
ME UnLock Control	**Lock** Unlock	ME Unlock control function. Set UnLock will system shutdown for active function.

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7.3.4 Advanced > Trusted Computing

Feature	Options	Description
Security Device Support	Disable **Enable**	Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A interface will not be available.
SHA256 PCR Bank	Disabled **Enabled**	Enable or Disable SHA256 PCR Bank
Pending operation	**None** TPM Clear	Schedule an Operation for the Security Device.

7.3.5 Advanced > ACPI Settings

Feature	Options	Description
Enable Hibernation	Disabled **Enabled**	Enables or Disables System ability to Hibernate. (OS/S4 Sleep State)
ACPI Sleep State	Suspend Disabled **S3 (Suspend to RAM)**	Select the highest ACPI sleep state the system will enter when the SUSPEND button is pressed.
S3 Video Repost	**Disabled** Enabled	Enables or Disables S3 Video Repost
PCIE# Wake from S5	**Disabled** Enabled	Enables or Disables PCIE to wake the system from S5.
Wake on Ring	**Disabled** Enabled	Enables or Disables wake on ring function under ACPI S3/S4/S5.

7.3.6 Advanced > NCT6126D Super IO Configuration

Feature	Options	Description
Serial Port 1 Configuration▶	Submenu	Sets Parameters of Serial Port 1
Serial Port 2 Configuration▶	Submenu	Sets Parameters of Serial Port 2
Serial Port 3 Configuration▶	Submenu	Sets Parameters of Serial Port 3
Serial Port 4 Configuration▶	Submenu	Sets Parameters of Serial Port 4
Serial Port 5 Configuration▶	Submenu	Sets Parameters of Serial Port 5
Serial Port 6 Configuration▶	Submenu	Sets Parameters of Serial Port 6
Parallel Port Configuration▶	Submenu	Sets Parameters of Parallel Port (LPT/LPTE)
WatchDog Count Mode	**Second** Minute	WatchDog Count Mode Selection
WatchDog TimeOut Value	**0**	WatchDog Timeout Value, 0 = disabled.
Chassis Opened Warning	**Disabled** Enabled	Enables or Disables chassis intrusion detection. Note: If chassis tamper occurs, you can only enter setup to clear this error.
ErP/EuP S5 Support	**Disabled** Enabled	Enables or Disables ErP/EuP S5 support

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7.3.7 Advanced > NCT6126D Super IO Configuration > Serial Port 1 Configuration

Feature	Options	Description
Serial Port	Disabled **Enabled**	Enable or Disable Serial Port (COM)
Device Settings	IO=3F8hIRQ=4	Serial Port device settings
Change Settings	**Auto**	Selects optimal setting for Super IO Device
RS485 Auto Flow	**Disabled** Enabled	Enables or Disables RS485 Auto Flow Control Function (Make sure to set RS485 on the COM1 jumper header if this setting is enabled.)

7.3.8 Advanced > NCT6126D Super IO Configuration > Serial Port 2 Configuration

Feature	Options	Description
Serial Port	Disabled **Enabled**	Enables or Disables Serial Port (COM)
Device Settings	IO=2F8hIRQ=3	Serial Port device settings
Change Settings	**Auto**	Selects optimal settings for Super IO Device
RS485 Auto Flow	**Disabled** Enabled	Enables or Disables RS485 Auto Flow Control Function (Make sure to set RS485 on the COM2 jumper header if this setting is enabled.)

7.3.9 Advanced > NCT6126D Super IO Configuration > Serial Port 3 Configuration

Feature	Options	Description
Serial Port	Disabled **Enabled**	Enables or Disables Serial Port (COM)
Device Settings	IO=3E8h IRQ=5	Serial Port device settings
Change Settings	**Auto**	Selects optimal settings for Super IO Device

7.3.10 Advanced > NCT6126D Super IO Configuration > Serial Port 4 Configuration

Feature	Options	Description
Serial Port	Disabled **Enabled**	Enables or Disables Serial Port (COM)
Device Settings	IO=2E8h; IRQ=10	Serial Port device settings
Change Settings	**Auto**	Selects optimal settings for Super IO Device

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7.3.11 Advanced > NCT6126D SUPER IO Configuration > Serial Port 5 Configuration

Feature	Options	Description
Serial Port	Disabled **Enabled**	Enables or Disables Serial Port (COM)
Device Settings	IO=220h; IRQ=6	Serial Port device settings
Change Settings	**Auto**	Selects optimal settings for Super IO Device

7.3.12 Advanced > NCT6126D SUPER IO Configuration > Serial Port 6 Configuration

Feature	Options	Description
Serial Port	Disabled **Enabled**	Enables or Disables Serial Port(COM)
Device Settings	IO=228h; IRQ=15	Serial Port device settings
Change Settings	**Auto**	Selects optimal settings for Super IO Device

7.3.13 Advanced > NCT6126D SUPER IO Configuration > Parallel Port Configuration

Feature	Options	Description
Parallel Port	Disabled **Enabled**	Enables or Disables Parallel Port (LPT/LPTE)
Device Settings	IO=378h; IRQ=7	Parallel Port device settings
Change Settings	**Auto**	Selects optimal settings for Super IO Device
Device Mode	**STD Printer Mode** SPP Mode EPP-1.9 and SPP Mode EPP-1.7 and SPP Mode ECP mode ECP and EPP 1.9 Mode ECP and EPP 1.7 Mode	Changes the Printer Port mode.

7.3.14 Advanced > NCT6126D SUPER IO Configuration

Feature	Options	Description
WatchDog Count Mode	**Second** Minute	WatchDog Count Mode Selection
WatchDog TimeOut Value	**0**	Fill WatchDof TimeOut value, 0 mean disabled
Chassis Opened Warning	**Disabled** Enabled	Select whether to enable Chassis Intrusion Detection.
ErP/EuP S5 Support	**Disabled** Enabled	Enable/Disable ErP/EuP S 5 Support.

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7.3.15 Advanced > Hardware Monitor

Feature	Options	Description
Smart Fan Function	Disabled **Enabled**	Enables or Disables Smart Fan Function
Smart Fan Mode Configuration▶	Submenu	Smart Fan Mode Configuration

7.3.16 Advanced > Hardware Monitor > Smart Fan > Smart Fan Mode Configuration – SMART FAN IV Mode

Feature	Options	Description
xxxFAN Temperature 1	**30**	Input the Smart Fan IV Temperature 1
xxxFAN PWM 1	**1**	Input the Smart FAN IV PWM 1 Value
xxxFAN Temperature 2	**40**	Input the Smart Fan IV Temperature 2
xxxFAN PWM 2	**84**	Input the Smart FAN IV PWM 2 Value
xxxFAN Temperature 3	**50**	Input the Smart Fan IV Temperature 3
xxxFAN PWM 3	**168**	Input the Smart FAN IV PWM 3 Value
xxxFAN Temperature 4	**60**	Input the Smart Fan IV Temperature 3
xxxFAN PWM 4	**255**	Input the Smart FAN IV PWM 3 Value
xxxFAN Tolerance of Target Temp	**5**	FAN Tolerance of Target Temperature
xxxFAN Critical Temperature	**90**	Input the Smart IV Critical Temperature
xxxFAN Critical Temp Tolerance	**1**	Input Tolerance of Critical Temperature (Range: 0~7)

7.3.17 Advanced > Hardware Monitor > Smart Fan > Smart Fan Mode Configuration – Manual Mode

Feature	Options	Description
SYS Smart Fan1 Mode	Manual Mode Thermal Cruise Mode **Smart FAN IV Mode**	SYS Smart Fan1 Mode
SYS expect PWM Output/DC Voltage	**255**	System FAN1 expect PWM Output/DC Voltage
CPU Smart Fan Mode	Manual Mode Thermal Cruise Mode **Smart FAN IV Mode**	CPU Smart Fan Mode
CPU expect PWM Output/DC Voltage	**255**	CPU FAN expect PWM Output/DC Voltage
SYS Smart Fan2 Mode	Manual Mode Thermal Cruise Mode **Smart FAN IV Mode**	SYS Smart Fan2 Mode
SYS expect PWM Output/DC Voltage	**255**	CHA FAN2 expect PWM Output/DC Voltage

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7.3.18 Advanced > S5 RTC Wake Settings

Feature	Options	Description
Wake System From S5	**Disabled** Enabled	Enables or Disables System wake on alarm event.

7.3.19 Advanced > Serial Port Console Redirection

Feature	Options	Description
COM1		
Console Redirection	**Disabled** Enabled	Enables or Disables Console Redirection
Console Redirection Settings	Submenu	Show when Console Redirection is enabled

7.3.20 Advanced > Serial Port Console Redirection > Console Redirection Settings

Feature	Options	Description
Terminal Type	VY100 VT100+ VT- UTF8 **ANSI**	Type Select
Bits per second	9600 19200 38400 57600 **115200**	Select serial port transmission speed.
Data Bits	7 **8**	Data Bits
Parity	**None** Even Odd Mark Space	A parity bit can be sent with the data bits to detect some transmission error.
Stop Bits	**1** 2	Stop bits indicate the end of a serial data packet.
Flow Control	**None** Hardware RTS/CTS	Flow control can prevent data loss from buffer overflow.
VT-UTF8 Combo Key Support	Disabled **Enabled**	Enables or Disables VT-UTF8 Combo Key
Recorder Mode	**Disabled** Enabled	With this mode enabled, only a test will be sent.
Resolution 100x31	**Disabled** Enabled	Enables or Disables extended terminal resolution.
Putty KeyPad	**VT100** LINUX XTERMR6 SCO ESCN VT400	Selects FunctionKey and KeyPad on Putty.

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7.3.21 Advanced > USB Configuration

Feature	Options	Description
USB Devices	Show current USB devices	
USB Mass Storage Driver Support	**Enabled** Disabled	Enable/Disable USB Mass Storage Driver Support

7.3.22 Advanced > Network Stack Configuration

Feature	Options	Note
Network Stack	**Disabled** Enabled	Enables or Disables UEFI Network Stack.

7.3.23 Advanced > NVMe Configuration

Feature	Options	Description
NVMe controller and Drive information	Info Only	NVMe Device Options Settings
Show device	Info Only	

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7.4 Chipset Menu

This menu contains settings for other user interfaces in the system.

7.4.1 Chipset > System Agent (SA) Configuration

Feature	Options	Description
VT-d Capability	Info Only	
Memory Configuration	Submenu	Memory Configuration Parameters
Graphics Configuration	Submenu	Graphics Configuration
PCI Express Configuration	Submenu	PCI Express Options
VT-d	Disabled **Enabled**	VT-d capability

7.4.2 Chipset > System Agent (SA) Configuration > Memory Configuration

Feature	Options	Description
Memory RC Version	Info Only	
Memory Frequency	Info Only	
tCL-tRCD-tRP-tRAS	Info Only	Memory Timings
Total Memory	Info Only	
DIMM_A1	Info Only	
DIMM_B1	Info Only	
Max TOULUD	**Dynamic** / 1 GB / 1.25 GB / 1.5 GB / 1.75 GB / 2 GB / 2.25 GB / 2.5 GB / 2.75 GB / 3 GB / 3.25 GB / 3.5 GB	Maximum value of TOULUD

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7.4.3 Chipset > System Agent (SA) Configuration > Graphics Configuration

Feature	Options	Description
Primary Display	**Auto** IGFX PEG Slot PCIE	Selects which of IGFX/PEG/PCIE Graphics device should be Primary Display
Internal Graphics	**Auto** Disabled Enabled	Keeps IGFX enabled based on the setup options.
PSMI Support	**Disabled** Enabled	PSMI Enable/Disable.
DVMT Pre-Allocated	0M 32M 64M 96M 128M 160M 4M 8M 12M 16M 20M 24M 28M 32M/F7 36M 40M 44M 48M 52M 56M **60M**	Selects DVMT 5.0 Pre-Allocated (Fixed) Graphics Memory size usedby the Internal Graphics Device.

7.4.4 Chipset > System Agent (SA) Configuration > PCI Express Configuration

Feature	Options	Description
PCI Express Root Port 2(x16 Slot)	Submenu	PCI Express Root Port Setting
PCI Express Root port 2	Disabled **Enabled**	Control the PCI Express Root Port
ASPM	**Disabled** L0S L1 L0sL1	Set the ASPM Level: Force L0s – Force all links to L0s State
PCIe Speed	**Auto** Gen1 Gen2 Gen3 Gen4 Gen5	Configure PCIe Speed
Detect Non-Compliance Device	**Disabled** Enabled	Detects Non-Compliance PCI Express Device in PEG

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7.4.5 Chipset > PCH-IO Configuration

Feature	Options	Description
PCI Express Configuration	Submenu	PCI Express Configuration settings
SATA Configuration	Submenu	SATA Device Options Setting
USB Configuration	Submenu	USB Configuration settings
HD Audio Configuration	Submenu	HD Audio Subsystem Configuration Settings
Serial IO Configuration	Submenu	Serial IO Configuration Settings
LAN1 Controller	Disabled **Enabled**	Enables or Disables onboard NIC.
LAN1 PXE OpROM	**Disabled** Enabled	Shows if Network to Legacy
Wake on Lan Enable	**Disabled** Enabled	Enables or Disables integrated LAN to wake the system.
LAN2 Controller	Disabled **Enabled**	Enables or Disables onboard Lan2
LAN2 PXE OpROM	**Disabled** Enabled	Shows if Network to Legacy
Restore AC Power Loss	Power on **Power Off** Last State	Specifies what state to go to when power is re-applied after a power failure (G3 state).
Flash Protection Range Registers (FPRR)	**Disabled** Enabled	Enable Flash Protection Range Registers
GPIO Group Control	**Disabled** Enabled	Configures the digital GPIO pins

7.4.6 Chipset > PCH-IO Configuration > PCI Express Configuration

Feature	Options	Description
PCI Express Configuration	Info only	
PCI Express Root Port 2(LAN2)	Info only	
PCI Express Root Port 3(ITE8892E)	Submenu	PCI Express Root Port Settings.
PCI Express Root Port 4(x1 Slot)	Submenu	PCI Express Root Port Settings.
PCI Express Root Port 5(x4 Slot) (Key M)	Submenu	PCI Express Root Port Settings.
PCI Express Root Port 15(LAN1)	Info only	

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7.4.7 Chipset > PCH-IO Configuration > PCI Express Configuration > PCI Express Root Port 3

Feature	Options	Description
PCI Express Root Port 3	Disabled **Enabled**	Control the PCI Express Root Port.
ASPM 3	Auto L1 **Disabled**	Sets the ASPM Level: Force L1 – Force all link to L1 State AUTO – BIOS auto configure DISABLE – Disables ASPM
PCIe Speed	**Auto** Gen1 Gen2 Gen3 Gen4	Configures PCIe Speed
Detect Non-Compliance Device	**Disabled** Enabled	Detects Non-Compliance PCI Express Device. If enabled,it will take more time at POST time.

7.4.8 Chipset > PCH-IO Configuration > PCI Express Configuration > PCI Express Root Port 4

Feature	Options	Description
PCI Express Root Port 4	Disabled **Enabled**	Control the PCI Express Root Port.
ASPM 4	Auto L1 **Disabled**	Sets the ASPM Level: Force L1 – Force all link to L1 State AUTO – BIOS auto configure DISABLE – Disables ASPM
PCIe Speed	**Auto** Gen1 Gen2 Gen3 Gen4	Configures PCIe Speed
Detect Non-Compliance Device	**Disabled** Enabled	Detects Non-Compliance PCI Express Device. If enabled,it will take more time at POST time.

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7.4.9 Chipset > PCH-IO Configuration > PCI Express Configuration > PCI Express Root Port 5

Feature	Options	Description
PCI Express Root Port 5	Disabled **Enabled**	Control the PCI Express Root Port.
ASPM 5	Auto L1 **Disabled**	Sets the ASPM Level: Force L1 – Force all link to L1 State AUTO – BIOS auto configure DISABLE – Disables ASPM
PCIe Speed	**Auto** Gen1 Gen2 Gen3 Gen4	Configures PCIe Speed
Detect Non-Compliance Device	**Disabled** Enabled	Detects Non-Compliance PCI Express Device. If enabled,it will take more time at POST time.

7.4.10 Chipset > PCH-IO Configuration > SATA Configuration

SATA Configuration	Options	Note
SATA Controller(s)	Disabled **Enabled**	Enable/Disable SATA Device
SATA Mode Selection	**AHCI**	Determines how SATA controllers operate.
Serial ATA Port 1	Show device	
Software Preserve		
Port 1	Disabled **Enabled**	Enables or Disables SATA Port
Serial ATA Port 2	Show device	
Software Preserve		
Port 2	Disabled **Enabled**	Enables or Disables SATA Port
Serial ATA Port 3	Show device	
Software Preserve		
Port 3	Disabled **Enabled**	Enables or Disables SATA Port
Serial ATA Port 4(M.2 KeyM)	Show device	
Software Preserve		
Port 4(M2 Port)	Disabled **Enabled**	Enables or Disables SATA Port

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7.4.11 Chipset > PCH-IO Configuration > USB Configuration

Feature	Options	Description
USB12 Standby Power Support	Disabled **Enabled**	Enable/Disable USB standby power
USB34 Standby Power Support	Disabled **Enabled**	Enable/Disable USB standby power
USB56 Standby Power Support	Disabled **Enabled**	Enable/Disable USB standby power
USB78 Standby Power Support	Disabled **Enabled**	Enable/Disable USB standby power
USB910 Standby Power Support	Disabled **Enabled**	Enable/Disable USB standby power

7.4.12 Chipset > PCH-IO Configuration > HD Audio Configuration

Feature	Options	Description
HD Audio	Disabled **Enabled**	Control Detection of the HD-Audio device.

7.4.13 Chipset > PCH-IO Configuration > Serial IO Configuration

Feature	Options	Description
I2C0 Controller	Disabled **Enabled**	Enables/Disables Serial IO Controller
I2C3 Controller	Disabled **Enabled**	Enables/Disables Serial IO Controller
Serial IO I2C0 Settings	Info only	
Serial IO I2C3 Settings	Info only	

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7.5 Security Menu

Feature	Options	Description
Administrator Password	Enter password	Set Administrator Password
User Password	Enter password	Set User Password

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7.6 Boot Menu

Feature	Options	Description
Boot Configuration	**Info Only**	
Setup Prompt Timeout	**1** (seconds)	Number of seconds to wait for setup activation key. 65535(0xFFFF) means infinite waiting.
Bootup NumLock State	**On** Off	Select the keyboard NumLock state.
Quiet Boot	Disabled **Enabled**	Enables or Disables Quiet Boot option
Boot mode select	**UEFI**	Selects Legacy or UEFI boot mode
FIXED BOOT ORDER Priorities	**Info Only**	
Boot Option #1	USB Key	Sets the system boot order.
Boot Option #2	CD/DVD	Sets the system boot order.
Boot Option #3	USB CD/DVD	Sets the system boot order.
Boot Option #4	USB Floppy	Sets the system boot order.
Boot Option #5	NVME	Sets the system boot order.
Boot Option #6	Hard Disk	Sets the system boot order.
Boot Option #7	Network	Sets the system boot order.

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7.7 Save & Exit Menu

Save & Exit	Options	Note
Save Changes and Exit	Yes No	Exits system setup after saving the changes.
Discard Changes and Exit	Yes No	Exits system setup without saving any changes.
Save Changes and Reset	Yes No	Resets the system after saving the changes.
Default Options		
Restore Defaults	Yes No	Restores or Loads Default values for all Setup options.
Boot Override		
Launch EFI Shell from filesystem device	Yes No	Attempts to Launch EFI Shell application (Shell.efi) from one of the available filesystem devices

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Safety Instructions

Read and follow all instructions marked on the product and in the documentation before you operate your system. Retain all safety and operating instructions for future use.

- Please read these safety instructions carefully.
- Please keep this User‘s Manual for later reference.
- Read the specifications section of this manual for detailed information on the operating environment of this equipment.
- When installing/mounting or uninstalling/removing equipment, turn off the power and unplug any power cords/cables.
- To avoid electrical shock and/or damage to equipment:
  - Keep equipment away from water or liquid sources.
  - Keep equipment away from high heat or high humidity.
  - Keep equipment properly ventilated (do not block or cover ventilation openings).
  - Make sure to use recommended voltage and power source settings.
  - Always install and operate equipment near an easily accessible electrical socket-outlet.
  - Secure the power cord (do not place any object on/over the power cord).
  - Only install/attach and operate equipment on stable surfaces and/or recommended mountings.
  - If the equipment will not be used for long periods of time, turn off and unplug the equipment from its power source.
- Never attempt to fix the equipment. Equipment should only be serviced by qualified personnel.

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Getting Service

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