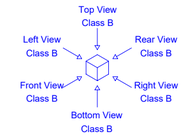
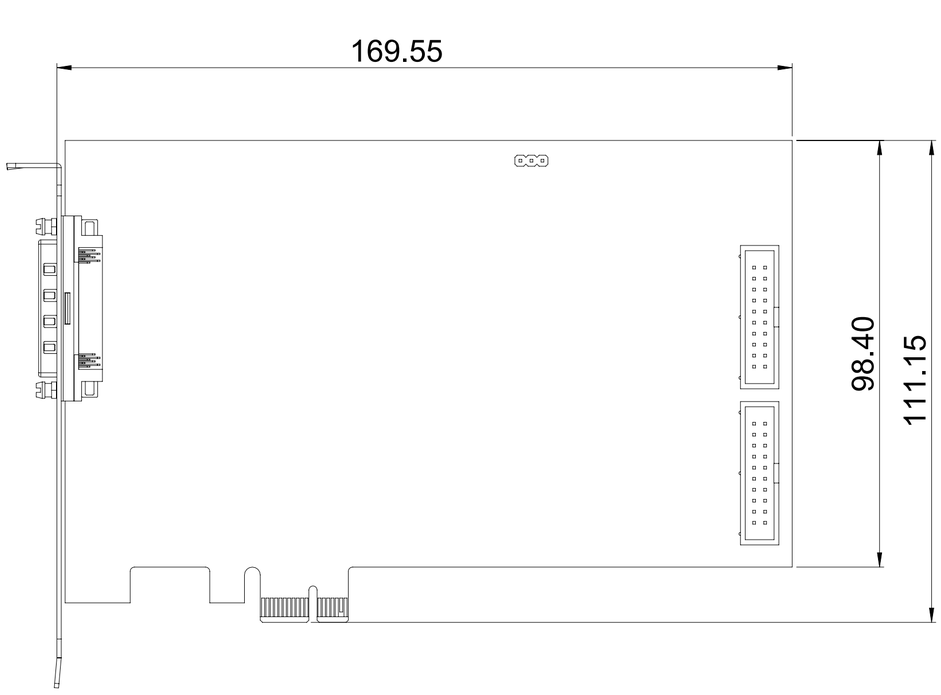
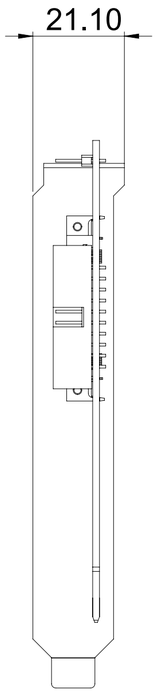
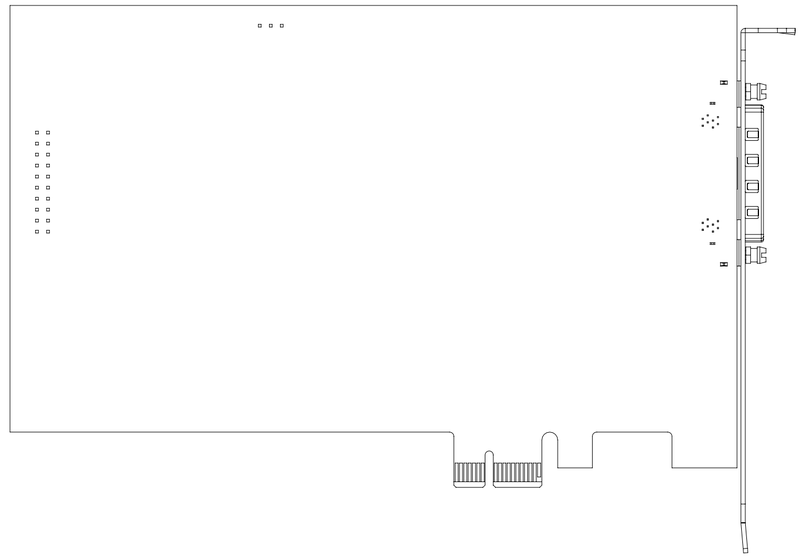
VER.	DATE	DESCRIPTION	DESIGN
0			









Note:
1.SAMPLE TO BE ENGINEERING APPROVAL, PRODUCTION TO BE IN ACCORDANCE WITH APPROVED SAMPLE.

2.RESTRICTED DRAWING, DO NOT COPY, CITE, OR DISTRIBUTE WITHOUT PERMISSION OF THE AUTHOR.

3.Inspection reference documents QA-SQA (032, 034, 037, 057)

	TOL ±							
RANGE	M1	M2	D1	D2	P1	P2	C	USR
0~6	0.05	0.1	0.05	0.1	0.05	0.1	0.5	
>6~30	0.1	0.1	0.08	0.1	0.08	0.15	1	
>30~80	0.15	0.2	0.1	0.15	0.1	0.2	2	
>80~180	0.15	0.25	0.15	0.2	0.15	0.3	3	
>180~315	0.2	0.4	0.2	0.3	0.2	0.5	3	
>315~800	0.3	0.8	0.25	0.5	0.25	1.0	4	
ANG. TOL ±	0°~30°		0.2°	30°~60°	0.5°	60°~90°	1°	

Restricted

MAT'L	材質 <未指定>	A2	UNIT:MM
FINISH		SHEET	1 OF 1
DESIGN	Josh Lin	SCALE	1:1
APPRO.	Anson Jm Lin	PART NO.	
LAST SAVED DATE :	2024年12月11日 上午 10:57:00		
ADLINK		DRAWING NAME	PCIe-9163
		PROJECT NAME	PCIe-9163