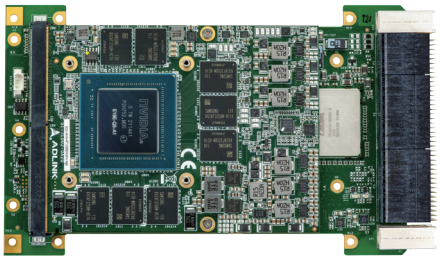
ADLINK
LEADING EDGE COMPUTING

3U VPXGPGPU Blade with NVIDIA A4500

VPX3-MXM/A4500

Datasheet

New



Features

- NVIDIA® A4500 embedded graphics based on Ampere architecture
- 5888 CUDA cores, 17.66 TFLOPS peak FP32 performance
- 16GB GDDR6 memory, 256-bit, Bandwidth: 512 GB/s
- Four DisplayPort 1.4
- VITA 46/48/65 compliant for quick deployment
- SOSA-aligned

Specifications

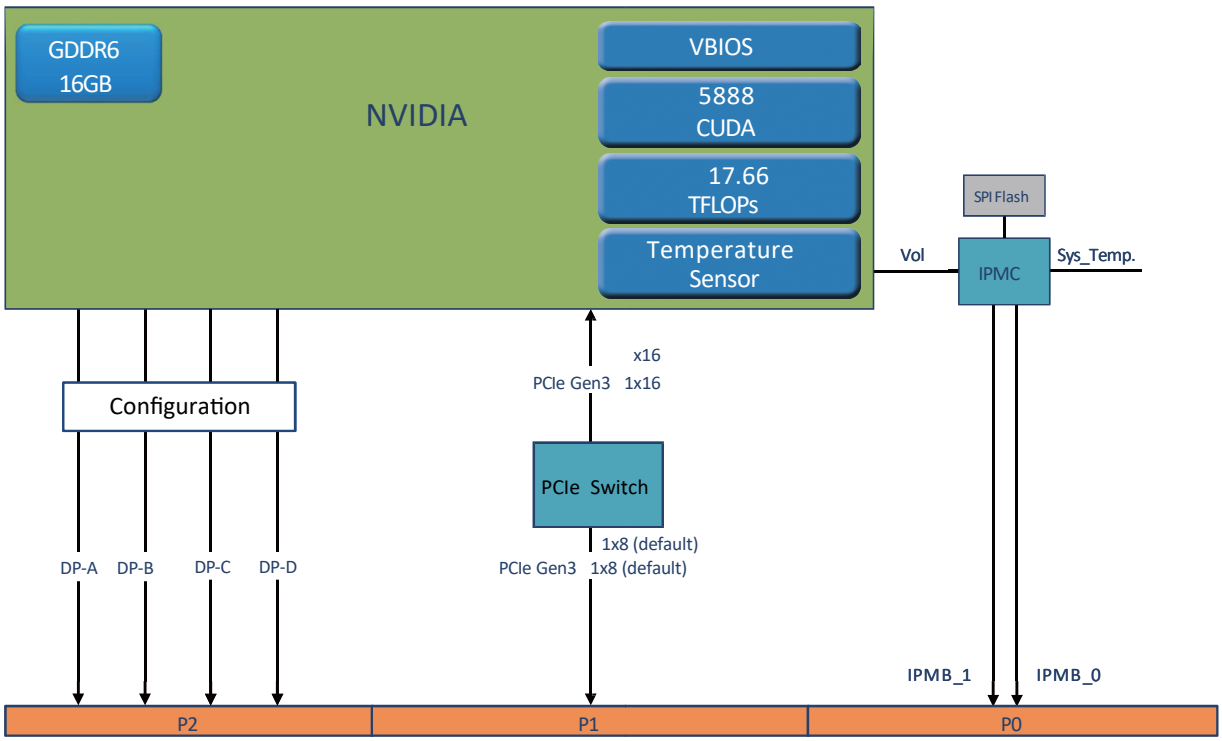
Graphic Core	GPU	NVIDIA RTXA4500 GA104-955 GPU
	Memory	16GB GDDR6 memory, 256-bit, Bandwidth: 512 GB/s
GPGPU Computing	CUDA Cores	5888 CUDA cores, 17.66 TFLOPS peak FP32 performance
	Tensor Cores	184 Tensor Cores
	RT Cores	46 RT Cores
	Compute API	CUDA Compute 8.0 and above, OpenCL1.2
	Graphics API	DirectX® 12, OpenGL 4.6
Connectivity	Video Output	Four DisplayPort 1.4 outputs to P2
	PCI Express	PCIe x8 Gen3 to P1 (PCIe x16 Gen3 configurable)
	VITA Standards	VITA 46/48/65 compliant
	OpenVPXProfile	Support SOSA slot profile (PCIe x 8): SLT3-PAY-2F2U-14.2.3
Power Requirement	Power Input	12V (VS1/VS2) 3.3V_AUX only
Operating System	Windows	Windows 10 64-bit
	Linux	Support by request
Miscellaneous	LED	Status LED
	Reset Button	Board reset button on front panel

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Specifications

Mechanical	Form Factor	3U VPX 0.85" with conduction cooling; secondary side retainer
	Dimensions	100mm x 160mm
	MXM Support	MXM 4.1 Type B+ (110mm)
	Thermal Dissipation	Conduction-cooling by wedge lock
Environmental	Operating Temp.	-40°C to +85°C at wedge locks, GPU TGP at 80W (VITA47.0, ECC4-CC4)
	Storage Temp.	-55°C to +105°C
	Vibration	Random: 12Grms, 5Hz to 2000Hz (VITA47.0, ECC4-V3) Sinusoidal: 5g, 20Hz to 2000Hz
	Shock	40g, 11 ms (VITA47.0, ECC4-OS2)
	Relative Humidity	95% non-condensing
	Altitude	60,000 feet. (operating)
	EMC	CE (EN555032 / EN55035);FCC Part 15BClassA

Block Diagram



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Ordering Information

GPGPU Blades, SOSA-aligned payload: SLT3-PAY-2F2U-14.2.3	
VPX3-MXM/A4500,DP,SOSA, non-COMM,ETT,CC	3U VPX GPGPU blade with MXM A4500 inside, 4x DP output, SOSA-aligned, conformal coating & ETT, -40 to +85˚C, conduction-cooled, supports non-common PCIe clock mode
VPX3-MXM/A4500,DP,SOSA, COMM,ETT,CC	3U VPX GPGPU blade with MXM A4500 inside, 4x DP output, SOSA-aligned, conformal coating & ETT, -40 to +85˚C, conduction-cooled, supports common PCIe clock mode

RTM for Test	
VPX3-RMXM-DP	RTM for VPX3-RTX5000-STD-R1 DP output provides four DisplayPort outputs

NVIDIA Elite Partner

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