VPX3000 Series

Performance Rugged Conduction Cooled
3U VPX 3rd Gen Intel® Core™ i7
Processor Blade



Manual Rev.: 1.1

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Part No: 50-15092-2000

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Leading EDGE COMPUTING

Revision History

Revision	Release Date	Description of Change(s)
1.1	2019/06/04	Initial release

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Preface

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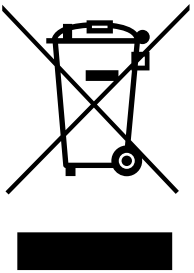
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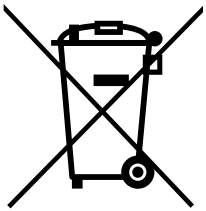
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Conventions

Take note of the following conventions used throughout this manual to make sure that users perform certain tasks and instructions properly.

NOTE: Additional information, aids, and tips that help users perform tasks.

CAUTION: Information to prevent ***minor*** physical injury, component damage, data loss, and/or program corruption when trying to complete a task.

WARNING: Information to prevent ***serious*** physical injury, component damage, data loss, and/or program corruption when trying to complete a specific task.

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Table of Contents

Revision History ii

Preface iii

List of Figures vii

List of Tables ix

1 Introduction 1

1.1 Overview 1
1.2 Features 2
1.3 Block Diagrams 3
1.4 Model Number Decoder - Processor Blade 6
1.5 Package Contents 7

2 Specifications 9

2.1 VPX3000 Blade Specifications 9
2.2 VPX-R300 RTM Specifications 11
2.3 Power Consumption 12

3 Functional Description 13

3.1 Processors 13
3.2 Chipset 15
3.3 Intel® Turbo Boost Technology 16
3.4 Intel® Hyper Threading Technology 16
3.5 XMC Site 17
3.6 Real Time Clock 17

4 Board Interfaces 19

4.1 VPX3000 Board Layout 19
4.2 VPX3000 Mechanical Drawing 20
4.3 VPX3000 Connector Pin Assignments 21

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4.4 VPX-R300 RTM Board Layout 26
4.5 VPX-R300 RTM Connector Pin Assignments 27
4.6 Switches 34

5 Getting Started 35
5.1 Installing the VPX3000 to the Chassis 35
5.2 Installing the VPX-R300 to the Chassis 35
5.3 Driver Installation 36

6 Utilities 37
6.1 Watchdog Timer 37

7 BIOS Setup 43
7.1 Starting the BIOS 43
7.2 Main Setup 48
7.3 Advanced BIOS Setup 49
7.4 Chipset Setup 64
7.5 Boot Settings 68
7.6 Security Setup 71
7.7 Save & Exit Menu 72

8 IPMI User Guide 75
8.1 Introduction 75
8.2 Summary of Commands Supported by VPX3000 75
8.3 Communications with IPMC 78
8.4 IPMI Sensors List 78
8.5 System Event Log (SEL) 80
8.6 FRU Information 81
8.7 Relevant Documents 82

Important Safety Instructions 83

Getting Service 85

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List of Figures

Figure 1-1: VPX3000 Functional Block Diagram..... 3
Figure 1-2: VPX-R300 RTM Functional Block Diagram..... 4
Figure 1-3: tBP-VPX3000 Test Backplane Func Block Diagram..... 5
Figure 4-1: VPX3000 Board Layout..... 19
Figure 4-2: VPX3000 Mechanical Drawing..... 20
Figure 4-3: VPX-R300 RTM Board Layout..... 26

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List of Tables

Table 2-1: VPX3000 Blade Specifications 9
Table 2-2: VPX-R300 RTM Specifications 11

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1 Introduction

1.1 Overview

The VPX3000 Series is a 3U VPX processor blade featuring the 3rd Generation Intel® Core i7 processor with Mobile Intel® QM77 Express Chipset. The VPX3000 provides up to 8GB DDR3-1066/1333 dual channel ECC memory soldered onboard, one PCI Express x8 XMC.3 site with VITA 46.9 rear IO, and onboard soldered 16GB SLC SATA solid state drive.

The VPX3000 provides two Gen2 PCI Express x4 (or 1xPCIe x8) to P1 with DMA and non-transparent bridge capability. Rear I/O via P1 and P2 includes 1000BASE-T, HD Audio (Line-in, Line-out), 2x SATA 6 Gb/s, 1x SATA 3 Gb/s, USB 3.0, USB 2.0, GPIO, VGA, DVI, RS-232, and RS-422.

A VPX-R300 Rear Transition Module is available to access rear I/O signals from the VPX3000 and a tBP-VPX3000 Test Backplane supporting three payload slots is available for users to validate VPX3000 functionality.

The VPX3000 Series is rugged conduction cooled with conformal coating, making it ideal for mission critical applications such as military and aerospace platforms.

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1.2 Features

- 3U VPX VITA 46, OpenVPX VITA 65, VPX REDI 48
- 4HP (0.8") rugged conduction cooled 3U VPX blade with conformal coating
- Intel® 3rd generation Core™ i7-3612QE Processor (4 cores, 6M cache, 2.1 GHz, 35W TDP)
- Dual channel DDR3-1333/1666 SDRAM with ECC soldered onboard, up to 8GB
- Intel® QM77 PCH
- Intel® HD Graphics 4000 with DX11, OpenGL 3.1 and OCL 1.1
- Onboard PCI Express x8 XMC site with VITA 46.9 rear I/O
- Onboard soldered SATA Solid State Drive, up to 32GB
- Data Plane: VITA 46.4 PCIe 2.0 x8, or 2x PCIe 2.0 x4 to P1
- Control Plane: VITA 46.6 2x 1000BASE-BX
- Rear I/O:
  - P1: 1000BASE-T, Line-in, Line-out, 2x SATA 6 Gb/s, USB 3.0, USB 2.0, 3x GPIO
  - P2: VGA, DVI, RS232, RS422, SATA Gen2, 3x GPIO
- RAID 0/1/5/10 support
- uEFI BIOS 8MB flash
- Module Profile: MOD3-PAY-2F2U-16.2.3-3
- Slot Profile: SLT3-PAY-2F2U-14.2.3

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1.3 Block Diagrams

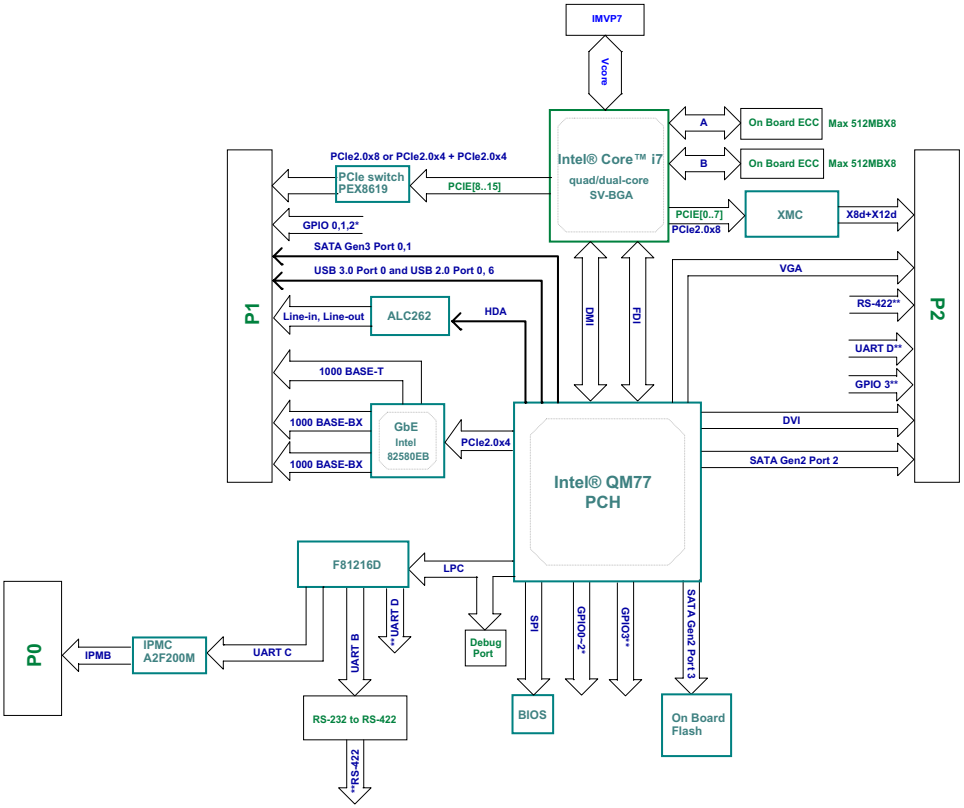


Figure 1-1: VPX3000 Functional Block Diagram

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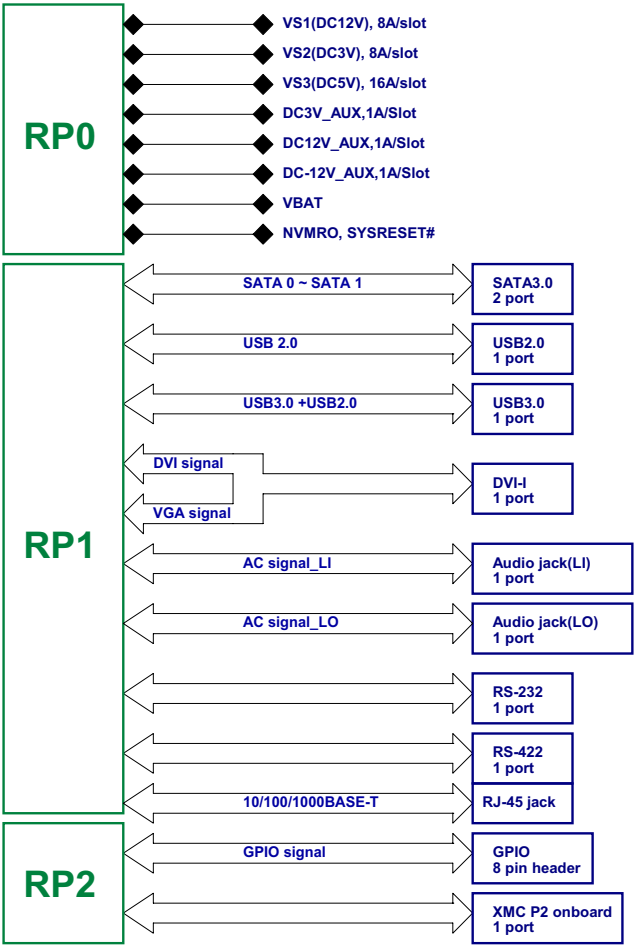


Figure 1-2: VPX-R300 RTM Functional Block Diagram

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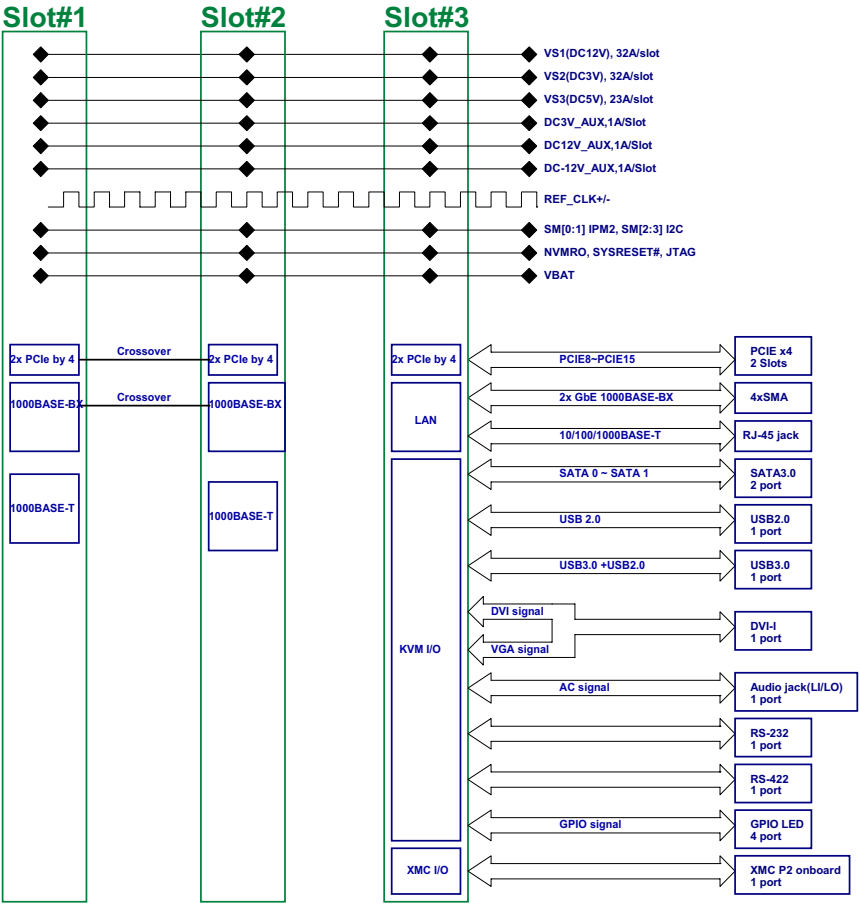
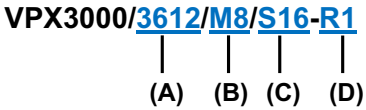


Figure 1-3: tBP-VPX3000 Test Backplane Functional Block Diagram

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1.4 Model Number Decoder - Processor Blade



(A) CPU Code

- ▷ **3612** = Quad-Core Intel® i7-3612QE processor
- ▷ **3555** = Dual-Core Intel® i7-2655LE processor

(B) Memory Size Code

- ▷ **M8** = Onboard 8GB DDR3-1333/1600 memory
- ▷ **M16** = Onboard 16GB DDR3-1333/1600 memory

(C) SATA NAND Flash Size Code

- ▷ **S16** = Onboard 16GB
- ▷ **S32** = Onboard 32GB

(D) Ruggedized Level Code

- ▷ R1 = Conduction Cooled version with conformal coating supporting -40°C to +75°C at card edge
- ▷ R2 = Conduction Cooled version with conformal coating supporting -40°C to +85°C at card edge

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1.5 Package Contents

The VPX3000 is packaged with the following components. If any of the items on the contents list are missing or damaged, retain the shipping carton and packing material and contact the dealer for inspection. Please obtain authorization before returning any product to ADLINK. The packing contents of the VPX3000 are non-standard configurations and may vary depending on customer requests.

Processor Blade

- VPX3000
  - CPU and memory specifications will differ depending on options selected
  - Thermal module is assembled on the board

Rear Transition Module

- VPX-R300 RTM
- DVI to VGA adapter

Test Backplane

- tBP-VPX3000 test backplane
- DVI to VGA adapter


NOTE:

The contents of non-standard VPX3000 configurations may vary depending on the customer requirements.


CAUTION:

This product must be protected from static discharge and physical shock. Never remove any of the components except at a static-free workstation. Use the anti-static bag shipped with the product when putting the board on a surface. Wear an anti-static wrist strap properly grounded on one of the system's ESD ground jacks when installing or servicing system components.

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2 Specifications

2.1 VPX3000 Blade Specifications

VITA Standards	• VITA 46.0 VPX Base Standard • VITA 46.4 PCI Express on VPX Fabric Connector • VITA 46.6 Gigabit Ethernet Control Plane on VPX • VITA 46.9 XMC/Ethernet Signal Mapping to 3U/6U VPX • VITA 46.11(draft) System Management on VPX • VITA 48.0 Ruggedized Enhanced Design Implementation Mechanical Base Specification • VITA 65 OpenVPX Architecture Framework for VPX
Mechanical	• IEEE 1101.1-1998 - Mechanical Core Specifications for Microcomputers • IEEE 1101.2-1992 - Conduction Cooled VME Mechanics • 3U 4HP (0.8") • Board size: 100mm x 160mm • P0, P1, P2 connectors; alignment key 1 & 2 equipped
Processor	• Intel® Core™ i7-3612QE Processor, 2.1 GHz, 6MB LLC cache, FCBGA1023, TDP 35W • Intel® Core™ i7-3555LE Processor, 2.5 GHz, 4MB LLC cache, FCBGA1023, TDP 25W • Passive ruggedized heatsink
Chipset	• Intel® BD82QM77 Platform Controller Hub (PCH)
Memory	• Dual channel DDR3-1333/1600 ECC memory • Soldered onboard with 256Mb*8*18 devices • Up to 8GB
PCI Express	• PCI Express 2.0 x8, or 2x PCIe x4 to P1 by switch setting
Gigabit Ethernet	• One Intel® 82580EB Gigabit Ethernet Controller • One 10/100/1000BASE-T port and two 1000BASE-BX SerDes ports to P1
Graphics	• Integrated in Intel® Core™ i7 processor • Intel HD Graphics 4000 • Analog VGA and digital DVI routed to P2 • DVI port resolutions up to 1920x1200 at 60Hz • Analog monitor support up to 2048x1536 at 75Hz • Dual independent display

Table 2-1: VPX3000 Blade Specifications

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XMC	• One PCIe 2.0 x8 conduction cooled XMC.3 site • Jn6 rear XMC I/O connector compliant to VITA 46.9, X8d+X12d
Front Panel I/O	• Reset button • Power LED
Storage Interfaces[1]	• Onboard SATA NAND flash on SATA 3 Gb/s, up to 32GB • SATA 6 Gb/s port 0 and 1 to P1 • SATA 3 Gb/s port 2 to P2
BIOS	• AMI EFI BIOS, 64Mbit SPI flash memory
P1 Interface	• Line-in • Line-out • 2x SATA 6 Gb/s ports • 2x USB 2.0 • USB 3.0 • 10/100/1000BASE-T Gigabit Ethernet Ports • 2x 1000BASE-BX SerDes Port
P2 Interface	• VGA • RS-232 • RS-422 • DVI • SATA 3 Gb/s port • XMC X8d+X12d
Environmental	• Operating Temperature: -40°C to +75°C (for Intel® Core™ i7-3612QE) -40°C to +85°C (for Intel® Core™ i7-3555LE) • Storage Temperature: -50°C to 100°C • Humidity: 95%, non-condensing • Shock: 40G peak-to-peak, 11ms duration, non-operating • Vibration[1]: Operating 5Grms, 20-2000Hz, each axis w/o hard drive • Altitude:
EMI	• CE EN55022 • FCC Class A

Table 2-1: VPX3000 Blade Specifications

Notes:

1. The storage device limits the operational vibration. When application requires higher specification for anti-vibration, it is recommended to use a flash storage device.

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2.2 VPX-R300 RTM Specifications

VITA Standards	• VITA 46.10 Rear Transition Module on VPX
Mechanical	• 100mm x 80mm
Faceplate I/O	• DVI-I port • USB 2.0 port • USB 3.0 port • 10/100/1000BASE-T Ethernet port
Onboard Interfaces	• Line-in audio jack • Line-out audio jack • RS-232 (RJ-45) • RS-422 (RJ-45) • 2x SATA 3.0 ports • 8x pin headers for GPIO • XMC port • 4x status LEDs for GPIO

Table 2-2: VPX-R300 RTM Specifications

Note: Specifications are subject to change without prior notice.

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2.3 Power Consumption

This section provides information on the power consumption of the VPX3000 when using Intel® Core™ i7 processors with Dual Channel 8GB DDR3-1333 soldered memory and soldered onboard 16GB SATA SSD. The VPX3000 is powered by 12V, 5V and 3.3V. Power consumption at 100% CPU usage was measured by running Intel Thermal Analysis Tool (TAT).

Intel® Core i7-2612QE

Mode	Voltage(V)	Current(A)	Power(W)	Total(W)
Idle Load	5V	1.85A	9.25	13.687W
	12V	0.18A	2.16	
	3.3V	0.69A	2.277	
CPU Max	5V	3.55A	17.65	57.007W
	12V	3.09A	37.08	
	3.3V	0.69A	2.277	

Intel® Core i7-2555LE

Mode	Voltage(V)	Current(A)	Power(W)	Total(W)
Idle Load	5V	1,86A	9.3	13.53W
	12V	0.16A	1.92	
	3.3V	0.70A	2.31	
CPU Max	5V	3.36A	16.8	45.90W
	12V	2.23A	26.76	
	3.3V	0.71A	2.343	

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3 Functional Description

The following sections describe the VPX3000 features and functions.

3.1 Processors

The 3rd Generation Intel® Core™ i7 Processor is the next generation of 64-bit, multi-core mobile processors built on 22-nanometer process technology. Based on a new micro-architecture, the processor is designed for a two-chip platform. The two-chip platform consists of a processor and Platform Controller Hub (PCH). The platform enables higher performance, lower cost, easier validation, and improved x-y footprint. The processor includes an integrated display engine, processor graphics, integrated memory controller, and integrated I/O on a single silicon die.

Features	Core™ i7-3612QE	Core™ i7-3555LE
Clock	2.1 GHz	2.5 GHz
Max. Single Core Turbo Freq.	3.1 GHz	3.2 GHz
Last Level Cache	6 MB	4 MB
No. of Cores/Threads	4/8	2/4
Maximum Power (TDP[1])	35 W	25 W
$T_{junction,MAX}$ [2]	105°C	105°C

Notes:

1. The highest expected sustainable power while running known power intensive applications. TDP is not the maximum power that the processor can dissipate.
2. The maximum supported operating temperature.

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Supported Technologies

- ▷ Intel® Virtualization Technology for Directed I/O (Intel® VT-d)
- ▶ Intel® Virtualization Technology (Intel® VT-x)
- ▶ Intel® vPro Technology (Intel® VT)
- ▶ Intel® Trusted Execution Technology (Intel® TXT)
- ▶ Intel® Hyper-Threading Technology
- ▶ Intel® 64 Architecture
- ▶ Intel® Turbo Boost Technology 2.0
- ▶ AES New Instructions
- ▶ Intel® My WiFi Technology
- ▶ Enhanced Intel SpeedStep® Technology
- ▶ Thermal Monitoring Technologies
- ▶ Intel® Fast Memory Access
- ▶ Execute Disable Bit
- ▶ Intel® VT-x with Extended Page Tables (EPT)

Interfaces

- ▶ Dual channel DDR3 memory with one channel of unbuffered SODIMM and one channel of soldered SDRAM
- ▶ Memory DDR3 data transfer rates of 1333 MT/s and 1600 MT/s
- ▶ 64-bit wide channels plus 8-bits of ECC support for each channel
- ▶ System Memory Interface I/O Voltage of 1.35V and 1.5V
- ▶ 1-Gb, 2-Gb, and 4-Gb DDR3 DRAM technologies supported
  - ▷ Using 4Gb DRAM device technologies, the largest memory capacity possible is 8 GB.
  - ▷ Intel Fast Memory Access:
    Just-in-Time Command Scheduling
    Command Overlap
    Out-of Order Scheduling
- ▶ The PCI Express lanes are fully-compliant with the PCI Express Base Specification, Revision 3.0, including support for 8.0 GT/s transfer speeds.

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3.2 Chipset

The VPX3000 incorporates the Mobile Intel® QM77 Express Chipset (Intel® BD82QM77 PCH).

Mobile Intel® QM77 Express Chipset

- PCI Express Base Specification, Revision 2.0 support for up to eight ports with transfer rate up to 5 GT/s
- Supports dual display
- ACPI Power Management Logic Support, Revision 4.0a
- Enhanced DMA controller, interrupt controller, and timer functions
- Integrated Serial ATA host controllers with independent DMA operation on up to six ports and SATA 6.0 Gb/s up to two ports
- Supports USB 2.0, USB 3.0
- USB host interface with two EHCI high-speed USB 2.0 Host controllers and two rate matching hubs provide support for up to fourteen USB 2.0 ports
- System Management Bus (SMBus) Specification, Version 2.0 with additional support for I2C devices
- Supports Intel Trusted Execution Technology
- Support vPro Technology
- Supports Intel® High Definition Audio
- Supports Intel® Rapid Storage Technology
- Supports Intel® Virtualization Technology for Directed I/O
- Low Pin Count (LPC) interface
- Serial Peripheral Interface (SPI) support

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3.3 Intel® Turbo Boost Technology

Intel Turbo Boost Technology is a feature that allows the processor to opportunistically and automatically run faster than its rated operating core and/or render clock frequency when there is sufficient power headroom, and the product is within specified temperature and current limits. The Intel Turbo Boost Technology feature is designed to increase performance of both multi-threaded and single-threaded workloads. The processor supports a Turbo mode where the processor can use the thermal capacity associated with package and run at power levels higher than TDP power for short durations. This improves the system responsiveness for short, bursty usage conditions.

Turbo Mode availability is independent of the number of active cores; however, the Turbo Mode frequency is dynamic and dependent on the instantaneous application power load, the number of active cores, user configurable settings, operating environment, and system design. If the power, current, or thermal limit is reached, the processor will automatically reduce the frequency to stay with its TDP limit.

3.4 Intel® Hyper Threading Technology

The processor supports Intel® Hyper-Threading Technology (Intel® HT Technology), that allows an execution core to function as two logical processors. While some execution resources (such as caches, execution units, and buses) are shared, each logical processor has its own architectural state with its own set of general-purpose registers and control registers. This feature must be enabled using the BIOS and requires operating system support.

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3.5 XMC Site

The VPX3000 Series supports one XMC site for rear I/O expansion. The XMC site provides a x8 PCI Express 2.0 lane. Jn6 rear XMC I/O connector is compliant to VITA 46.9, X8d+X12d.

3.6 Real Time Clock

The Intel® QM77 PCH contains a Motorola MC146818B-compatible real-time clock (RTC) with 256 bytes of RAM. The real-time clock performs two key functions: keeping track of the time of day and storing system data. The RTC operates on a 32.768 KHz crystal and is powered by P3V3_AUX or VBAT. There is no external RTC battery on the VPX3000 to power the RTC after the system is powered off.

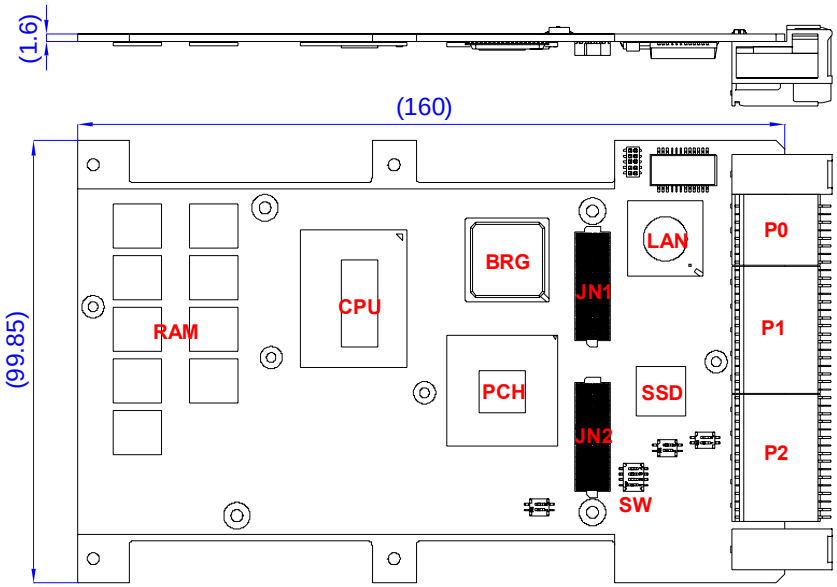
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4 Board Interfaces

4.1 VPX3000 Board Layout



CPU	Intel Core Processor	**SSD**	16/32GB SATA NAND Flash
PCH	Intel QM77 PCH	**JN1**	XMC Primary Connector
BRG	Pericom PEX8619 Bridge	**JN2**	XMC Secondary Connector (to rear I/O via P2)
LAN	Intel 82580EB Ethernet Controller	**SW**	DIP Switch for PCIe to P1 configuration
RAM	DDR3 Memory Chips	**P0-2**	VPX Connectors

Figure 4-1: VPX3000 Board Layout

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4.2 VPX3000 Mechanical Drawing

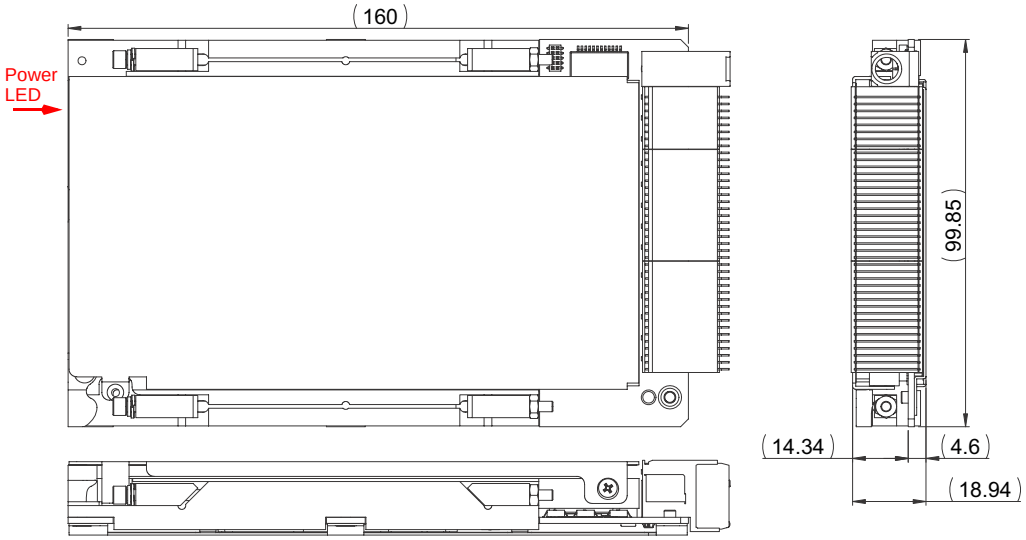


Figure 4-2: VPX3000 Mechanical Drawing

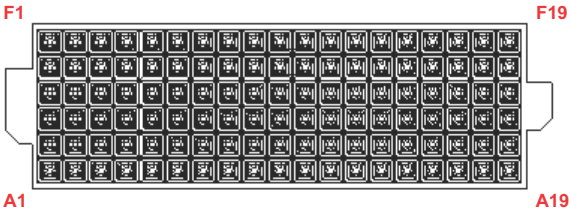
Power LED

LED	Color	Condition	Indication
Power	Green	OFF	System is off
		ON	System is on

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4.3 VPX3000 Connector Pin Assignments

XMC Connectors



XMC Primary Connector (JN1)

Pin	A	B	C	D	E	F
1	RXP0	RXN0	+3.3V	RXP1	RXN1	VPWR
2	GND	GND	TRST#	GND	GND	MRSTI#
3	RXP2	RXN2	+3.3V	RXP3	RXN3	VPWR
4	GND	GND	TCK	GND	GND	MRSTO#
5	RXP4	RXN4	+3.3V	RXP5	RXN5	VPWR
6	GND	GND	TMS	GND	GND	+12V
7	RXP6	RXN6	+3.3V	RXP7	RXN7	VPWR
8	GND	GND	TDI	GND	GND	-12V_AUX
9	NC	NC	NC	NC	NC	NC
10	GND	GND	TDO	GND	GND	GA0
11	TXP0	TXN0	MBIST#	TXP1	TXN1	VPWR
12	GND	GND	GA1	GND	GND	MPRSENT#
13	TXP2	TXN2	+3.3V_AUX	TXP3	TXN3	VPWR
14	GND	GND	GA2	GND	GND	DATA
15	TXP4	TXN4	NC	TXP5	TXN5	VPWR
16	GND	GND	MVMRO	GND	GND	CLK
17	TXP6	TXN6	NC	TXP7	TXN7	NC
18	GND	GND	NC	GND	GND	NC
19	CLK-P	CLK-N	NC	WAKE#	ROOT#	NC

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Secondary Connector (JN2, to rear I/O via P2)

Pin	A	B	C	D	E	F
1	JN2_A1	JN2_B1	NC	JN2_D1	JN2_E1	NC
2	GND	GND	NC	GND	GND	NC
3	JN2_A3	JN2_B3	NC	JN2_D3	JN2_E3	NC
4	GND	GND	NC	GND	GND	NC
5	JN2_A5	JN2_B5	NC	JN2_D5	JN2_D6	NC
6	GND	GND	NC	GND	GND	NC
7	JN2_A7	JN2_B7	NC	JN2_D7	JN2_D7	NC
8	GND	GND	NC	GND	GND	NC
9	JN2_A9	JN2_B9	NC	JN2_D9	JN2_D9	NC
10	GND	GND	NC	GND	GND	NC
11	JN2_A11	JN2_B11	NC	JN2_D11	JN2_D11	NC
12	GND	GND	NC	GND	GND	NC
13	JN2_A13	JN2_B13	NC	JN2_D13	JN2_D13	NC
14	GND	GND	NC	GND	GND	NC
15	JN2_A15	JN2_B15	NC	JN2_D15	JN2_D16	NC
16	GND	GND	NC	GND	GND	NC
17	JN2_A17	JN2_B17	NC	JN2_D17	JN2_D17	NC
18	GND	GND	NC	GND	GND	NC
19	JN2_A19	JN2_B19	NC	JN2_D19	JN2_D19	NC

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VPX Connectors

P0 Connector

Pin	G	F	E	D	C	B	A
1	+12V	+12V	+12V	NC	+3.3V	+3.3V	+3.3V
2	+12V	+12V	+12V	NC	+3.3V	+3.3V	+3.3V
3	+5V	+5V	+5V	NC	+5V	+5V	+5V
4	I2C_CLK	I2C_DAT	GND	-12V_AUX	GND	SYSRESET	NC
5	GAP	GA4	GND	+3.3V_AUX	GND	IPMB_CLK	IPMB_DATA
6	GA3	GA2	GND	+12V_AUX	GND	GA1	GA0
7	TCK	GND	TDO	TDI	GND	TMS	TRST
8	GND	REF_CLK-	REF_CLK+	GND	NC	NC	GND

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P1 Connector

Pin	G	F	E	D	C	B	A
1	NC	GND	L0-TX-	L0-TX+	GND	L0-RX-	L0-RX+
2	GND	L1-TX-	L1-TX+	GND	L1-RX-	L1-RX+	GND
3	VBAT	GND	L2-TX-	L2-TX+	GND	L2-RX-	L2-RX+
4	GND	L3-TX-	L3-TX+	GND	L3-RX-	L3-RX+	GND
5	SYSCON#	GND	L4-TX-	L4-TX+	GND	L4-RX-	L4-RX+
6	GND	L5-TX-	L5-TX+	GND	L5-RX-	L5-RX+	GND
7	NC	GND	L6-TX-	L6-TX+	GND	L6-RX-	L6-RX+
8	GND	L7-TX-	L7-TX+	GND	L7-RX-	L7-RX+	GND
9	GPIO1	GND	SATA0-TX-	SATA0-TX+	GND	SATA0-RX-	SATA0-RX+
10	GND	SATA1-TX-	SATA1-TX+	GND	SATA1-RX-	SATA1-RX+	GND
11	GPIO6	GND	USB3_RX1-	USB3_RX1+	GND	USB3_TX1-	USB3_TX1+
12	GND	USB2_0-	USB2_0+	GND	USB2_6-	USB2_6+	GND
13	GPIO7	GND	TP01_DB-	TP01_DB+	GND	TP01_DA-	TP01_DA+
14	GND	TP01_DD-	TP01_DD+	GND	TP01_DC-	TP01_DC+	GND
15	MASKABLE_RESET	GND	UTP2_TD-	UTP2_TD+	GND	UTP2_RD-	UTP2_RD+
16	GND	UTP1_TD-	UTP1_TD+	GND	UTP1_RD-	UTP1_RD+	GND



Lxx: PCI Express x8
SATAxx: SATA
USB3xx: USB 3.0
USB2xx: USB 2.0
TPxx: 1000 BASE-T
UTPxx: 1000 BASE-BX

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P2 Connector

Pin	G	F	E	D	C	B	A
1	GPIO3	GND	DVI_TX1-	DVI_TX1+	GND	DVI_TX0-	DVI_TX0+
2	GND	DVI_TXC-	DVI_TXC+	GND	DVI_TX2-	DVI_TX2+	GND
3	VGA_SDA	GND	NC	DVI_HPD	GND	DVI_SCL	DVI_SDA
4	GND	COM_SIN	COM_SOUT	GND	COM_CTS#	COM_RTS#	GND
5	VGA_SCL	GND	RS422_TX-	RS422_TX+	GND	RS422_RX-	RS422_RX+
6	GND	Linein-L	Linein-R	AGND	Lineout-L	Lineout-R	AGND
7	VGA-HS	GND	JN2-A1	JN2-B1	GND	JN2-D1	JN2-E1
8	GND	JN2-A3	JN2-B3	GND	JN2-D3	JN2-E3	GND
9	VGA-VS	GND	JN2-A11	JN2-B11	GND	JN2-D11	JN2-E11
10	GND	JN2-A13	JN2-B13	GND	JN2-D13	JN2-E13	GND
11	VGA-R	GND	JN2-A5	JN2-B5	GND	JN2-D5	JN2-E5
12	GND	JN2-A7	JN2-B7	GND	JN2-D7	JN2-E7	GND
13	VGA-B	GND	JN2-A9	JN2-B9	GND	JN2-D9	JN2-E9
14	GND	JN2-A15	JN2-B15	GND	JN2-D15	JN2-E15	GND
15	VGA-G	GND	JN2-A17	JN2-B17	GND	JN2-D17	JN2-E17
16	GND	JN2-A19	JN2-B19	GND	JN2-D19	JN2-E19	GND



DVIxx: DVI

COMxx: RS-232

RS422xx: RS-422

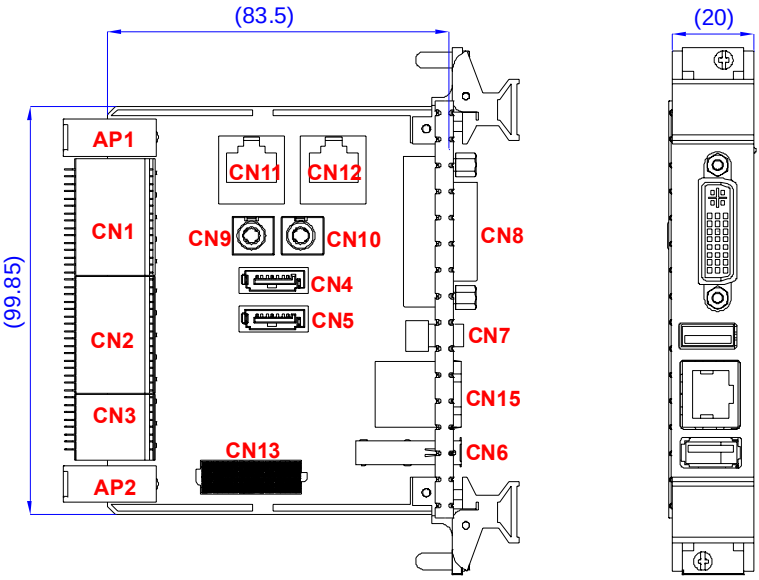
Linexx: Audio

VGAxx: Analog VGA

Jxx: XMC rear I/O (from J2)

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4.4 VPX-R300 RTM Board Layout



CN1	VPX Connector RP0	**CN8**	DVI-I port
CN2	VPX Connector RP1	**CN9**	Line-in Port
CN3	VPX Connector RP2	**CN10**	Line-out Port
AP1/2	Alignment keys	**CN11**	RS-232 Port
CN4/5	7-pin SATA connectors	**CN12**	RS-422 Port
CN6	USB 3.0 port	**CN13**	XMC connector
CN7	USB 2.0 port	**CN15**	1000BASE-T port

Figure 4-3: VPX-R300 RTM Board Layout

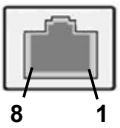
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4.5 VPX-R300 RTM Connector Pin Assignments

Rear I/O Connectors

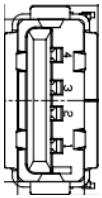
Gigabit Ethernet Connectors (CN11, RJ-45)

Pin #	GbE Signal
1	MX0+
2	MX0-
3	MX1+
4	MX2+
5	MX2-
6	MX1-
7	MX3+
8	MX3-



USB 2.0 Connector (CN16)

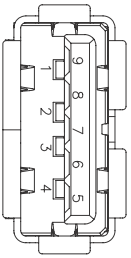
Pin #	Signal Name
1	+5V
2	USB-
3	USB+
4	GND



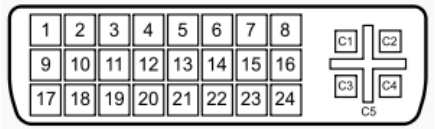
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USB 3.0 Connector (CN15)

Pin #	Signal Name
1	+5V
2	Data-
3	Data+
4	GND
5	RX_N
6	RX_P
7	GND
8	TX_N
9	TX_P



DVI-I Connector (CNY1-R)



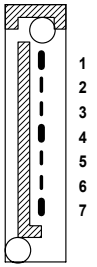
Pin #	Signal	Pin #	Signal
1	NC	16	NC
2	NC	17	NC
3	GND	18	NC
4	NC	19	GND
5	NC	20	NC
6	DDCCLK_5V	21	NC
7	DDCDAT_5V	22	GND
8	VSYNC	23	NC
9	NC	24	NC
10	NC	C1	RED
11	GND	C2	GREEN
12	NC	C3	BLUE
13	NC	C4	HSYNC
14	P5V	C5	GND
15	GND		

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Onboard Connectors

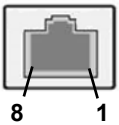
SATA Connector (CN12/13)

Pin #	Signal
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND



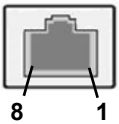
RS-232 (CN31, RJ-45)

Pin #	RS-232
1	N.C.
2	S3_RTS-L(RTS)
3	N.C.
4	S3_SOUT(TXD)
5	S3_SIN(RXD)
6	GND.
7	S3_CTS-L(CTS)
8	N.C.



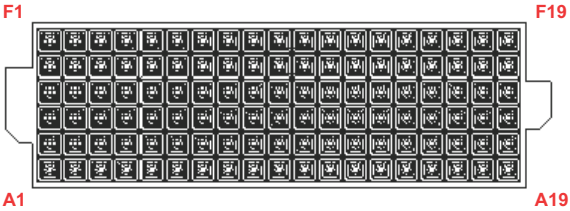
RS-422 (CN32, RJ-45)

Pin #	RS-232
1	S3_TXD422-
2	N.C.
3	N.C.
4	S3_RXD422+
5	S3_TXD422+
6	GND
7	N.C.
8	S3_RXD422-



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XMC Connector (CN13)



Pin	A	B	C	D	E	F
1	+5V	S3_JN6-E1	S3_JN6-D1	+3.3V	S3_JN6-B1	S3_JN6-A1
2	NC	GND	GND	TRST#	GND	GND
3	+5V	S3_JN6-E3	S3_JN6-D3	+3.3V	S3_JN6-B3	S3_JN6-A3
4	NC	GND	GND	TCK	GND	GND
5	+5V	S3_JN6-E5	S3_JN6-D5	+3.3V	S3_JN6-B5	S3_JN6-A5
6	+12V	GND	GND	TMS	GND	GND
7	+5V	S3_JN6-E7	S3_JN6-D7	+3.3V	S3_JN6-B7	S3_JN6-A7
8	-12V	GND	GND	TDI	GND	GND
9	+5V	S3_JN6-E9	S3_JN6-D9	+3.3V	S3_JN6-B9	S3_JN6-A9
10	GA0	GND	GND	TDO	GND	GND
11	+5V	S3_JN6-E11	S3_JN6-D11	NC	S3_JN6-B11	S3_JN6-A11
12	GND	GND	GND	GA1	GND	GND
13	+5V	S3_JN6-E13	S3_JN6-D13	3.3V_AUX	S3_JN6-B13	S3_JN6-A13
14	XMC_DAT	GND	GND	GA2	GND	GND
15	+5V	S3_JN6-E15	S3_JN6-D15	NC	S3_JN6-B15	S3_JN6-A15
16	XMC_CLK	GND	GND	NVMRO	GND	GND
17	NC	S3_JN6-E17	S3_JN6-D17	NC	S3_JN6-B17	S3_JN6-A17
18	NC	GND	GND	NC	GND	GND
19	NC	S3_JN6-E19	S3_JN6-D19	NC	S3_JN6-B19	S3_JN6-A19

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RTM VPX Connectors

RP0 Connector

Pin	G	F	E	D	C	B	A
1	No Wafer						
2	+12V	+12V	+12V	No Pad	NC	+3.3V	+3.3V
3	+5V	+5V	+5V	No Pad	NC	+5V	+5V
4	SM2	SM3	GND	-12V_AUX	GND	SYSRESET	NVMRO
5	GAP*	GA4*	GND	3.3V_AUX	GND	SM0	SM1
6	GA3*	GA2*	GND	+12V_AUX	GND	GA1*	GA0*
7	TCK	GND	TDO	TDI	GND	TMS	TRST*
8	GND	REF_CLK-	REF_CLK+	GND	RES_BUS-	RES_BUS+	GND
9	NC	GND	NC	NC	GND	NC	NC
10	GND	NC	NC	GND	NC	NC	GND
11	P1_VBAT	GND	NC	NC	GND	NC	NC
12	GND	NC	NC	GND	NC	NC	GND
13	SYSCON#	GND	NC	NC	GND	NC	NC
14	GND	NC	NC	GND	NC	NC	GND
15	NC	GND	NC	NC	GND	NC	NC
16	GND	NC	NC	GND	NC	NC	GND

RP1 Connector

Pin	G	F	E	D	C	B	A
1	GPIO0	GND	NC	NC	GND	NC	NC
2	GND	NC	NC	GND	NC	NC	GND
3	GPIO1	GND	NC	NC	GND	NC	NC
4	GND	NC	NC	GND	NC	NC	GND
5	GPIO2	GND	MDIN1	MDIP1	GND	MDIN0	MDIP0
6	GND	MDIN3	MDIP3	GND	MDIN2	MDIP2	GND
7	Maskreset	GND	NC	NC	GND	NC	NC
8	GND	NC	NC	GND	NC	NC	GND
9	GPIO3	GND	DVI_TX1-	DVI_TX1+	GND	DVI_TX0-	DVI_TX0+
10	GND	DVI_TXC-	DVI_TXC+	GND	DVI_TX2-	DVI_TX2+	GND
11	VGA_SDA	GND	NC	DVI_HPD	GND	DVI_SCL	DVI_SDA
12	GND	SIN	SOUT	GND	CTS#	RTS#	GND
13	VGA_SCL	GND	RS422_TX-	RS422_TX+	GND	RS422_RX-	RS422_RX+
14	GND	Linein-L	Linein-R	AGND	Lineout-L	Lineout-R	AGND
15	VGA-HS	GND	J16-A1	J16-B1	GND	J16-D1	J16-E1
16	GND	J16-A3	J16-B3	GND	J16-D3	J16-E3	GND

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RP2 Connector

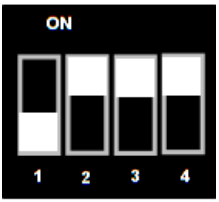
Pin	G	F	E	D	C	B	A
1	VGA-VS	GND	J16-A11	J16-B11	GND	J16-D11	J16-E11
2	GND	J16-A13	J16-B13	GND	J16-D13	J16-E13	GND
3	VGA-R	GND	J16-A5	J16-B5	GND	J16-D5	J16-E5
4	GND	J16-A7	J16-B7	GND	J16-D7	J16-E7	GND
5	VGA-B	GND	J16-A9	J16-B9	GND	J16-D9	J16-E9
6	GND	J16-A15	J16-B15	GND	J16-D15	J16-E15	GND
7	VGA-G	GND	J16-A17	J16-B17	GND	J16-D17	J16-E17
8	GND	J16-A19	J16-B19	GND	J16-D19	J16-E19	GND

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4.6 Switches

PCI Express to P1 Configuration Switch (SW)

This switch sets the PCI Express Lane configuration at the P1 connector.



Mode	1	2	3	4
8x PCIe x1	OFF	ON	OFF	ON
1x PCIe x4 + 4x PCIe x1	OFF	ON	ON	OFF
2x PCIe x4 (default)	OFF	ON	ON	ON
PCIe x8	ON	OFF	OFF	OFF

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5 Getting Started

This chapter describes the installation of the and VPX3000 blade and VPX-R300 rear transition module.

5.1 Installing the VPX3000 to the Chassis

These instructions are for reference only. Refer to the user guide that comes with the chassis for more information.

1. Be sure to select the system slot to install the blade. The system power may be powered either on or off.
2. Align the module's top and bottom edges to the chassis card guides, and then carefully slide the module into the chassis. A slight resistance may be felt when inserting the module. If the resistance is too strong, check if there the wedge locks are tightened or if the board's connector pins are not properly aligned with connectors on the backplane. Then push the blade into the slot until it is completely flush with the chassis.
3. With the blade properly inserted, tighten the wedge locks to secure it to the chassis.

5.2 Installing the VPX-R300 to the Chassis

The installation and removal procedures for a RTMs are the same as those for VPX blades. Because they are shorter than front boards, pay careful attention when inserting or removing RTMs.

Refer to previous sections for peripheral connectivity of all I/O ports on the RTM. When installing the VPX3000 Series and related RTMs, make sure the RTM is the correct matching model.

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5.3 Driver Installation

The VPX3000 drivers are available from the ADLINK website (**www.adlinktech.com**). ADLINK provides validated drivers for Windows 7.

Follow the steps below to install the drivers for Windows 7 64-bit.

1. Install the **Windows operating system** before installing any driver. Most standard I/O device drivers are installed during Windows installation.
2. Install the **chipset driver** by extracting and running the program in **Intel Chipset Device Software_AllOS_ 9.3.0.1026.zip**
3. Install the **LAN drivers** by extracting and running the program in **Intel Network_Adapter_WIN7_64_v 18.0.zip**
4. Install the **graphics driver and utilities** by extracting and running the program in **Intel_HD_Graphics_Family_ WIN7_64_ 15.28.12.64.2932.zip**
5. Install the **audio drivers and utilities** by extracting and running the program in **Realtek_High Definition Audio_ Win7_64_6.0.1.6662.zip**
6. Install the **RAID Drivers** by extracting and running the program in **Intel RST Driver Files_F6 Install_64bit_ 11.7.0.1013.zip**
7. Install the **IME interface driver** by extracting and running the program in **Intel Management Engine Interface 8.0.10.1464.zip**
8. Install the **USB 3.0 driver** by extracting and running the program in **Intel(R) USB 3.0 eXtensible Host Controller Driver AllOS_1.0.4.225.zip**

We recommend using the drivers provided on the ADLINK website to ensure compatibility. VxWorks BSPs can aldo be downloaded from the VPX3000 product page.

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6 Utilities

6.1 Watchdog Timer

This section describes the operation of the VPX3000's watchdog timer (WDT). The primary function of the WDT is to monitor the VPX3000's operation and to reset the system if a software application fails to function as programmed. The following WDT functions may be controlled using a software application:

- enabling and disabling
- set and get current configuration
- reloading timeout value

The VPX3000 custom WDT circuit is implemented using the IPMC and operated by IPMI commands.

Watchdog Timer IPMI Commands

The following section describes the IPMI commands for controlling watchdog timer (WDT) functions. For more details about IPMI commands, please refer to the IPMI specification: "*Intelligent Platform Management Interface Specification Second Generation v2.0*" .

WDT Command Table

Command	NetFn Code	Cmd Code
Reset Watchdog Timer	App (18h)	22h
Set Watchdog Timer	App (18h)	24h
Get Watchdog Timer	App (18h)	25h

The WDT IPMI commands allow the user to set the countdown period and reload the timeout value periodically to keep it from resetting the system. If the timer countdown value is not reloaded, the watchdog resets the system hardware after its counter reaches zero.

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Reset Watchdog Timer

This command is used to reload the WDT.

Action	Byte	Value	Description
Request	0	18h	NetFn/LUN
	1	22h	Defined command
Response	0	Complete Code	00h means OK

Set Watchdog Timer:

This command is used to set the parameters of the WDT.

Action	Byte	Value	Description
Request	0	18h	NetFn/LUN
	1	24h	Defined command
	2	[7] - 1b = don't log [6] - 1b = don't stop timer [5:3] - reserved [2:0] - timer use 000b = reserved 001b = BIOS FRB2 010b = BIOS/POST 011b = OS Load 100b = SMS/OS 101b = OEM 110b -111b = reserved	Timer Use
	3	[7] - reserved [6:4] - pre-timeout interrupt 000b = none 001b = SMI (optional) 010b = NMI / Diagnostic Interrupt (optional) 011b = Messaging Interrupt 100b,111b = reserved [3] - reserved [2:0] - timeout action 000b = no action 001b = Hard Reset 010b = Power Down 011b = Power Cycle 100b,111b = reserved	Timer Actions

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Action	Byte	Value	Description
Request	4	1h~ffh (‘1h’ based.)	Pre-timeout interval in seconds.
	5	[7] - reserved [6] - reserved [5] - OEM [4] - SMS/OS [3] - OS Load [2] - BIOS/POST [1] - BIOS FRB2 [0] - reserved 0b = leave alone 1b = clear timer use expiration bit	Timer Use Expiration flags clear
	6	00h~ffh ,LS byte (100 ms/count)	Initial countdown value
	7	00h~ffh ,MS byte (100 ms/count)	Initial countdown value
Response	0	Complete Code	00h means OK

Get Watchdog Timer

This command is used to get the current configuration in WDT.

Action	Byte	Value	Description
Request	0	18h	NetFn/LUN
	1	25h	Defined command
Response	0	Complete Code	00h means OK
	1	[7] - 1b = don’t log [6] - 1b = don’t stop timer [5:3] - reserved [2:0] - timer use 000b = reserved 001b = BIOS FRB2 010b = BIOS/POST 011b = OS Load 100b = SMS/OS 101b = OEM 110b -111b = reserved	Timer Use

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Action	Byte	Value	Description
Response	2	[7] - reserved [6:4] - pre-timeout interrupt 000b = none 001b = SMI (optional) 010b = NMI / Diagnostic Interrupt (optional) 011b = Messaging Interrupt 100b,111b = reserved [3] - reserved [2:0] - timeout action 000b = no action 001b = Hard Reset 010b = Power Down 011b = Power Cycle 100b,111b = reserved	Timer Actions
	3	1h~ffh ('1h' based.)	Pre-timeout interval in seconds.
	4	[7] - reserved [6] - reserved [5] - OEM [4] - SMS/OS [3] - OS Load [2] - BIOS/POST [1] - BIOS FRB2 [0] - reserved 0b = leave alone 1b = clear timer use expiration bit	Timer Use Expiration flags clear
	5	00h~ffh ,LS byte (100 ms/count)	Initial countdown value
	6	00h~ffh ,MS byte (100 ms/count)	Initial countdown value
	7	00h~ffh ,LS byte (100 ms/count)	Present countdown value
	8	00h~ffh ,MS byte (100 ms/count)	Present countdown value

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Example of WDT Process

The sample program written in C shown below offers an interactive way to test the Watchdog Timer under DOS.

Configure WDT Parameters

```
0x40 : Don't stop timer.
0x01 : Hard Reset.
0x01 : Pre-timeout interval in 1 second.
0x08 : Timer Use Expiration flags clear by OS Load.
0x50 : 80 count. 100ms * 80 = 8000ms = 8 seconds.

[18 00 24 40 01 01 08 50 00]
```

Reset WDT

```
[18 00 22]
```


NOTE:

After setting a new configuration, the Reset WDT command must be sent. Then the WDT will be activated.

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7 BIOS Setup

The following chapter describes basic navigation for the AMIBIOS®8 BIOS setup utility.

7.1 Starting the BIOS

To enter the setup screen, follow these steps:

1. Power on the motherboard
2. Press the < Delete > key on your keyboard when you see the following text prompt:
   < Press DEL to run Setup >
3. After you press the < Delete > key, the main BIOS setup menu displays. You can access the other setup screens from the main BIOS setup menu, such as Chipset and Power menus.



Note: In most cases, the < Delete > key is used to invoke the setup screen. There are several cases that use other keys, such as < F1 >, < F2 >, and so on.

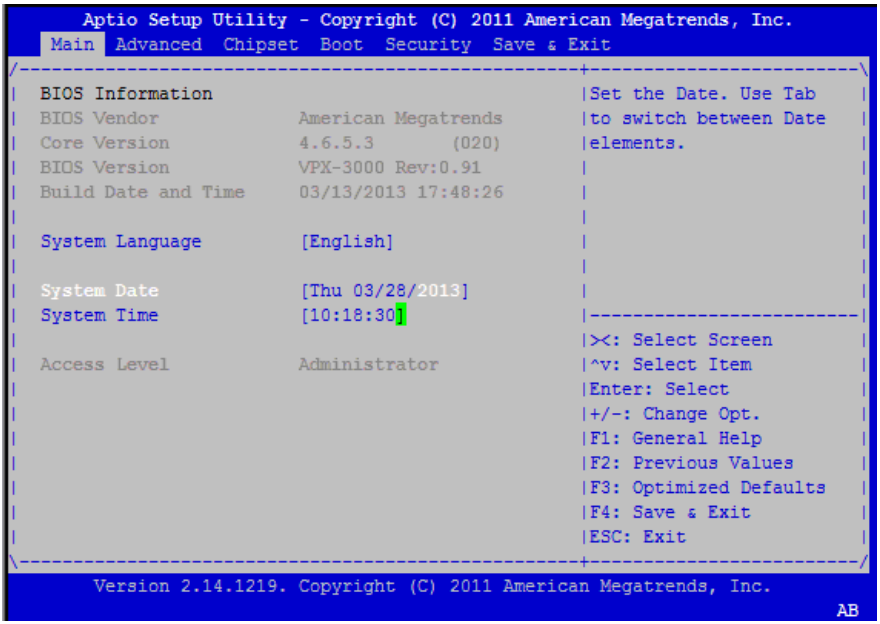
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Setup Menu

The main BIOS setup menu is the first screen that you can navigate. Each main BIOS setup menu option is described in this user's guide.

The Main BIOS setup menu screen has two main frames. The left frame displays all the options that can be configured. "Grayed" options cannot be configured, "Blue" options can be.

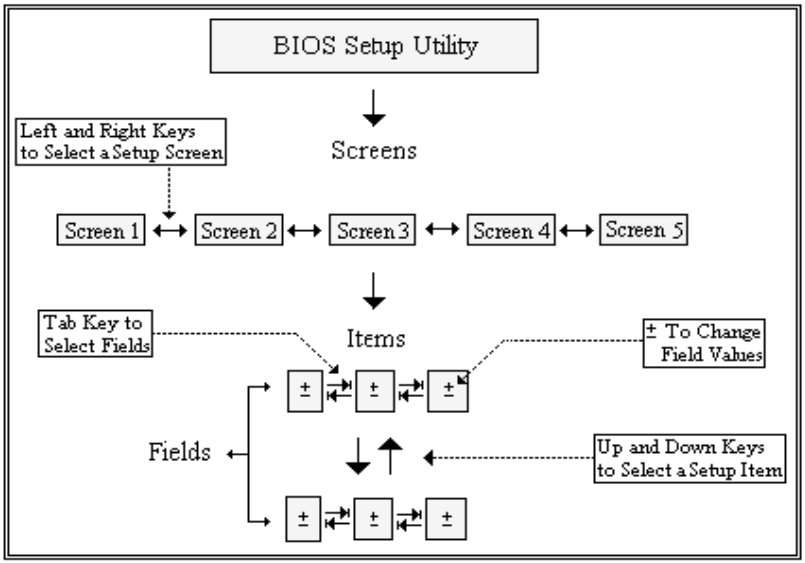
The right frame displays the key legend. Above the key legend is an area reserved for a text message. When an option is selected in the left frame, it is highlighted in white. Often a text message will accompany it.



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Navigation

The BIOS setup/utility uses a key-based navigation system called hot keys. Most of the BIOS setup utility hot keys can be used at any time during the setup navigation process. These keys include < F1 >, < F4 >, < Enter >, < ESC >, < Arrow > keys, and so on.



NOTE: There is a hot key legend located in the right frame on most setup screens.

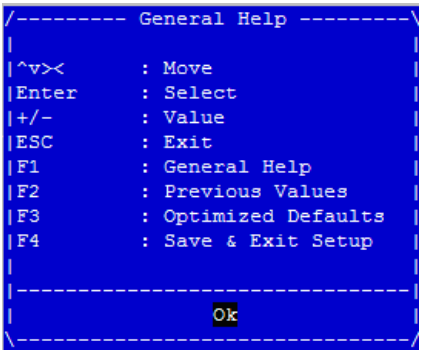
→←	Left/Right. The Left and Right < Arrow > keys allow you to select a setup screen. For example: Main screen, Advanced screen, Chipset screen, and so on.
↑↓	Up/Down The Up and Down < Arrow > keys allow you to select a setup item or sub-screen.
+-	Plus/Minus The Plus and Minus < Arrow > keys allow you to change the field value of a particular setup item. For example: Date and Time.
Tab	The < Tab > key allows you to select setup fields.

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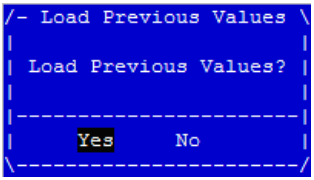
Hotkey Descriptions

Enter The < Enter > key allows you to display or change the setup option listed for a particular setup item. The < Enter > key can also allow you to display the setup sub-screens.

F1 The < F1 > key allows you to display the General Help screen. Press the < F1 > key to open the General Help screen.

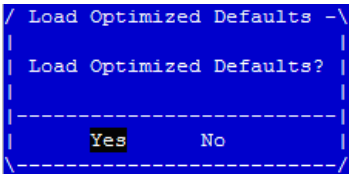


F2 The < F2 > key on your keyboard is the previous values key. It is not displayed on the key legend by default. To set the previous values settings of the BIOS, press the < F2 > key on your keyboard. It is located on the upper row of a standard 101 keyboard. The previous values settings allow the motherboard to boot up with the least amount of options set. This can lessen the probability of conflicting settings.

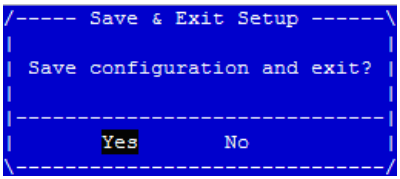


F3 The < F3 > key on your keyboard is the optimized defaults key. To set the optimized defaults settings of the BIOS, press the < F3 > key on your keyboard. It is located on the upper row of a standard 101 keyboard. The optimized defaults settings allow the motherboard to boot up with the optimized defaults of options set. This can lessen the probability of conflicting settings.

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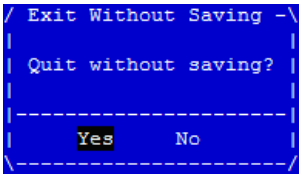


F4 The < F4 > key allows you to save any changes you have made and exit Setup. Press the < F10 > key to save your changes. The following screen will appear:



Press the < Enter > key to save the configuration and exit. You can also use the < Arrow > key to select Cancel and then press the < Enter > key to abort this function and return to the previous screen.

ESC The < Esc > key allows you to discard any changes you have made and exit the Setup. Press the < Esc > key to exit the setup without saving your changes. The following screen will appear:

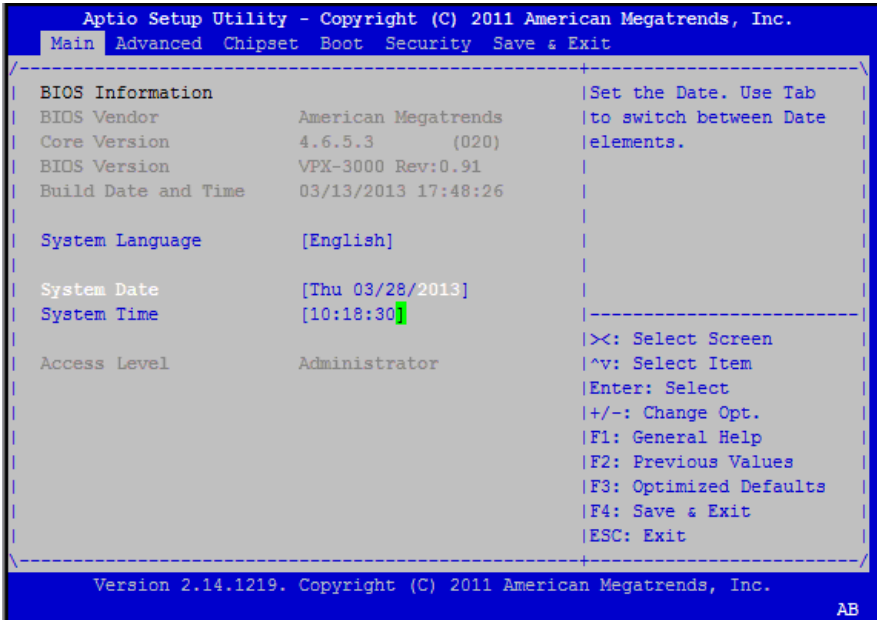


Press the < Enter > key to discard changes and exit. You can also use the < Arrow > key to select Cancel and then press the < Enter > key to abort this function and return to the previous screen.

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7.2 Main Setup

When you first enter the Setup Utility, you will enter the Main setup screen. You can always return to the Main setup screen by selecting the Main tab. The Main BIOS Setup screen is shown below.



System & Board Info

The Main BIOS setup screen reports BIOS and Board version information.

System Time/System Date

Use this option to change the system time and date. Highlight System Time or System Date using the < Arrow > keys. Enter new values using the keyboard. Press the < Tab > key or the < Arrow > keys to move between fields. The date must be entered in MM/DD/YY format. The time is entered in HH:MM:SS format.

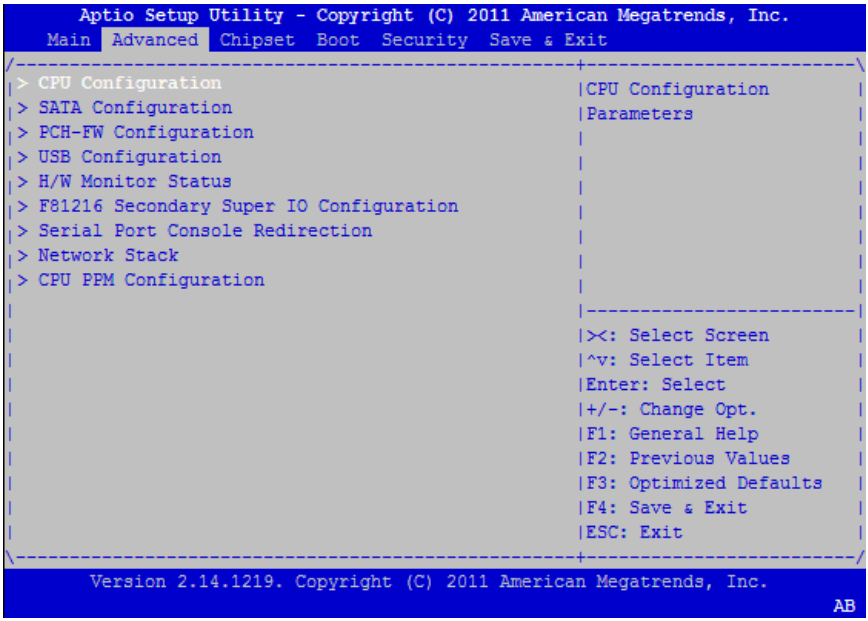
NOTE: The time is in 24-hour format. For example, 5:30 A.M. appears as 05:30:00, and 5:30 P.M. as 17:30:00.

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7.3 Advanced BIOS Setup

Select the Advanced tab from the setup screen to enter the Advanced BIOS Setup screen. You can select any of the items in the left frame of the screen, such as SuperIO Configuration, to go to the sub menu for that item. You can display an Advanced BIOS Setup option by highlighting it using the < Arrow > keys. The Advanced BIOS Setup screen is shown below.

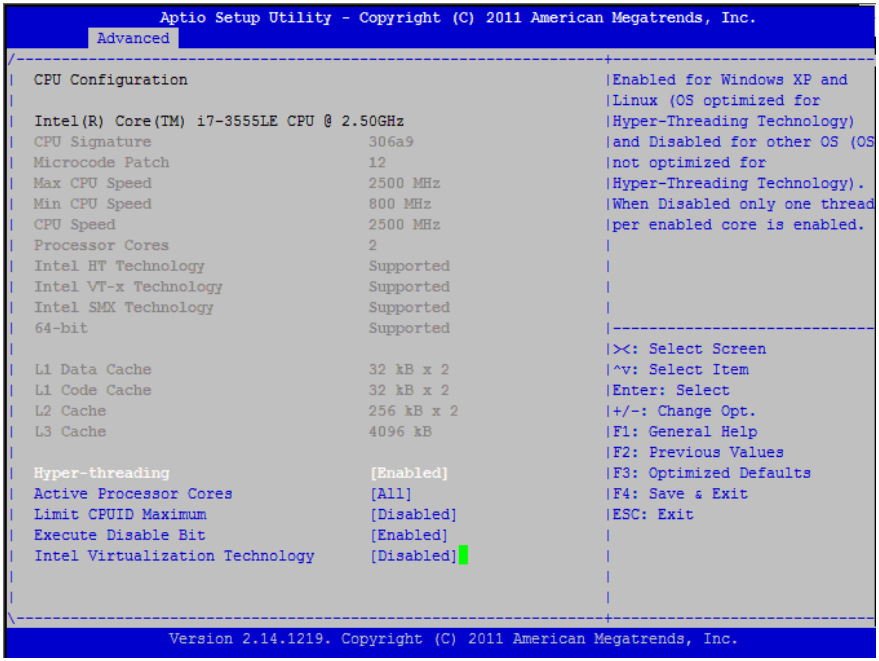
The sub menus are described on the following pages.



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7.3.1 CPU Configuration

You can use this screen to select options for the CPU Configuration Settings. Use the up and down < Arrow > keys to select an item. Use the < + > and < - > keys to change the value of the selected option. A description of the selected item appears on the right side of the screen. The settings are described on the following pages. An example of the CPU Configuration screen is shown below.



Hyper-Threading

- **Enabled:** for Windows and Linux (OS optimized for Hyper-Threading Technology).
- **Disabled:** for other OS (OS not optimized for Hyper-Threading Technology).

Active Processor Core

Number of cores to enable in each processor package. Set this value to All, 1, 2, 3.

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Limit CPUID Maximum

When the computer is booted up, the operating system executes the CPUID instruction to identify the processor and its capabilities. Before it can do so, it must first query the processor to find out the highest input value CPUID recognized. This determines the kind of basic information CPUID can provide the operating system. This option allows you to circumvent problems with older operating systems.

When Enabled, the processor will limit the maximum CPUID input value to 03h when queried, even if the processor supports a higher CPUID input value. When Disabled, the processor will return the actual maximum CPUID input value of the processor when queried.

Execute-Disable Bit

Execute-Disable Bit can prevent certain classes of malicious buffer overflow attacks when combined with a supporting OS.(Windows Server 2003 SP1, Windows XP SP2, SuSE Linux 9.2 Red Hat Enterprise 3 Update 3.)

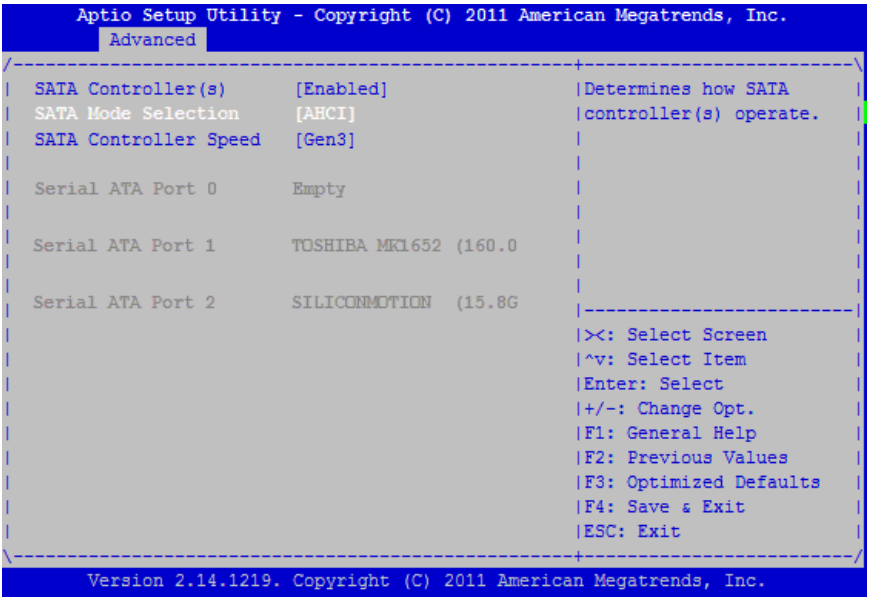
Intel Virtualization Tech

When enabled, a VMM can utilize the additional hardware capability provided by Vanderpool Technology. Set this value to Enabled/ Disabled.

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7.3.2 SATA Configuration

You can use this screen to select options for the SATA Configuration Settings. An example of the SATA Configuration screen is shown below.



SATA Controller(s)

Enable or disable SATA device.

SATA Mode Selection

The SATA can be configured as a legacy IDE, RAID and AHCI mode.

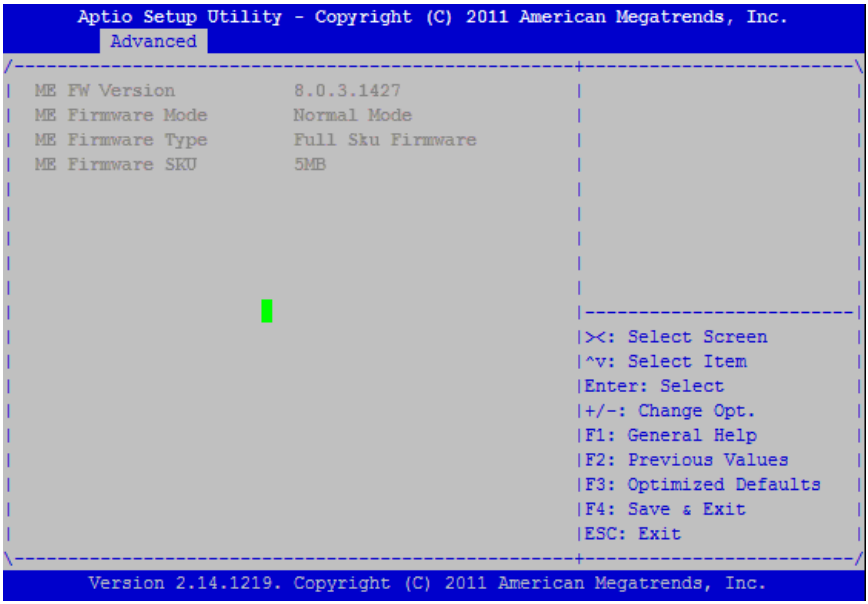
SATA Port 0-2

Display SATA device name string.

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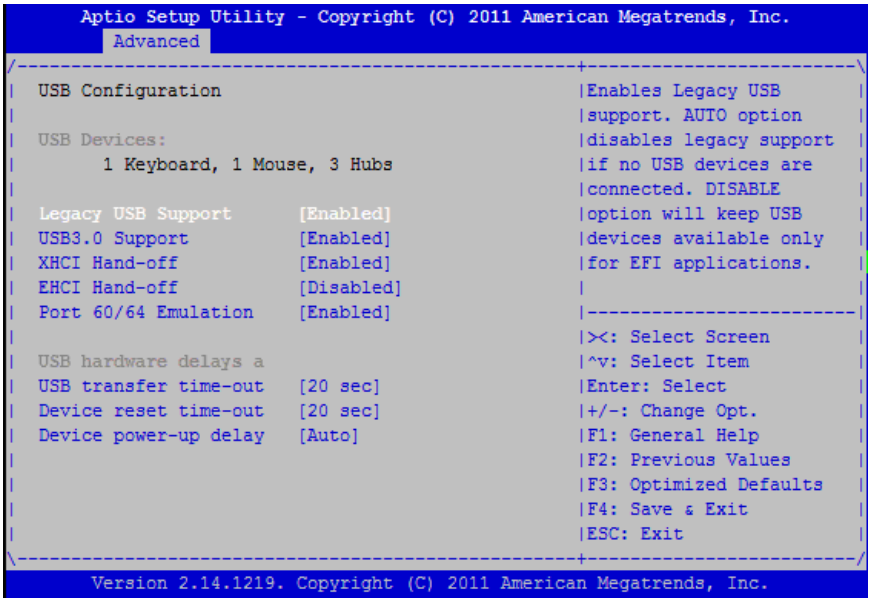
7.3.3 PCH-FW Configuration

You can use this screen to get the Intel ME FW version.



7.3.4 USB Configuration

You can use this screen to select options for the USB Configuration. Use the up and down < Arrow > keys to select an item. The screen is shown below.



Legacy USB Support

Enables legacy USB support. Auto option disables legacy support if no USB devices are connected. Disable option will keep USB devices available only for EFI applications. Set this value to Enabled/Disabled/Auto.

USB 3.0 Support

To enable or disable USB 3.0 (XHCI) controller support. Set this value to Enabled/Disabled.

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XHCI Hand-off

This is a workaround for OSes without XHCI hand-off support. The XHCI ownership change should be claimed by the XHCI driver. Set this value to Enabled/Disabled.

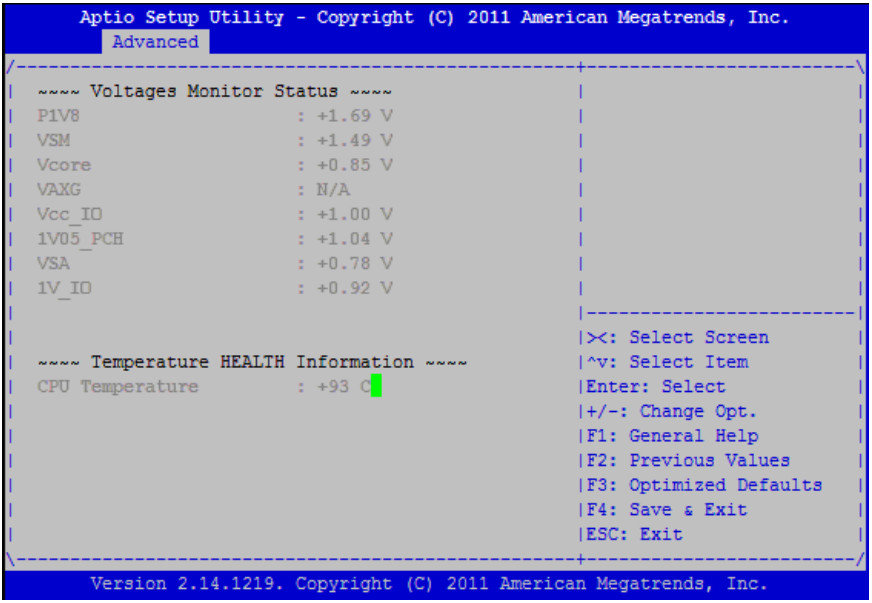
EHCI Hand-off

This is a workaround for OSes without EHCI hand-off support. The EHCI ownership change should be claimed by the EHCI driver. Set this value to Enabled/Disabled.

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7.3.5 Hardware Monitor

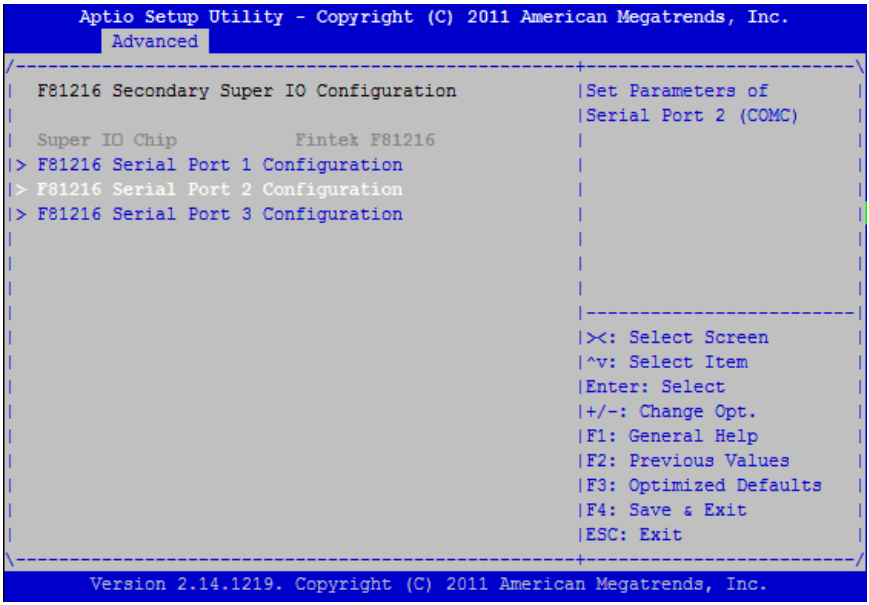
This option displays the current status of all of the monitored hardware devices/components such as voltages and temperatures.



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7.3.6 Super IO Configuration

You can use this screen to select options for the Super IO settings. Use the up and down < Arrow > keys to select an item. Use the < + > and < - > keys to change the value of the selected option. The settings are described on the following pages. The screen is shown below.



Serial Port 1, 2, 3 Configuration

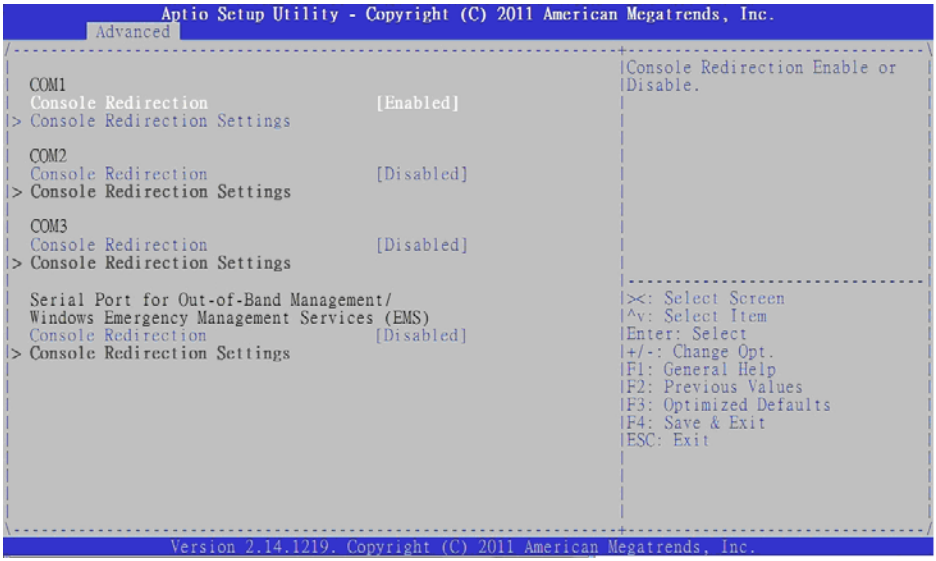
Sub-menus allow you to Enable/Disable and set the parameters of Serial Ports 1, 2, 3 (F81216: COM B,C,D). The screen is shown below.



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7.3.7 Serial Port Console Redirection

You can use this screen to select options for the serial port console redirection settings. Use the up and down < Arrow > keys to select an item. Use the < + > and < - > keys to change the value of the selected option. A description of the selected item appears on the right side of the screen. The settings are described on the following pages. An example of the Serial Port Console Redirection screen is shown below.



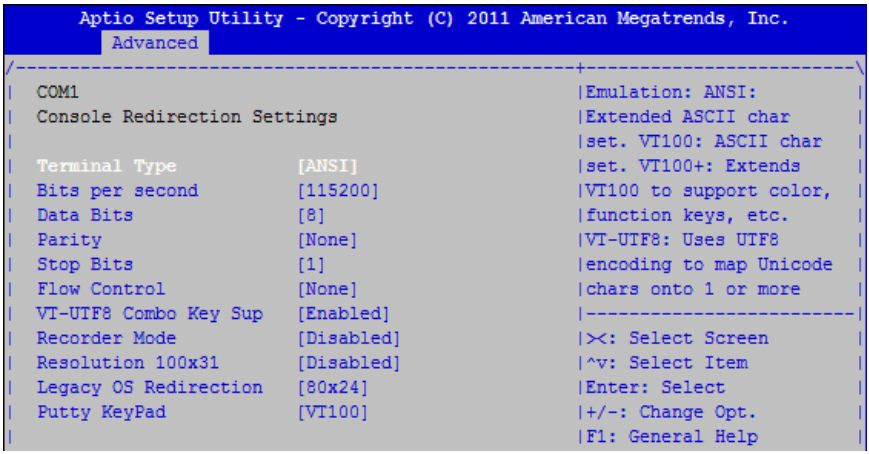
Console Redirection

Set this value to Enabled/Disabled.

COM1/2/3 Console Redirection Settings

The settings specify how the host computer and the remote computer will exchange data. Both computers should have the same or compatible settings. The screen is shown below.

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Terminal Type

VT100+ is the preferred terminal type for out-of-band management. Configuration options: VT100, VT100+, VT-UTF8, ANSI.

Bits per second

Select the bits per second you want the serial port to use for console redirection. The options are 115200, 57600, 38400, 19200, 9600.

Data Bits

Select the data bits you want the serial port to use for console redirection. Set this value to 7, 8.

Parity

Set this option to select Parity for console redirection. The settings for this value are None, Even, Odd, Mark, Space.

Stop Bits

Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit. Set this value to 1 and 2.

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Flow Control

Set this option to select Flow Control for console redirection. The settings for this value are None, Hardware RTS/CTS.

VT-UTF8 Combo Key Support

Enables VT-UTF8 combination key support for ANSI/VT100 terminals.Set this value to Enabled/Disabled.

Recorder Mode

When this mode is enabled, only text will be sent. This is to capture terminal data. Set this value to Enabled/Disabled.

Resolution 100x31

Set this option to extended terminal resolution. Set this value to Enabled/Disabled.

Legacy OS Redirection

On legacy OS, the number of rows and columns supported for redirection. Set this value to 80x24, 80x25.

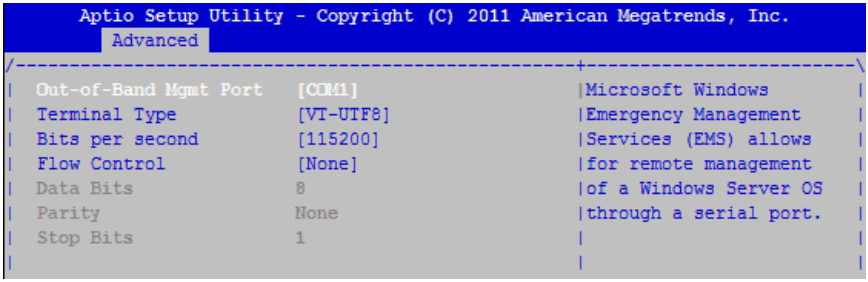
Putty Key Pad

Select FunctionKey and KeyPad on Putty. Set this value to VT100, LINUX, XTERMR6, SCO, ESCN, VT400.

Serial Port for Out-of-Band Management/Windows Emergency Management Services (EMS)

The following functions control the presence and content of the ACPI serial port redirection table (SPCR). This table is mainly used by the Windows server variants to provide Windows Emergency Management Services (EMS). This functionality is totally independent from serial redirection of other console output. OoB Management or EMS makes it possible to control selected components of (Windows) servers, even when a server is not connected to the network or the network is not available. In short: EMS allows for remote management of a Windows Server OS through a serial port

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Out-of-Band Mgmt Port

Microsoft Windows emergency management services (EMS) allows for remote management of a Windows Server OS through a serial port. Set this value to COM0, COM1, COM2 (Disabled), COM3 (Disabled)

Terminal Type

VT-UTF8 is the preferred terminal type for out-of-band management. The next best choice is VT100+ and then VT100. See above, in Console Redirection Settings page, for more Help with Terminal Type/Emulation. Configuration options: VT100, VT100+, VT-UTF8, ASNI.

Bits per second

Select the bits per second you want the serial port to use for console redirection. The options are 115200 ,57600 ,38400, 19200, 9600.

Flow Control

Set this option to select Flow Control for console redirection. The settings for this value are None and Hardware RTS/CTS.

Data Bits

This is a display-only function providing information about the frame width for the Out-of-Band Management.

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Parity

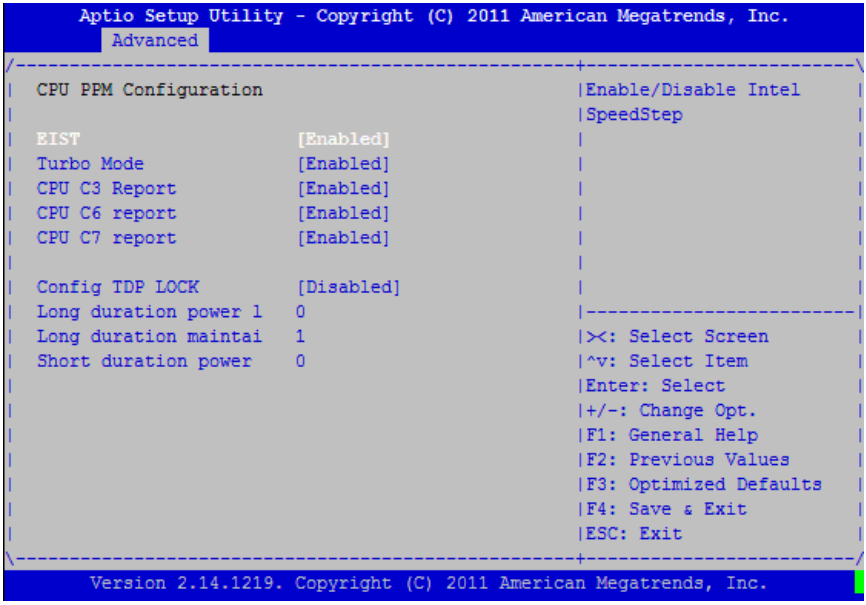
This is a display-only function providing information about the parity for Out-of-Band Management.

Stop Bits

This is a display-only function providing information about the number of stop bits for Out-of-Band Management.

7.3.8 CPU PPM Configuration

Processor Power Module (PPM) configuration parameters.



EIST

Enable or disable Intel SpeedStep. Set this value to Enabled/Disabled.

Turbo Mode

Set this option to enable or disable turbo mode. Set this value to Enabled/Disabled.

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CPU C3 Report

Enable or disable CPU C3 (ACPI C2) report to OS. Set this value to Enabled/Disabled.

CPU C6 Report

Enable or disable CPU C6 (ACPI C3) report to OS. Set this value to Enabled/Disabled.

CPU C7 Report

Enable or disable CPU C7 (ACPI C3) report to OS. Set this value to Enabled/Disabled.

Config TDP Lock

Lock the Config TDP Control register. Set this value to Enabled/ Disabled.

Long duration power limit

Long duration power limit in watts, 0 means use factory default.

Long duration maintained

Time window which the long duration power is maintained.

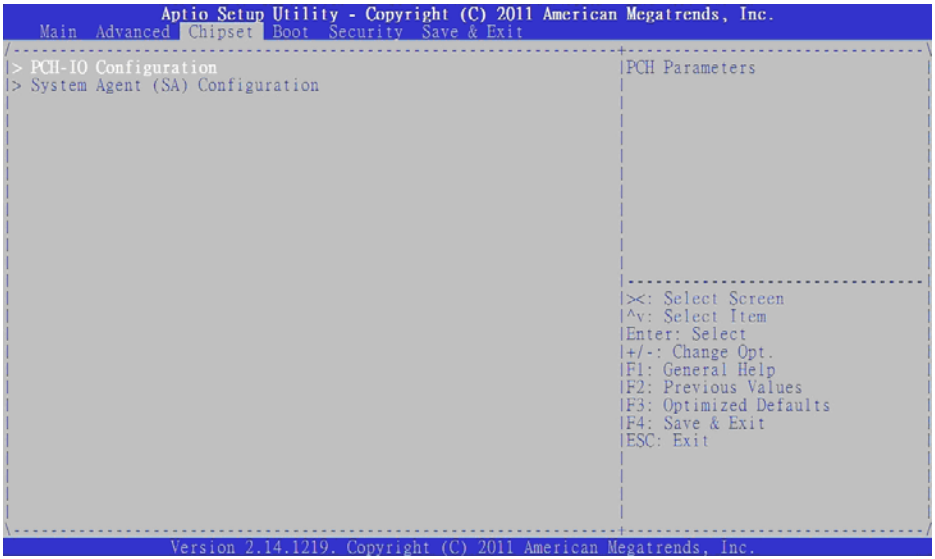
Short duration power limit

Short duration power limit in watts, 0 means use factory default.

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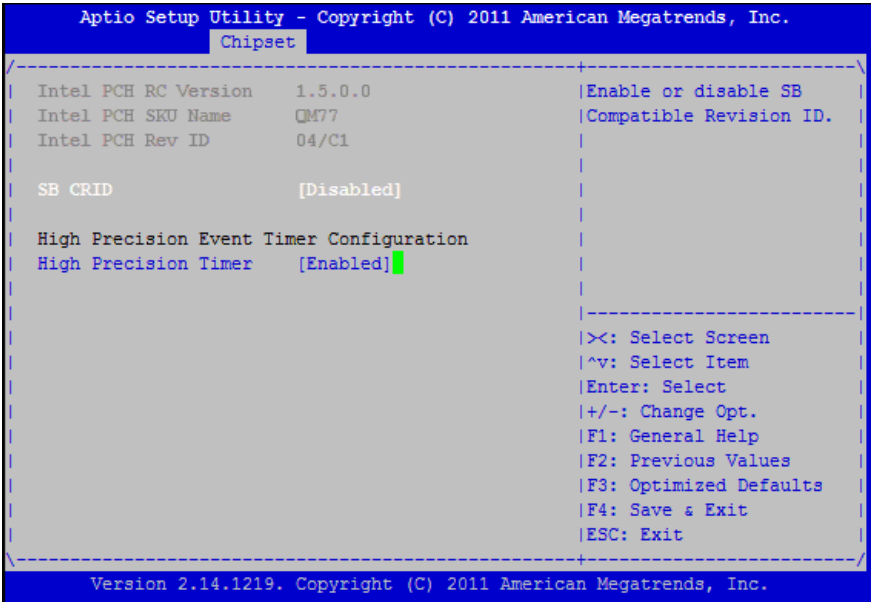
7.4 Chipset Setup

Select the Chipset tab from the setup screen to enter the Chipset BIOS Setup screen. You can select any of Chipset BIOS Setup options by highlighting it using the < Arrow > keys. The Chipset BIOS Setup screen is shown below.



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7.4.1 PCH-IO Configuration



SB CRID

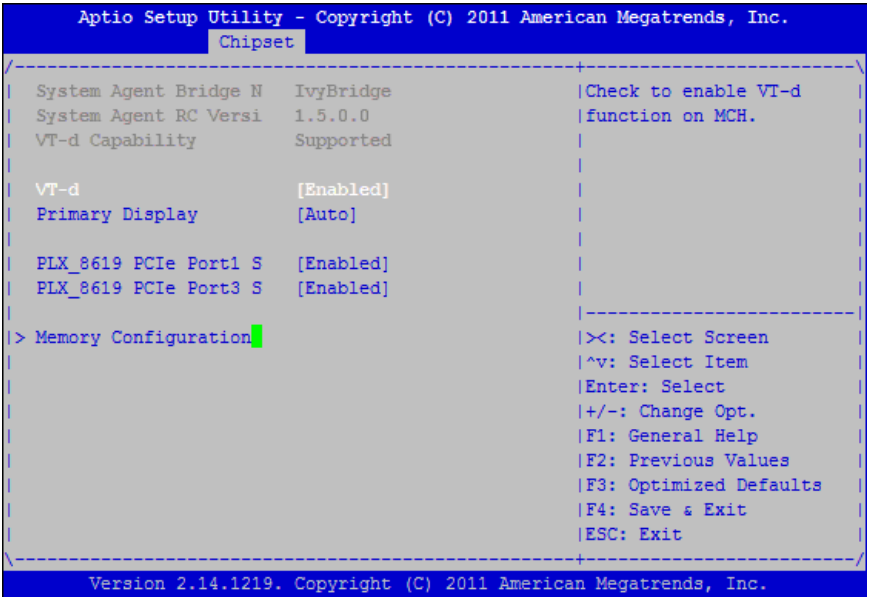
Enable or disable SB Compatible Revision ID.

High Precision Timer

Enable or disable the High Precision Event Timer.

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7.4.2 System Agent (SA) Configuration



VT-d

The Intel Virtualization Technology for Directed I/O. Set this value to Enabled/Disabled.

Primary Display

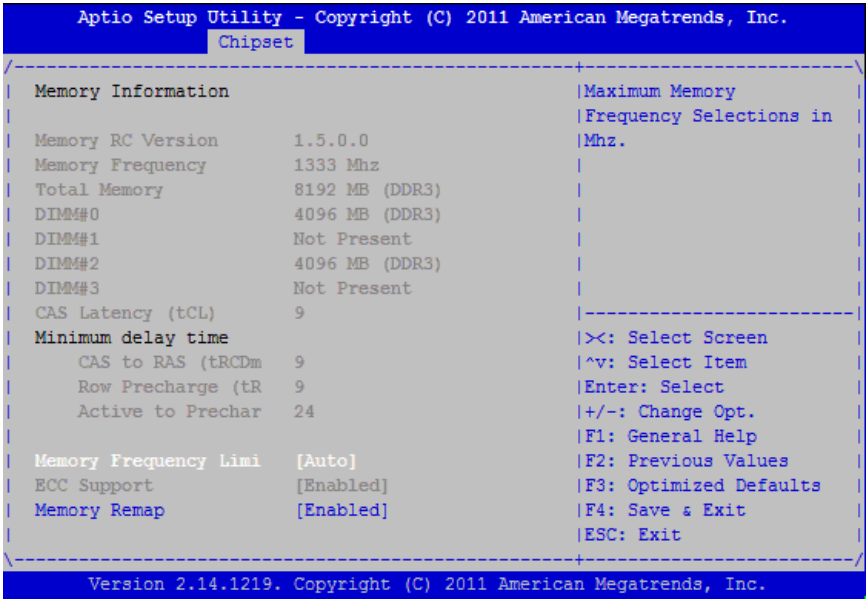
Select which graphics device should be the primary display. Set this value to Auto, IGFX, PCI.

PLX_8619 PCIe Port1/3 Support

Select which of IGFX/ PCI graphics device should be primary display. Set this value to Auto/ IGFX/ PCI.

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Memory Configuration

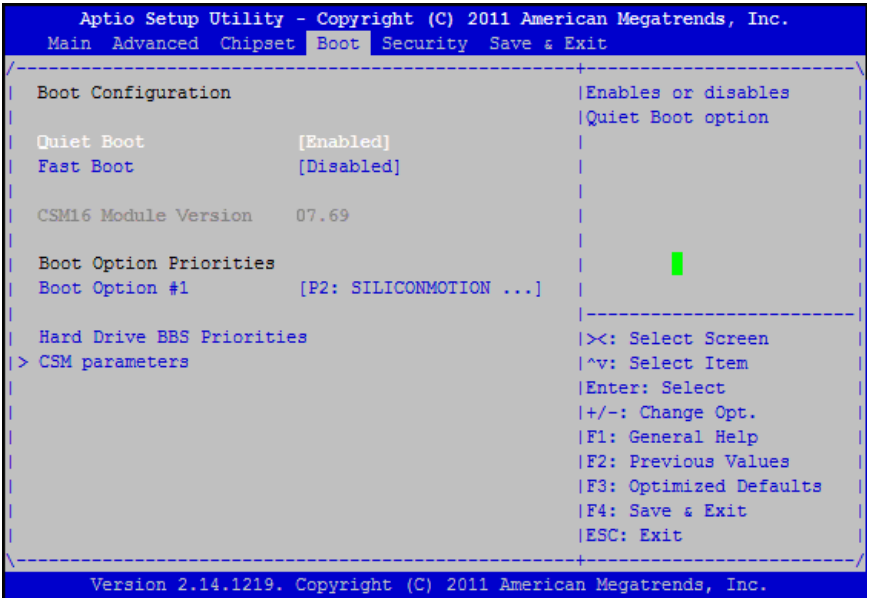


Memory Remap

Enable or disable memory remap above 4G. Set this value to Enabled/Disabled.

7.5 Boot Settings

Select the Boot tab from the setup screen to enter the Boot BIOS Setup screen. You can select any of the items in the left frame of the screen, such as Boot Device Priority, to go to the sub menu for that item. You can display a Boot BIOS Setup option by highlighting it using the < Arrow > keys. The Boot Settings screen is shown below:



Quiet Boot

- **Disabled** - Set this value to allow the computer system to display the POST messages.
- **Enabled** - Set this value to allow the computer system to display the OEM logo.

Fast Boot

Enables or disables boot with initialization of a minimal set of devices required to launch active boot option. Has no effect for BBS boot options. Set this value to Enabled/Disabled.

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Boot Option Priorities

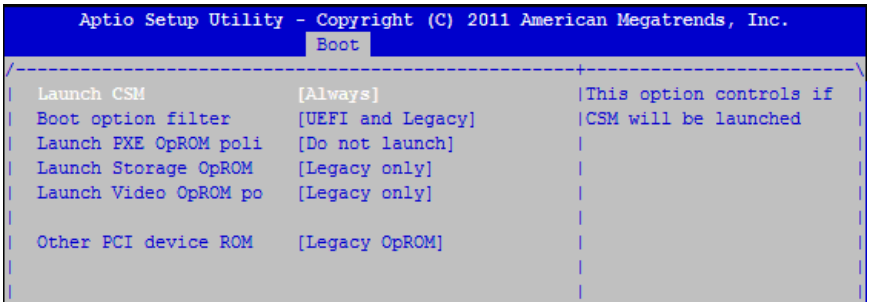
Set Boot Option #1 ~2 boot priority.

Hard Drive BBS Priorities

Specifies the boot device priority sequence from available hard drives.

7.5.1 CSM Parameter

OpROM execution, boot option filter, etc.



Launch CSM

This option controls if CSM will be launched. Set this value to Always, Never.

Boot option filter

This option controls what devices system can boot to. Set this value to UEFI and Legacy, Legacy only, UEFI only.

Launch PXE OpROM policy

This option controls the execution of UEFI and Legacy PXE OpROM. Set this value to Do not launch, Legacy only.

Launch Storage OpROM policy

This option controls the execution of UEFI and Legacy Storage OpROM. Set this value to Do not launch, UEFI only, Legacy only.

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Launch Video OpROM policy

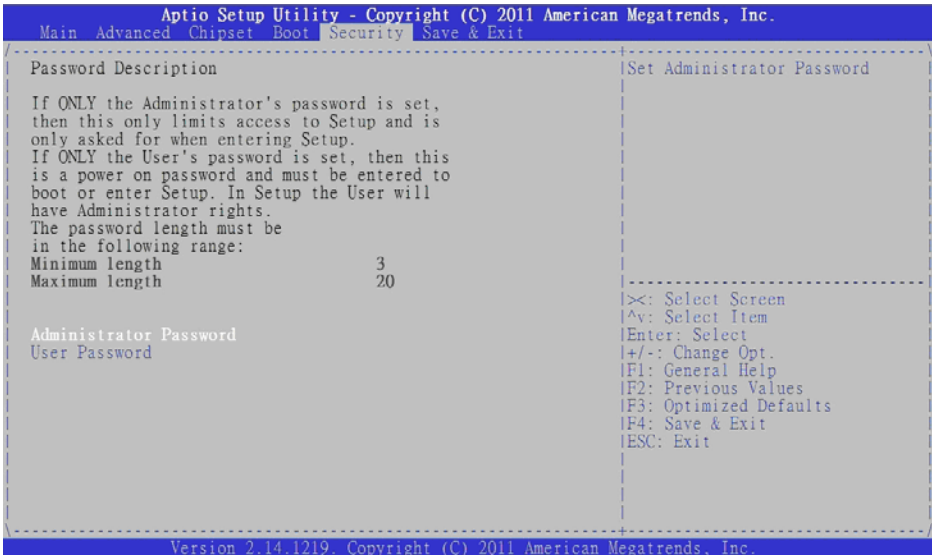
This option controls the execution of UEFI and Legacy Video OpROM. Set this value to Do not launch, UEFI only, Legacy only.

Other PCI device ROM priority

For PCI devices other than Network, Mass storage or Video defines which OpROM to launch. Set this value to UEFI OpROM, Legacy OpROM.

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7.6 Security Setup



Administrator, User Password

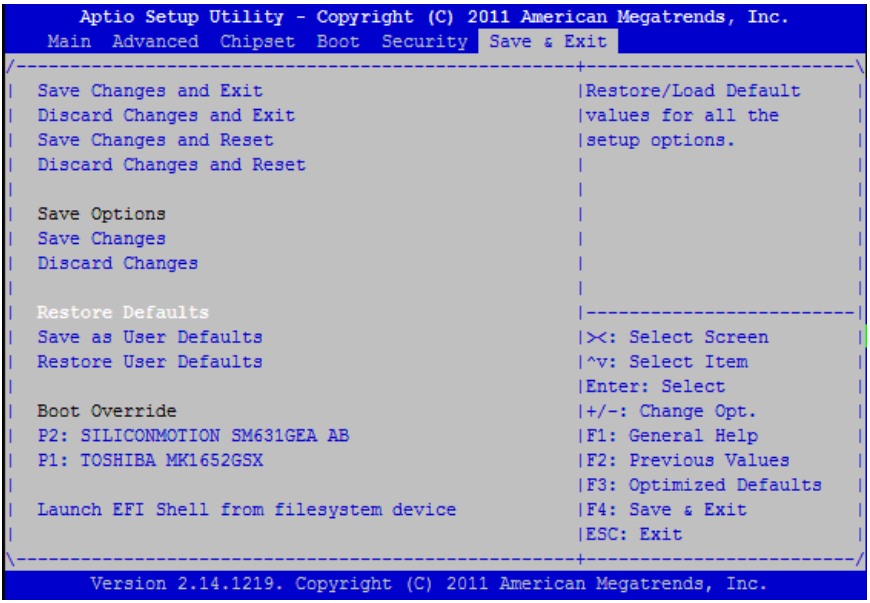
If only the administrator's password is set, then this only limits access to setup and is only asked for when entering setup.

If only the user's password is set, then this is a power on password and must be entered to boot or enter setup. In setup the user will have administrator rights.

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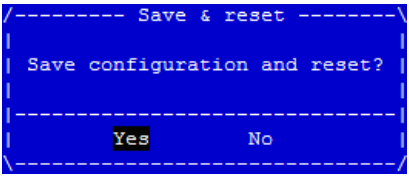
7.7 Save & Exit Menu

Select the Save & Exit tab from the setup screen to enter the Save & Exit BIOS Setup screen. You can display an Exit BIOS Setup option by highlighting it using the < Arrow > keys. The Save & Exit BIOS Setup screen is shown below.



Save Changes and Reset

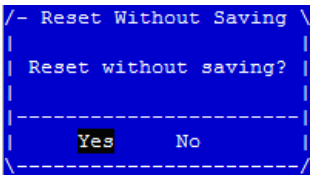
Reset the system after saving the changes.



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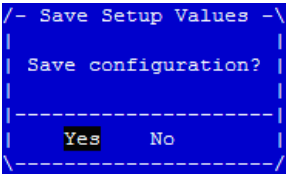
Discard Changes and Reset

Reset system setup without saving any changes.



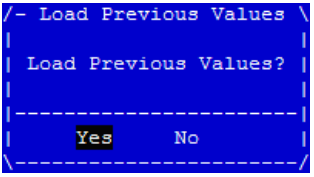
Save Changes

Save changes done so far to any of the setup options.



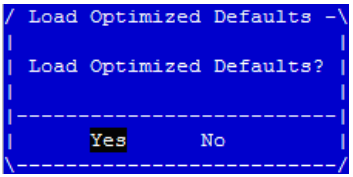
Discard Changes

Discard changes done so far to any of the setup options.



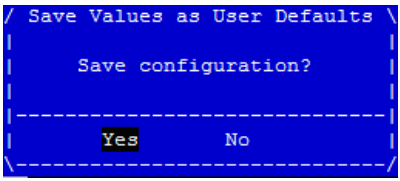
Restore Changes

Restore/Load Defaults values for all the setup options.



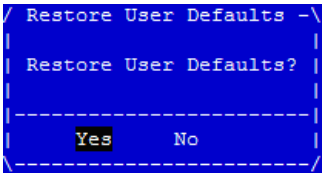
Save as User Defaults

Save the changes done so far as user defaults..



Restore User Defaults

Save changes done so far to any of the setup options.



Launch EFI Shell from Filesystem Device

Attempts to Launch EFI Shell application (Shellx64.efi) from one of the available filesystem devices.

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8 IPMI User Guide

8.1 Introduction

This chapter is written for those who already have a basic understanding of the newest implementation of the baseboard management controller (BMC) of the Intelligent Platform Management Interface (IPMI) specification rev. 2.0.

8.2 Summary of Commands Supported by VPX3000

The table below lists all the commands supported by the IPMC. The right most column indicates if a particular command is mandatory or optional for IPMI 2.0 and VITA 46.11.

Command Name	IPMI v2.0 Specification Section	NetFn	CMD
IPM Device "Global" Commands			
Reserved	NA	App (06h)	00h
Get Device ID	17.1	App (06h)	01h
Cold Reset	17.2	App (06h)	02h
Warm Reset	17.3	App (06h)	03h
Get Self Test Results	17.4	App (06h)	04h
BMC Watchdog Timer Commands			
Reset Watchdog Timer	21.5	App (06h)	22h
Set Watchdog Timer	21.6	App (06h)	24h
Get Watchdog Timer	21.7	App (06h)	25h
FRU Device Commands			
Get FRU Inventory Area Info	28.1	Storage (0Ah)	10h
Read FRU Data	28.2	Storage (0Ah)	11h
Write FRU Data	28.3	Storage (0Ah)	12h
SDR Device Commands			

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Command Name	IPMI v2.0 Specification Section	NetFn	CMD
Get SDR Repository Info	27.9	Storage (0Ah)	20h
Get SDR Repository Allocation Info	27.10	Storage (0Ah)	21h
Reserve SDR Repository	27.11	Storage (0Ah)	22h
Get SDR	27.12	Storage (0Ah)	23h
SEL Device Commands			
Get SEL Info	25.2	Storage (0Ah)	40h
Get SEL Allocation Info	25.3	Storage (0Ah)	41h
Reserve SEL	25.4	Storage (0Ah)	42h
Get SEL Entry	25.5	Storage (0Ah)	43h
Add SEL Entry	25.6	Storage (0Ah)	44h
Clear SEL	25.9	Storage (0Ah)	47h
Get SEL Time	25.10	Storage (0Ah)	48h
Set SEL Time	25.11	Storage (0Ah)	49h

Command Name	VITA 64.11 Specification Section	NetFn	CMD
VITA 46.11 Commands			
Get VSO Capabilities	VITA: section 10.1.3.1	VITA	00h
FRU Control	VITA: section 10.1.3.6	VITA	04h
Get FRU LED Properties	VITA: section 10.1.3.27	VITA	05h
Get LED Color Capabilities	VITA: section 10.1.3.28	VITA	06h
Set FRU LED State	VITA: section 10.1.3.30	VITA	07h

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Command Name	VITA 64.11 Specification Section	NetFn	CMD
Get FRU LED State	VITA: section 10.1.3.29	VITA	08h
Set IPMB State	VITA: section 10.1.3.7	VITA	09h
Set FRU State Policy Bits	VITA: section 10.1.3.9	VITA	0Ah
Get FRU State Policy Bits	VITA: section 10.1.3.8	VITA	0Bh
Set FRU Activation	VITA: section 10.1.3.10	VITA	0Ch
Get Device Locator Record ID	VITA: section 10.1.3.11	VITA	0Dh
Get Fan Speed Properties	VITA: section 10.1.3.12	VITA	14h
Set Fan Level	VITA: section 10.1.3.14	VITA	15h
Get Fan Level	VITA: section 10.1.3.13	VITA	16h
Get IPMB Link Info	VITA: section 10.1.3.17	VITA	18h

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8.3 Communications with IPMC

The VPX3000 communicates with the Chassis Manager through its primary IPMB (System IPMB) and responds to all mandatory commands for respective IPM Controllers (as defined in the VITA 46.11 Specification), as well as some optional ones.

8.4 IPMI Sensors List

Sensors include the mandatory sensors defined by VITA 46.11.

Sensor Number	Sensor name	Normal Reading	Sensor Reading Value of IPMI Get Sensor Reading Command
0	Hot Swap	N/A	FRU State Sensor
1	IPMB Physical	N/A	System IPMB link sensor
2	FRU#0 Health	N/A	FRU Health Sensor
3	FRU#0 Voltage	N/A	FRU Voltage Sensor
4	FRU#0 Temp	N/A	FRU Temperature Sensor
5	FRU#0 P.Test	N/A	Payload Test Result Sensor
6	FRU#0 P.TestState	N/A	Payload Test Status Sensor
7	Version change	N/A	TBD
8	3V3 AUX	3.3 V	Threshold Value
9	3V3 STB	3.3 V	Threshold Value
10	5V STB	5 V	Threshold Value
11	2.6V DRAM	2.6 V	Threshold Value
12	12V	12 V	Threshold Value
13	3.3V	3.3 V	Threshold Value
14	CPU Core Power	TBD	Threshold Value
15	1.2V DRAM	1.2V	Threshold Value
16	1.05V SOC	1.05V	Threshold Value
17	1.2V SSD	1.2V	Threshold Value

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Sensor Number	Sensor name	Normal Reading	Sensor Reading Value of IPMI Get Sensor Reading Command
18	1.8V NIC	1.8V	Threshold Value
19	1.0V NIC	1.0V	Threshold Value
20	1.5V AUX	1.5V	Threshold Value
21	0.6V DRAMD	0.6V	Threshold Value
22	Temp 1	TBD	Threshold Value
23	BMC Watchdog	N/A	Entity ID: 03h Sensor Type: 23h Event / Reading: 6Fh (sensor-specific) Timeout action: 000b = no action 001b = Hard Reset 000b = Power Down 011b = Power Cycle 100b - 111b = reserved
24	CPU Core Temp	TBD	TBD
25	Chassis Hot Swap	N/A	TBD
26	RTM Hot Swap	N/A	TBD
27	RTM Health	N/A	TBD
28	RTM Voltage	N/A	TBD
29	RTM Temp	TBD	TBD
30	1.5V PCH	1.5V	Threshold Value
0	Hot Swap	N/A	FRU State Sensor

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8.5 System Event Log (SEL)

The BMC provides a centralized, non-volatile System Event Log, or SEL. Having the SEL and logging functions managed by the BMC helps ensure that 'post-mortem' logging information is available should a failure occur that disables the system processor(s). The communication interface is used by I2C.

Example: IPMC Warm Reset

Description	NetFn	Cmd	Result
IPM Device “Global” Commands: Warm Reset	0x06	0x03	IPMITOOL: | 06/19/2018 | 01:07:39 | System Boot Initiated | Initiated by warm reset
IPMI SPEC v2.0, Table 42-1, Event/ Reading Type Code Ranges	Sensor Type Code	Sensor Class	Description
Sensor-specific	0x1D	Discrete	Sensor-specific Discrete. Indicates that the discrete state information is specific to the sensor type. State bit positions / event offsets for a particular sensor type are specified in the ‘sensor-specific offset’ column in Table 42-3, Sensor Type Codes
IPMI SPEC v2.0 , Table 32-1, SEL Event Records			
Record Type: 0x6F	GenID: 0x00 0x20	Sensor Number #: That is dependent in the sensor type.	
EvM Rev: 0x04	EvtDir Type: 0x6F	Event Data 1:0x02, Event Data 2:0xFF, Event Data 3:0xFF	

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8.6 FRU Information

Board Info

Mfg Date/Time = xx/xx/xxxx

Manufacturer = ADLINK

Product Name = VPX-3XXX

Serial Number = PPSxxxxxxx

Part Number = xxxxxxxxxxxx

Product Info

Manufacturer = ADLINK

Product Name = VPX-3XXX

Part/Model Number = xxxxxxxxxxxxx

Product Version = Rev A1

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8.7 Relevant Documents

- ANSI/VITA 46.0-2007 American National Standard for VPX Baseline Standard. (2007).
- ANSI/VITA 46.11-2007 System Management on VPX Draft Revision 0.11. (Nov. 18, 2013).
- ANSI/VITA 46.11-2015 System Management on VPX Draft Revision r022. (Nov. 18, 2013).
- Cortex-M3 Devices Generic User Guide. Issue A. (Dec. 16, 2010).
- IPMI- Intelligent Platform Management Interface Specification Second Generation v2.0 Rev. 1.0. (2004, 2 12).
- IPMI Platform Management FRU Information Storage Definition V1.0 Rev1.1. (1999, 9 27).
- Microsemi PPS BMR-A2F-ATCA Software Architecture Technical Specification, Revision 1.2.0. (April 17, 2013).
- Microsemi SmartFusion Microcontroller subsystem User Guide, Revision 4. (July ,2014).
- Microsemi SmartFusion Programmable Analog User Guide, Revision Draft B. (Dec.,2009).
- Pigeon Point BMR-A2F-VPX Software Architecture Technical Specification. Revision1.2.1. (Jan. 17, 2014).
- PICMG 3.0, Revision 3.0, AdvancedTCA Base Specification. (March 24, 2008).
- The Definitive Guide to the ARM Cortex-M3 Second Edition. (2010).

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Important Safety Instructions

For user safety, please read and follow all **instructions**, **WARNINGS**, **CAUTIONS,** and **NOTES** marked in this manual and on the associated equipment before handling/operating the equipment.

- Read these safety instructions carefully.
- Keep this user's manual for future reference.
- Read the specifications section of this manual for detailed information on the operating environment of this equipment.
- When installing/mounting or uninstalling/removing equipment:
  - Turn off power and unplug any power cords/cables.
- To avoid electrical shock and/or damage to equipment:
  - Keep equipment away from water or liquid sources;
  - Keep equipment away from high heat or high humidity;
  - Keep equipment properly ventilated (do not block or cover ventilation openings);
  - Make sure to use recommended voltage and power source settings;
  - Always install and operate equipment near an easily accessible electrical socket-outlet;
  - Secure the power cord (do not place any object on/over the power cord);
  - Only install/attach and operate equipment on stable surfaces and/or recommended mountings; and,
  - If the equipment will not be used for long periods of time, turn off and unplug the equipment from its power source.

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- Never attempt to fix the equipment. Equipment should only be serviced by qualified personnel.

A Lithium-type battery may be provided for uninterrupted, backup or emergency power.



Risk of explosion if battery is replaced with one of an incorrect type. Dispose of used batteries appropriately.

- Equipment must be serviced by authorized technicians when:
  - The power cord or plug is damaged;
  - Liquid has penetrated the equipment;
  - It has been exposed to high humidity/moisture;
  - It is not functioning or does not function according to the user's manual;
  - It has been dropped and/or damaged; and/or,
  - It has an obvious sign of breakage.

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Getting Service

Ask an Expert: http://askanexpert.adlinktech.com

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