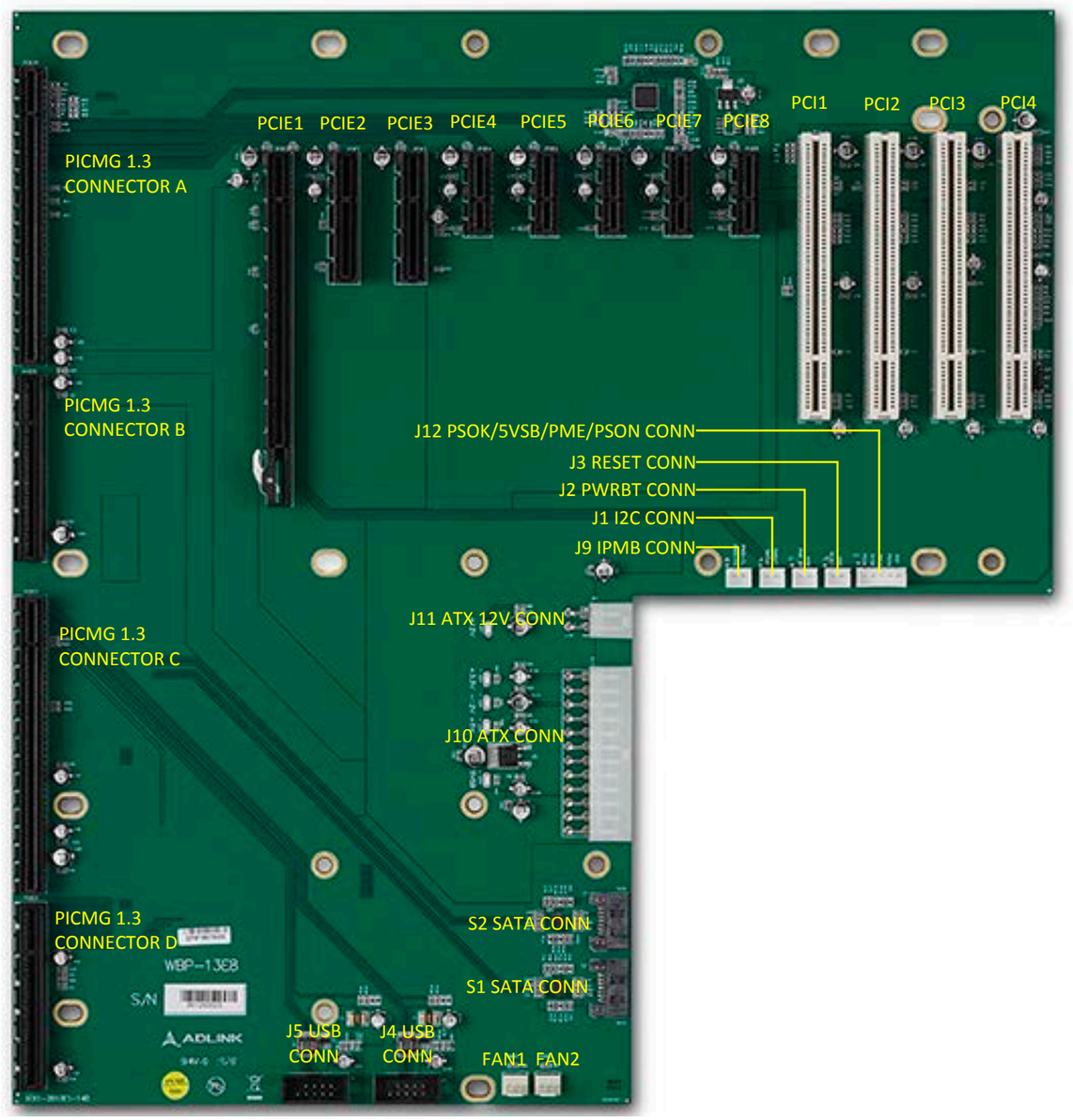
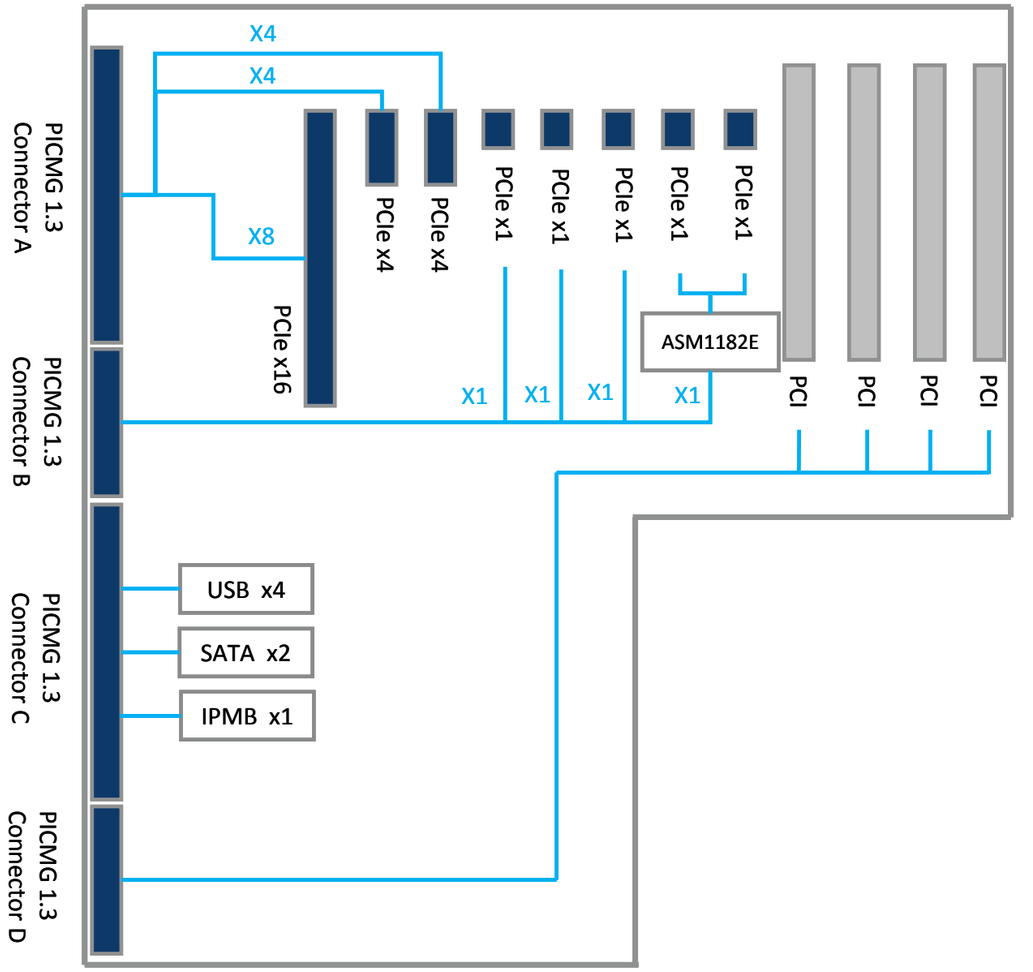
WBP-13E8 PICMG 1.3 Backplane

Backplane Layout



Block Diagram



Specifications

Form Factor	PICMG 1.3 Backplane for full-size SHB
Dimensions	327.6mm x 317.5mm
SHB Slots	1
PCI Express Slots	1 PCIe x16 (x8 signal), 2 PCIe x4, 5 PCIe x1 (PCIE7/8 support PCIe Gen2 only)
PCI slots	4
Peripheral Connectors	2 USB connectors (4 ports), 2 SATA connectors
Misc. Connectors	2 Fan connectors, Power connector, Reset connector
Power Requirement	ATX (24-pin ATX connector, 4-pin ATX 12V connector)

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Connectors

J4/J5 – USB Connector

Pin	Signal
1	U5V1
2	U5V1
3	USB1-
4	USB0-
5	USB1+
6	USB0+
7	GND
8	GND
9	N/A
10	NC

FAN1/FAN2 – Fan Connector

Pin	Signal
1	GND
2	+12V
3	NC

S1/S2 – SATA Connector

Pin	Signal
1	GND
2	DT+
3	DT-
4	GND
5	DR-
6	DR+
7	GND

J10 – ATX Connector

Pin	Signal	Pin	Signal
1	+3.3V	13	+3.3V
2	+3.3V	14	-12V
3	GND	15	GND
4	+5V	16	PS-ON
5	GND	17	GND
6	+5V	18	GND
7	GND	19	GND
8	PW-OK	20	-5V
9	+5VSB	21	+5V
10	+12V	22	+5V
11	+12V	23	+5V
12	+3.3V	24	GND

Connectors

J11 – ATX 12V Connector

Pin	Signal
1	GND
2	GND
3	+12V
4	+12V

J9 – IPMB Connector

Pin	Signal
1	IPMB-CL
2	IPMB-DA

J1 – I2C Connector

Pin	Signal
1	SMCLK
2	SMDAT

J2 – PWRBT Connector

Pin	Signal
1	PWRBT#
2	GND

J3 – Reset Connector

Pin	Signal
1	SHB-RST#
2	GND

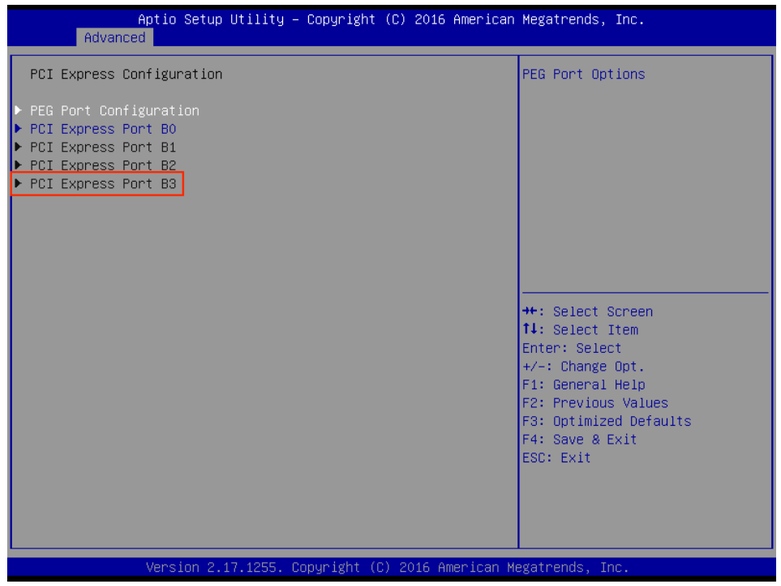
J12 – PSOK/5VSB/PME/PSON Connector

Pin	Signal
1	PS-OK
2	+5VSB
3	PME#
4	PS-ON
5	GND

BIOS Setup for WBP-13E8

NuPRO-E43 BIOS 4.03.10 or later supports the WBP-13E8 backplane. Max. Payload Size must be set to 128 byte for compatibility with the WBP-13E8.

In the BIOS Setup Menu, go to *Advanced → PCI Express Port B3 → Max Payload Size 128 byte.*



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